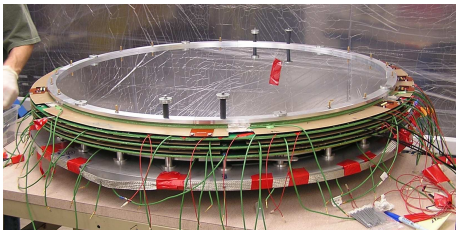


# Goal of Tests



- Determine HV plateau
- Determine best HV operation settings
- Determine efficiency (wires and strips)
- Determine parameters as function of length and position
- Determine cathode strip resolution
- ?

# Tests Setup

Use U-jig with Sr90 source and trigger scintillator.

- 64 ADC channels(strips), 16 TDC channels (wires)
- 4 pream cards for strips
- 1 pream caard for wires
- 5 24 channel cables
- Switch board 24-16 (x5)
- 5 post-amplifier (level translator)
- NO shaper boards used!
- Trigger DAQ on  $e^-$  from Sr90 source