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From jsch@phys.ualberta.ca Fri Mar 23 13:52:47 2007
Date: Fri, 23 Mar 2007 11:41:29 −0600
From: John Schaapman <jsch@phys.ualberta.ca>
To: elke@jlab.org
Cc: lars@phys.ualberta.ca
Subject: Fw: Electronics Workshop Mar 28 Agenda only

Hi Elke,
  Here is just the agenda which is the topics list with some detail removed.

John

−−−−−−−−−−−−− Forwarded message −−−−−−−−−−−−−
Electronics Meeting − March 28, 2007

  AGENDA v2 Mar 22

Summary Chart and initial discussion              Fernando/Lars

1. Drift Chamber Preamp ASIC                       John
     − UPENN ASIC development summary        ppt from Mitch
     − ASIC testing/allocation plans
      − Discussion − including ASIC next version

2.  FDC daughterboard design                       Fernando

3. Drift Chamber 100 MSps fADC                     Gerard

4. Constant Fraction Discriminators                Lars
     − commercial units − where would they be good enough ?
     − custom fast CFD − where are they needed?

5. Jlab fast electronics                           Chris
     − F1TDCs mods for Hall D requirements
     − update on fADC250s
      − Energy summing/track counts
     − Summing module (VXS)
     − Global clock/timing and trigger systems

6. Trigger system architecture                     Dave Doughty

7. Clock/Trigger Distribution                      Ed J.
     − local (crate level) distribution and
        trigger info to readout controller

8. Calibration System discussion

9. Scalers discussion
      − could it be done inside the fADC250s?

10. SiPM                                           George Lolos ??
        BCAL − cooling, power, risetimes

11. Cabling issues discussion

12. Signal splitters                               John

Close Out                                          Lars
  − decide priorities
  − alocate orphans
  − manpower needs
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