



Hall D Electronics Forum

23 - 24 October 2007

Jefferson Lab

Tuesday - 23 October 2007

Room: CC F113

Work Session

10:30	Welcome Remarks	Elke
10:45	Trigger Architecture Overview	Chris
12:00	Lunch	
1:00	VME & VXS Backplane Assignments	Fernando
1:10	Trigger Types & Supervisor Module	Ed
1:45	Break	
2:00	Signal Distribution - Clock, Sync, Trigger	Ed, Tanest, Ben
3:00	Break	
3:15	Energy Sum Module	Chris
3:35	Energy Sum Processor	Ben
4:00	Break	
4:10	Data Format	Ed
4:30	DAQ Integration	Elliott
4:45	Slow Controls	Vardan
5:15	Adjourn	

Status Session

9:00	Detector and Electronics Overview	Fernando
9:15	SiPMs at UConn	Richard
9:35	SensL SiPM Tests	Carl
9:55	Break	
10:00	fADC125	Gerard (Via phone)
10:30	fADC250	Fernando
10:50	Break	
11:00	Energy Sum Module Tests	Chris
11:20	Splitters & Discriminators	TBA
11:40	Cabling & Cooling	TBA
12:00	Lunch	
1:00	FDC Tests	Simon
1:20	The GlueX ASIC Development	Mitch Newcomer
	- Simulations, Design, Fab	
	- Test Results from MOSIS Prototype Run	
	- Further Developments & Plans	
2:00	Break	
2:15	The GlueX Preamp Card	Fernando
	- Test Results of 24-Ch Prototype	
2:30	Future Developments	Elke
2:50	Meeting Summary	
3:00	Adjourn – Oktoberfest Fall Festival	