

Magnets

- Upgraded the four magnet cRIOs to LabVIEW 2017.
- Updated firmware of sol-fast and tor-lv cRIOs.
- Enabled core dump functionality on the fastDAQ cRIOs.
- Noticed FPGA code works fine when compiled with Quartus 17.1 (current version).
- Full order of 25-turn 3/8" (in place of 12-turn 1/4") trim pots for QD boards ordered.
 - ★ After machining is finished in the front plate, it will allow access to adjustment screws showing it is feasible to use the larger pots.
- Fixed shorted pins on RS232 line driver in spare Low Voltage chassis #2 that prevented it from working.
- Table generated for voltage tap values when first VT 18 and VT2 voltage spikes occurred ~ 200 ms before the Solenoid fast dump events.
 - ★ Based on the voltage tap values acquired from the FastDAQ data calculated the values for the hardware quench detectors which are different combinations of the voltage taps.