

DSG Meeting Minutes – Wednesday, June 11, 2014

Antonioli, Mary Ann:

- Fabricated second HV test cable.
- Fabricated and tested HV cables 9 and 10.
- Made second versions of straightness and flatness graphs for 9T, 9B, 10T, 10B.
- Made straightness combination graphs, both layers on one graph, for 6, 8, 9, and 10.
- Learned to connect a sector of Hall B DC's region 2 to the HV distribution test stand.
- Turned channels of CAEN crate on and checked board voltages of sense wires.

Bonneau, Peter:

- Attended Rockwell Automation event in Greensboro, NC.
 - This event had hands-on labs which featured Rockwell PLC hardware and software. The programs we use at the lab, including Studio 5000 Logix Designer and FactoryTalk View, were featured at the event. PLC support products and technology exhibits were shown. While at the event, Rockwell engineers assisted in the install of Studio 5000 Logix Designer on to my laptop and transferred the license control to the dongle. This confirmed that we can run the PLC designer on multiple systems without the need for networked-based license servers.

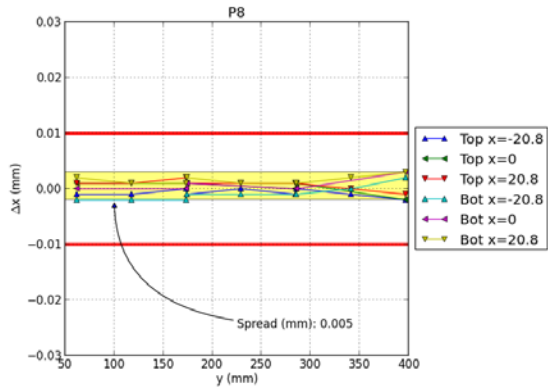
Butler, Dave:

- Attended a Rockwell Training Seminar in Greensboro NC.
 - Attended the following seminars:
 - Solve Your Application Challenges with New Sensing Solutions.
 - Applying EtherNet/IP in Real-Time Applications.
 - Achieving a Higher Level of Maintenance and Reliability to Help You Increase Uptime.
 - Understanding Advanced Programming and Editing Techniques in Studio 5000 with Logix™ Designer.
 - Survive the Maintenance Apocalypse.
 - Advanced Process Control Techniques.
 - Strategies and Tools for Migrating Legacy Distributed Control Systems
- Changed the Magnet Power Supply ramping profile software to ramp at different slew rates depending on current, I .
 - If $I < 800$ amps, slew rate is 0.2 amps/sec.
 - If $I > 800$ amps and < 1200 amps slew rate is 0.12 amps/sec.
- Ordered cables.
- Installed additional 1769-IF8 analog input module into the PLC.
- Fixed a PLC software issue with getting the Magnet Power Supply status bits.
- Editing the test procedure for the Hall D Quench Detector Tuning.

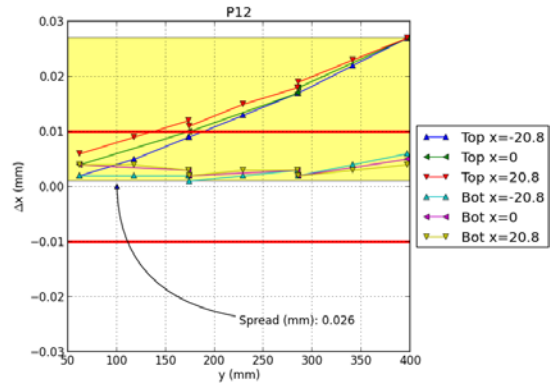
Eng, Brian:

Hall B SVT

- Generated first set of plots from FNAL CMM data showing module straightness.
 - Red lines indicate fabrication tolerance.
 - Δx of the yellow area shows the maximal offset between the strips on the topside and the strips on the bottom side of the module.



Δx within fabrication tolerance (red lines)



Δx outside fabrication tolerance (red lines)

- Set up database and web page from JLab survey data taken during module assembly.
- Participated in MRR dry runs and the MRR itself.
- Made travel arrangements for FNAL shift.
- Started SVT module production shift.

Jacobs, George:

Hall B Drift Chambers

- Moved items in ESB into wire mesh crates for stacking.
 - R1 DC shock absorber pods, R1 DC miscellaneous stringing equipment, miscellaneous R3 stringing items.
 - Re-located R2 under the air handler and gained access for R2 testing.
- Began assembly of DC GAS solenoid safety valve setup for CLAS12.
- Finished another R1.
 - Total of 9 sectors ready for installation, 6 in ESB.
 - Testing in process, and 3 need instrumentation.
- Made a spreadsheet to analyze free acid titration results from flux used for soldering SSC conductor.
- Replaced CAEN mainframe in ESB due to communication issues.
- Setting up DCRB crate for testing in the ESB.
- Attached unistrut legs to LTCC valve panel.
- Removed broken guard wire in R2Sy.
 - Wire had slipped through the crimp on the HV side.
- Meeting with Bob Miller about R2 DC HV hanger modifications.

Leffel, Mindy:

- Explained and demonstrated each phase of the Hall B SVT slow controls patch panel disconnect termination to Anatoly, as he went through the process, step by step.
- Worked at the ESB, connecting Hall B DC R2, sectors 2, 4 and 6 HV cables to verify voltage.
- Reworked two Hall B CTOF PMTs.

McMullen, Marc:

- Received final 6 HFCB V2.1 from population at Compunetix.
- Began visual QA of populated HFCBs.
 - Two completed.
- Arranged freight shipping and received 12 Hall B SVT modules from FNAL.
- Participated in Hall B SVT MRR talks and attended the review.
- Gave short demonstration on the QA procedures for the HFCB and Bus Cable panel.
- Ordered two Hall B SVT HV supply-to-distribution cables from WIENER.
- Tested the Hall B DC HV boards for R2S6 between 10–50 VDC to ensure connectivity prior to HV seasoning and signal tests.
 - Found one bad connection (supply side of cable for super layer 4 board 1.2, field wire 8 (yellow))

Mann, Tina:

- Verified voltage on Hall B DC R2 Sector 2,4,6 boards from guard, field and sense to the quad connectors.
- Repaired damaged pins on the quad connectors.
- Measured and cut wires for Hall B CTOF PMTs.
- Started web-based training.
 - SAF603A Electrical Safety Awareness

Sitnikov, Anatoly:

- Checked HV of channels of Hall B DC R2S2.
- Pulling broken wire # 223 from Hall B DC R2S2.
- Participated in setting up electronic rack for signal measurement from Hall B DCs.
 - moved rack, disconnecting, and connecting signal cables for rack.
- Made 10 jumper cables for Hall B SVT humidity/temperature sensor boards.

Teachey, WerthHall D Target

- Writing LabVIEW code to automatically calculate the 13 byte command to the Hall D target compressor.
 - The commands will be stored in an array in the PLC so that they may be accessed by the PLC code.
- Attended Rockwell PLC training in Greensboro, NC.
- Discussed and planned placement and cable routes of PLC equipment in the target group's caged area.
 - PLC equipment will be close enough to the target that all target equipment can be connected to the PLC and debugging of hardware / software can begin.

Hall B SVT

- Completed request to reboot the cRIO and MPOD mainframes for the JLab Module Reception Test Stand.