

DSG Meeting Minutes – Wednesday, August 20, 2014

Antonioli, Mary Ann:

Hall B SVT

- Completed, for modules P50-P82, current-draw calculations.
- Completed HV distribution box #3.
 - Inventoried and ordered connectors, pins, and screws for box #4. Began work on box #4.
- Updated slow controls spreadsheet. Labeled slow controls block in AutoCAD drawing with cable hook-ups, according to spreadsheet.
- Worked on termination of 37-pin CPC connector end of spare HV cable.
- Assisted Mindy with disconnecting and removing slow controls panel from the clean room.
- Placed orders and submitted PRs for parts and equipment.

Bonneau, Peter:

Hall B

- **LTCC** automated test station.
 - Added off-line troubleshooting mode to test program. Feature allows updates to the program without requiring the use of the Keithley Electrometer.
 - Updated default configuration files.
 - Recompiled and installed executable program.
- Programming PAC (cRio) for **SVT** cleanroom.
 - System will monitor environmental conditions in the clean room and equipment, such as the dry box and the backup dehumidification system.
 - Installed real time operating system and driver updates, and tested network configuration on cleanroom cRio chassis.
- Coordinated activities on **SVT** slow controls development.
 - Revised and released V450 driver to Accelerator Controls Group with test program.
 - Worked with the Computer Center and Accelerator Controls Group on the installation of a slow controls workstation.
 - Posted interlock updates to the slow controls website.
- Updating layout of **SVT** patch panel to include LV distribution for humidity sensors.

Butler, Dave:

Hall D

- Continued Hall D Magnet commissioning; was shift expert during ramp to 1250 A.

Eng, Brian:

Hall B

- Tested **SVT** HV Distribution box labeled "SVT HV 3".
 - All + and - pins are fine, but front panel cable shields aren't consistent (some go to iseg connectors, some don't; some connectors are tied to the box, some aren't).
 - Met with Pete to come up with a new scheme; all iseg cable shields go to one jack, all module cable shields go to one jack, and the final jack is tied to the box.
- Fixed bug in **SVT** module database website.
 - After entering a module the skins couldn't be changed (it was coded to check for a number, but skins have a hyphen in them).

- Set up **SVT** LV and HV for R1 and R2 in cleanroom.
 - Installed cards to match final installation.
 - Re-routed cables on the crate end.
- Revised hardware used in the **SVT** differential line test.
 - First test of spy board with an HFCB on test stand resulted in a differential line test passing even when only half of a chip's out-clock was connected. Issue fixed by Mindy remaking the pull-down resistors value from 1 K Ω to ~500 Ω (the 1 K Ω was found with trial and error on Peter's test stand).
- Corrected programming bug on **SVT** test stand at FNAL.
 - SBC SD card ran out of inodes resulting in disk out of space errors, even though there was free space. This was due to never deleting all the plot files the testchan program creates. These have since been transferred to a JLAB computer; should be able to do 300+ scans before this is an issue again.
- Removed P23 from R1 of **SVT**.
- Added vacuum interlock and chiller controls to **HPS SVT** PLC code.
 - Still no word on interfacing with flowmeter. (Available flowmeter is pulsed output type which current PLC modules can't handle)

Jacobs, George:

Hall B DC

- Removed:
 - R3S6 from the R3 A-frame in the clean room.
 - R3 A-frame from EEL rm 124 clean room
 - miscellaneous items from EEL rm 124 clean room, moved to ESB
- Placed clean room gantry in final location.
- Moved R3S4 from ESB to clean room.
- Meeting with Paul Hanson on TORUS cable tray design for CLAS12 R3.
- Re-arranged CLAS12 R2 storage area in ESB to minimize footprint size.
- Installed the CLAS12 R1 A-frame fixture in the EEL rm 124 clean room.
- Placed CLAS12 R1S3 on A-frame in EEL rm 124 clean room, ready for survey.
- Ordered miscellaneous gas fittings via GSS.
- Made an ATlist for survey of R1 on A-frame spit in multiple orientations.
- Continued supervision of
 - new DCGAS line installation; welding now complete, remainder will use fittings.
 - Installation of HV boards and HV cable attachment on R3S6.
 - Removal of broken wires from R3S4 after transport.
 - Wire wrapping and initial DMM testing of R3S4 in clean room.

Leffel, Mindy:

Hall B

- Testing, repackaging, and organizing **LTCC** Winston cones
- Removed the **SVT** slow controls patch panel and the cables out of the clean room.
- Assembled three terminator assemblies to be used as test fixtures for the differential clock lines of the spy-board, designed by Peter to look at **SVT** signals.

McMullen, Marc:

Hall B SVT

- Testing production modules at Fermi.

- Module P37 and P38 completed.
 - Started on P39 and P40.
 - QA'd 3 backing structures.
- Continued testing remaining HFCEBs
- Sent additional masking instructions to Compunetix to cover HFCEB HV- pads.

Mann, Tina:

Hall B

- Testing, repackaging, and organizing small **LTCC** Winston cones.
 - Made an EXCEL spreadsheet to track serial numbers of the cones tested daily.

Sitnikov, Anatoly:

Hall B

- Designing, cutting, and polishing equipment for 0.3 mm boron-silicone fibers needed for the laser calibration system of **CTOF**.
- Worked on HV and signals cabling for **PrimEx** experiment.

Teachey, Robert (Werth):

Hall D

- Developed PLC code for the **Tagger** HV reset.
 - Tested 1 of the 4 reset functions.
- Developed cRio FPGA test code for cRio/EPICS prototype interface.
- Installed MEDM EPICS GUI developer for cRio/EPICS prototype interface.
- Made a presentation at the Hall D Slow Controls meeting (below).