DSG Meeting Minutes – Wednesday, September 03, 2014

Antonioli, Mary Ann:

Hall B SVT

- Wired eight humidity temperature sensors (HTS) to the **Slow Controls System's (SCS)** panel.
- Worked on labeling power and LV for sensor distribution (5 V and 24 V) blocks #19 and #20 in the **SCS** AutoCAD drawing.
- Place order for water sensors for the **Water Detection System**.
- Linked, after discussion with Robert, four available bios to the **DSG website**.
  - Attempted to set up new pages to add presentations.
- Researched and ordered magnifying light **(DSG-Infrastructure)** for Tina.
- Set up for **DSG Technical Documentation** DSG_technical M drive space.

Bonneau, Peter:

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- Investigated **Slow Controls System's (SCS)** humidity temperature sensor fault in Region 2 support ring.
  - This sensor had been working previously but now the readback was out of range. The sensor was receiving a supply voltage but the output sensor voltage was incorrect. A pushed output pin was found on the connector as the cause of the fault.
- Setting up EPICS test stand for testing **SCS** patch panel.
  - Test will verify the temperature/humidity sensor connections. In addition, the patch panel terminal block connections for the HFCBs will be checked.
- Checked design implementation of the **SCS** patch panel LV distribution system.
  - This system will supply LV (5 V and 24 V) to the SVT slow control sensors including humidity, water flow, and leak detection.
- Specified **Water Detection System** for detector.
  - The system consists of a water detection cable connected to a control unit. The detection wire will be installed into the drip pan.
- Continued on PAC (cRio) projects for the **cleanroom** and for the **Gas System**.
  - Received and assembled rack mount for cRio and supporting electronics.
  - Installed power supply and fusing for modules into rack kit.
  - Delivery of the cRio 9300 processor and backplane is expected by October 1st.

Butler, Dave:

Hall D

- Continued making requested changes (removing edit protection of threshold values, sending individual tags instead of arrays) to the **BCAL** software alarm interface with EPICS.
- Made changes to the Cryo PID control code of the **Magnet** to reduce the chattering of the cryo valve relays.
- Started programming the PLC for the **Start Counter** temperature monitor (10 thermocouples).
- Prepared hardware/software to monitor temperature and humidity at the upstream and downstream bore of the **Magnet**.

Eng, Brian:

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- Fixed code for **HPS SVT's** PLC setup for MPOD interlocks.
  - The PLC TTL modules use inverted logic.
- Remote debugging on **Module P40** @ FNAL.
  - All chips show differential line failures on core-talking, got-hit, out 1 – 4, due to data lines being used and trying to parse number of hits (and always getting 0 hits when there should be some).
  - Changed code to use status words on these lines, now the chips pass the tests.
  - Either the pulser input or hit/no-hit discriminator failing is the source of the problem?
- Worked with ACC on getting V450 to read module temperature as well as environmental RTDs and humidity sensors for the **Slow Controls System.**
- Updated FNAL **module production** CMM plots.
- QA-ed two backing structures for **module production.**

Jacobs, George:

Hall B

- QA-ing **magnet** coils at AES.
  - Schedule crunch drives need for continuous presence at AES.

Leffel, Mindy:

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- Repaired the LV and SC drain wires for the last seven bundles for R2 **assembly.**
- Repaired the LV drain wires on nine cables and started SC cables for R3 **assembly.**
- Repaired two connectors in the clean room for the **Slow Control System**: one SC and one HTSB.
- Shortened four more humidity temperature sensor board cables for the **Slow Control System** (total seven) to19".

Hall D

- Completed three cables for the thermocouple of the **Pair Spectrometer.**
- Started making three MS to D-sub cables for **Tagger** radiation exposure monitor.

McMullen, Marc:

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- Completed **post-manufacturing tests** on 12 HFCBs.
  - Categorized them into four groups.
    - No issue.
    - Diff lines > 0.1 GΩ
    - Diff lines < 0.1 GΩ
    - High current.
  - Sent 11 for assembly.
- Tested four populated HFCBs.
  - Could not test temp sensors.
  - Sent to Fermi for module production.

- Sent two approved backing structures for module production.
- Turned in FNAL loaned equipment cost to property management.

Mann, Tina:

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- Currently in Fermi testing and troubleshooting HFCBs.
- Over viewing HFCB process and production.

Sitnikov, Anatoly:

Hall B

- Assembling four **CTOF** detectors.
- Checking noises from four **CTOF** detectors.

Teachey, Robert (Werth):

- Installed and cabled PLC Hardware for the **Hall D Tagger** CAEN HV reset.
- Started PLC code for **Hall D FDC** CAEN HV reset.
- Started troubleshooting bad readback of **SVT** R1 humidity sensor.