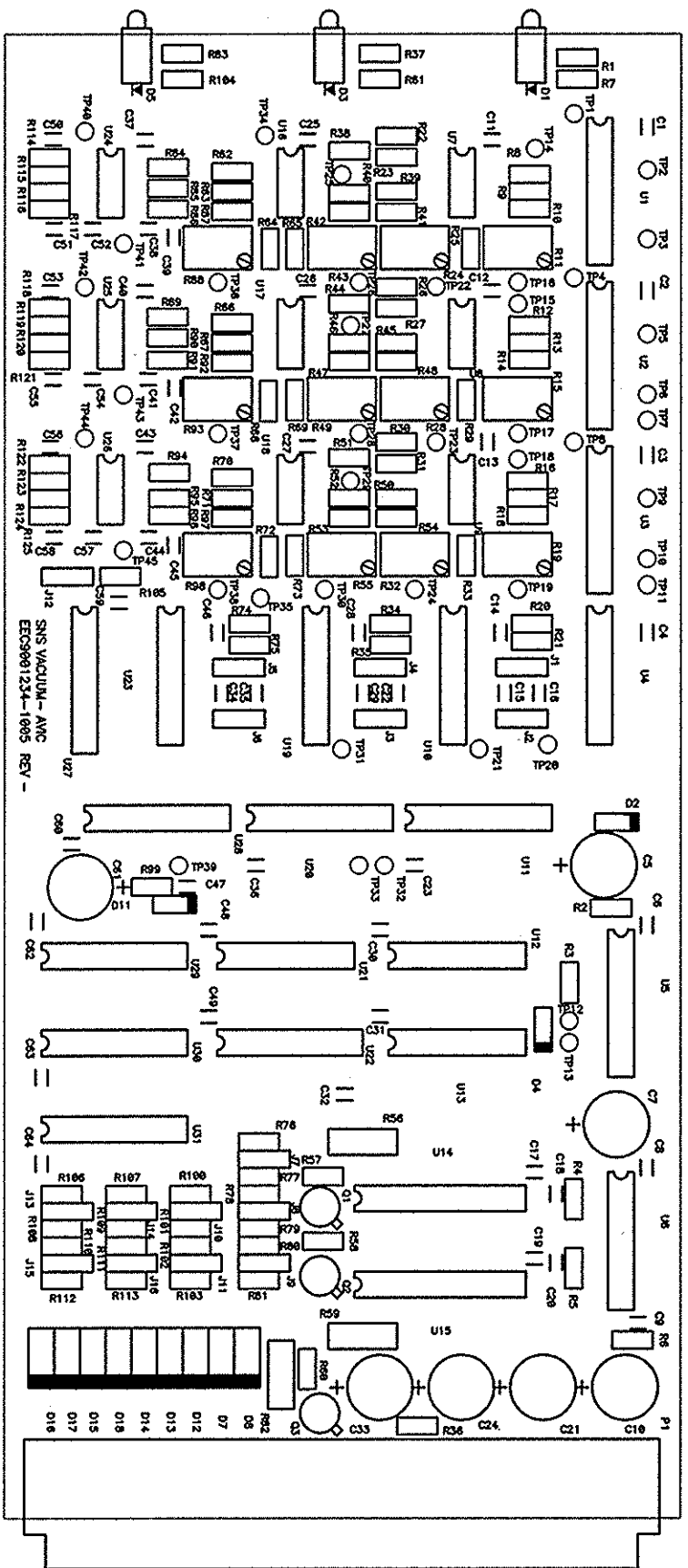


REVISIONS			
REV.	REMARKS	DATE	APPROVED



VIEWED FROM THE COMPONENT SIDE

- NOTES:
1. REFERENCE ELECTRICAL SCHEMATIC DRAWING, TUNAF EEC9001234-1005.
 2. ASSEMBLE AND SOLDER PER IPC-A-610A AND MIL-STD-2000.
 3. OBSERVE POLARITY OF CAPACITORS, DIODES, ETC.
 4. ELECTRICAL REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY AND NEED NOT APPEAR ON THE PARTS OR COMPONENTS UNLESS OTHERWISE SPECIFIED.

DRAWN ZUKERMAN	DATE 5/28/02	<p>Thomas Jefferson National Accelerator Facility NEWPORT NEWS, VIRGINIA UNITED STATES DEPARTMENT OF ENERGY</p>
CHECKED <i>[Signature]</i>	DATE 5/19/03	
APPROVED <i>[Signature]</i>	DATE 5/19/03	<p>TITLE</p> <p>SNS VACUUM SYSTEM BEAMLINE/WAVEGUIDE ANALOG VACUUM INTERLOCK CARD(AVIC) ASSEMBLY DRAWING</p>
ENGINEER DAVE EARDLEY	SCALE 1X	
<p>CAO T.D. NO. EEC9001234-1008</p> <p>MODEL V.SNS.AVC</p> <p>DRAWING NO. EEC9001234-1008</p>		<p>REV. -</p> <p>SHEET 1 OF 2</p>

