

① sum 8.

INPUT: { PL1, PL2, PL3a, PL4a.  
          { PR1, PR2, PR3a, PR4a.

Ans

OUTPUT: SS12TS1, PS1a-TS1a. ?

INPUT: { PL3b, PL4b, PL5, PL6a  
          { PR3b, PR4b, PR5, PR6a.

Ans

OUTPUT: SS34TS2, PS2a-TS2a. ?

② sum 8.

INPUT: { PL6b, PL7, PL8, PL9a.  
          { PR6b, PR7, PR8, PR9a.

Ans

OUTPUT: SS56TS3, PS3a-TS3a. ?

INPUT: { PL9b, PL10, PL11, PL12a,

Ans

OUTPUT: { PR9b, PR10, PR11, PR12a.  
          SS78TS4, PS4a-TS4a, T4, FADC.

③ sum 8:

INPUT: PL12b, PL13, PL14a, PL15a.  
          PR12b, PR13, PR14a, PR15a.

Ans

OUTPUT: SS910TS5, PS5a-TS5a. ?

INPUT: PL14b, PL15b, PL16, PL17.  
          PR14b, PR15b, PR16, PR17.

Ans

OUTPUT: SS1112TS6, PS6a-TS6a. ?