

GeP-III electronics cable Table

(In HMS Hut)

S1X-	Left #1 Discriminat or <i>INPUT</i> from top to bottom	Cable #	S1X+	Left #2 Discriminator <i>INPUT</i> from top to bottom	Cable #	S1Y-	Right #1 Discriminator <i>INPUT</i> from top to bottom	Cable #	S1Y+	Right #2 Discriminator <i>INPUT</i> from top to bottom	Cable #	S0+/-	Discriminator <i>INPUT</i> from top to bottom	Cable #	Comments
h1x1-	1	1	h1x1+	1	33	h1y1-	1	17 L	h1y1+	1	39 R	S0-1	1	35 R	Discriminator for S0 is located in the top create
h1x2-	2	2	h1x2+	2	34	h1y2-	2	18 L	h1y2+	2	40 R	S0-2	2	36 R	
h1x3-	3	3	h1x3+	3	35	h1y3-	3	19 L	h1y3+	3	41 R	S0-3	3	37 R	
h1x4-	4	4	h1x4+	4	36	h1y4-	4	20 L	h1y4+	4	42 R	S0-4	4	38 R	
h1x5-	5	5	h1x5+	5	37	h1y5-	5	21 L	h1y5+	5	43 R				
h1x6-	6	6	h1x6+	6	38	h1y6-	6	22 L	h1y6+	6	44 R				
h1x7-	7	7	h1x7+	7	39	h1y7-	7	23 L	h1y7+	7	45 R				
h1x8-	8	8	h1x8+	8	40	h1y8-	8	24 L	h1y8+	8	46 R				
h1x9-	9	9	h1x9+	9	41	h1y9-	9	25 L	h1y9+	9	47 R				
h1x10-	10	10	h1x10+	10	42	h1y10-	10	26 L	h1y10+	10	48 R				
h1x11-	11	11	h1x11+	11	43										
h1x12-	12	12	h1x12+	12	44										
h1x13-	13	13	h1x13+	13	45										
h1x14-	14	14	h1x14+	14	46										
h1x15-	15	15	h1x15+	15	47										
h1x16-	16	16	h1x16+	16	48										

All discriminators thresholds are----- 40mV

Discriminators output signal width---- 30ns

Delay time from PMT to Discriminator input ----- 27ns

Delay time from PMT to HMS hut elects. (cables + all electronics) – 84ns