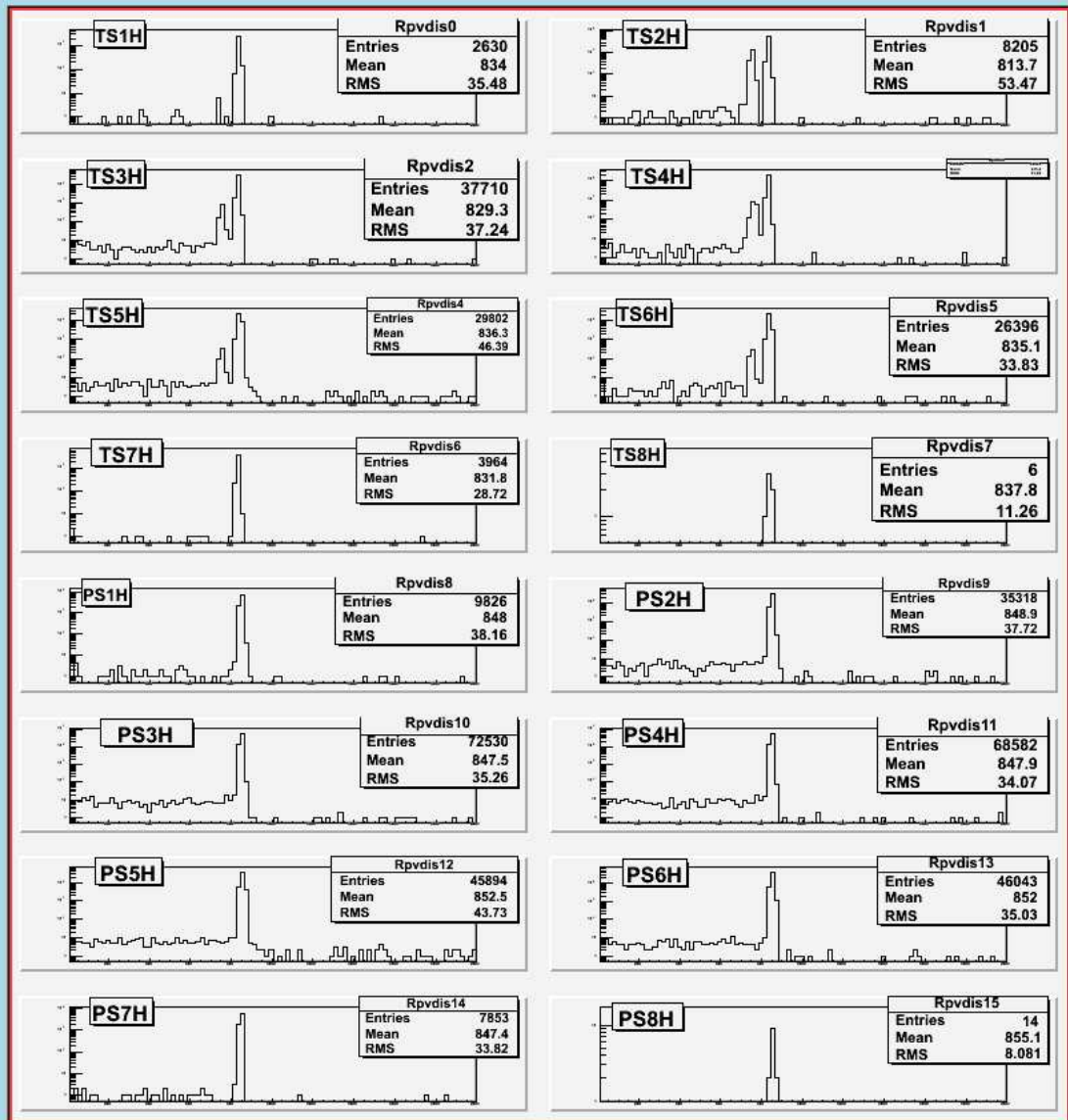


- R-arm S1 TDC (0-5)
- R-arm S1 ADC (0-5)
- R-arm S2 TDC (0-5)
- R-arm S2 ADC (0-5)
- R-arm Cherenkov (0-9)
- R-arm Preshower (0-15)
- R-arm Preshower (16-31)
- R-arm Preshower (32-47)
- R-arm Shower (0-16), 15 missing
- R-arm Shower (17-33), 31 missing
- R-arm Shower (34-50), 47 missing
- R-arm Shower (51-67), 63 missing
- R-arm Shower (68-78), 79 missing
- Page 13
- Page 14
- Page 15
- Page 16
- Event Type
- R-arm VDC wires
- R-arm VDC TDC
- R-arm VDC hits



Prev

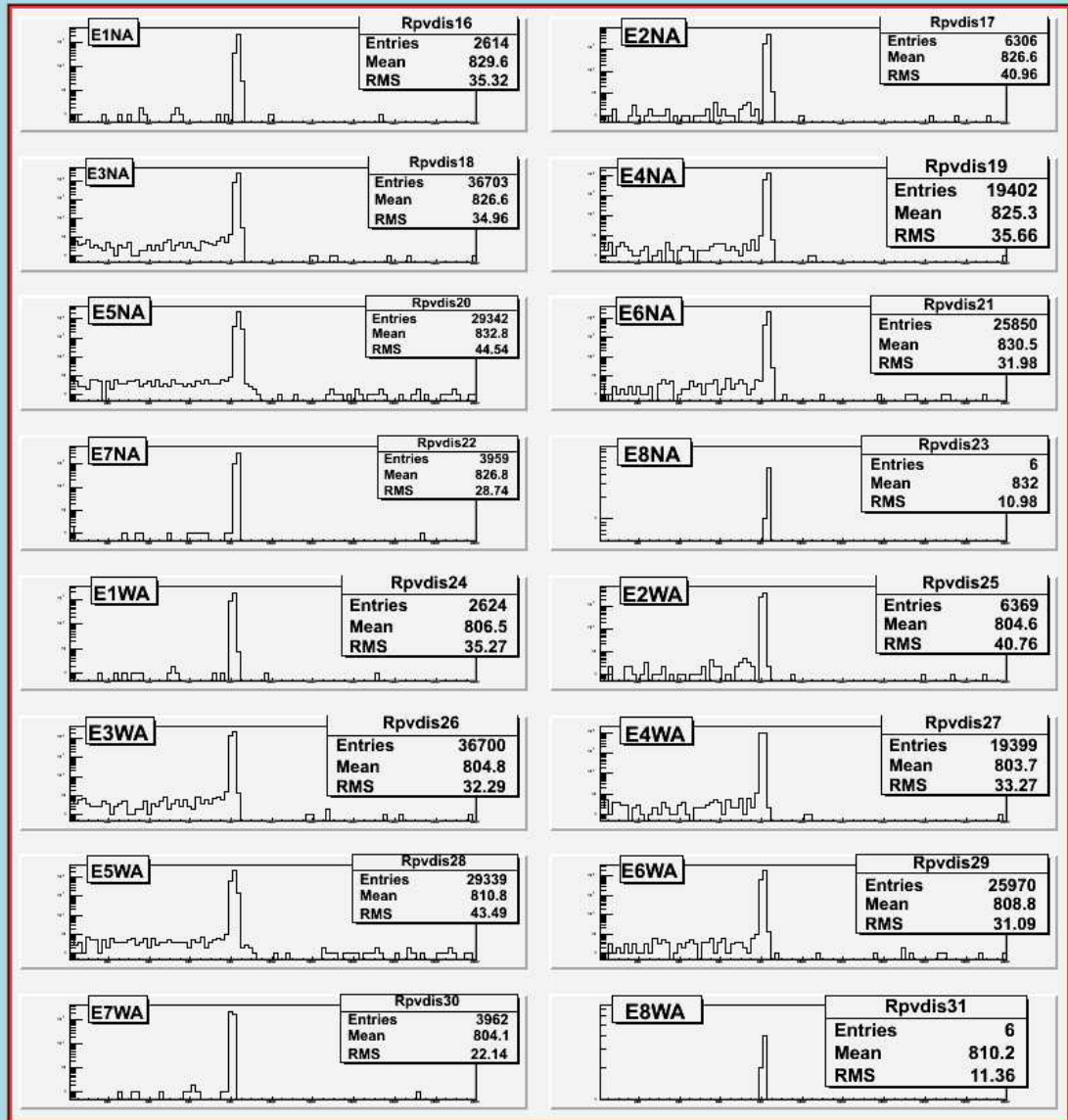
Next

Exit (0)

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Figure 1: Fastbus 1877 TDC 1 to 16. The title in the plot shows which TDC channel has which signal from the PVDIS flow-chart-diagram. The TDC was in common-stop mode. The common-stop for these data was provided by only T1 (S1 .and. S2)-trigger. The rightmost peak in the TDC-histogram belongs to an EDTM pulser signal. Any trace left to the EDTM signal is the signal due to PVDIS triggers.

- R-arm S1 TDC (0-5)
- R-arm S1 ADC (0-5)
- R-arm S2 TDC (0-5)
- R-arm S2 ADC (0-5)
- R-arm Cherenkov (0-9)
- R-arm Preshower (0-15)
- R-arm Preshower (16-31)
- R-arm Preshower (32-47)
- R-arm Shower (0-16), 15 missing
- R-arm Shower (17-33), 31 missing
- R-arm Shower (34-50), 47 missing
- R-arm Shower (51-67), 63 missing
- R-arm Shower (68-78), 79 missing
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- R-arm VDC wires
- R-arm VDC TDC
- R-arm VDC hits



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Figure 2: Fastbus TDC 17 to 32

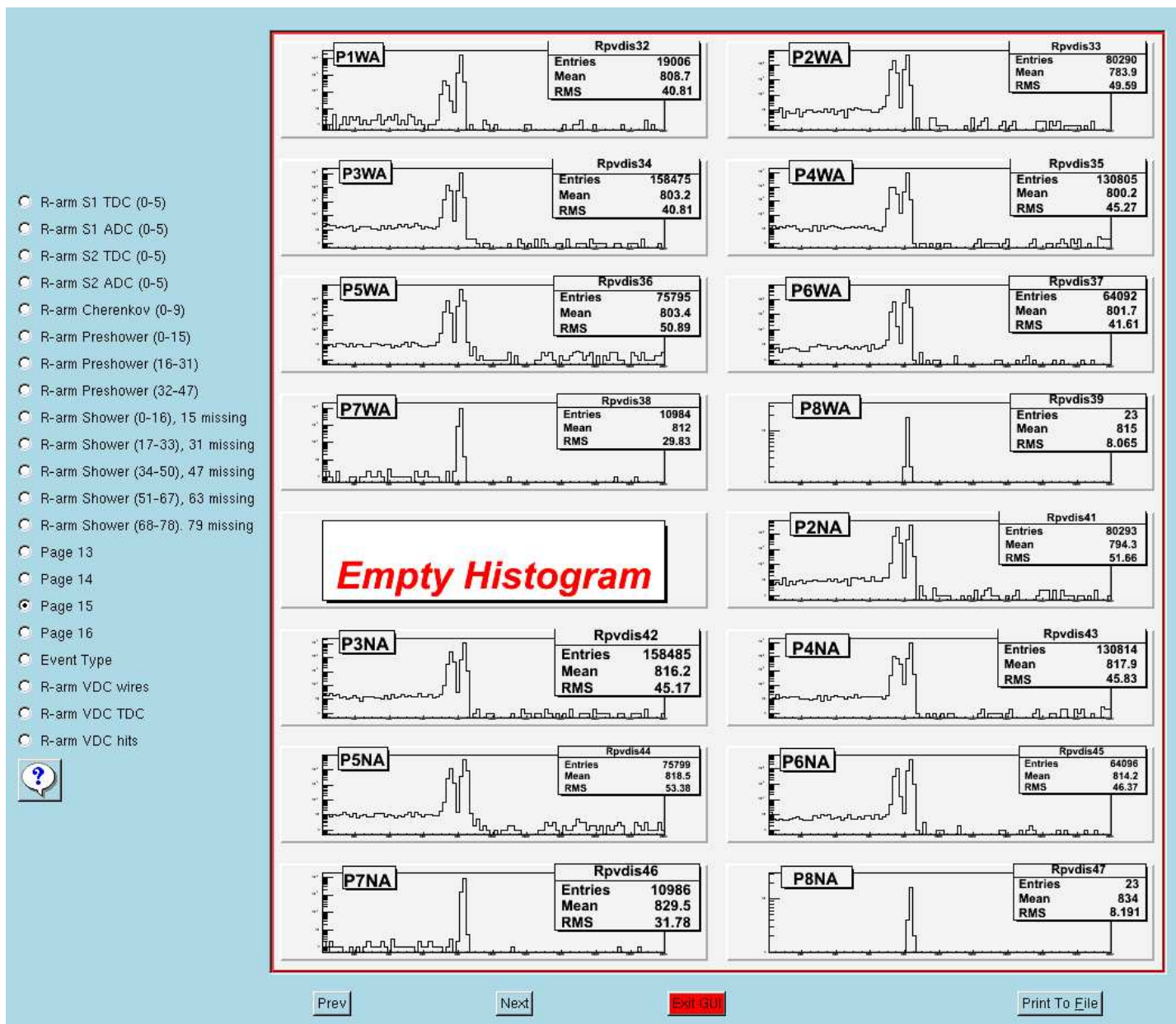
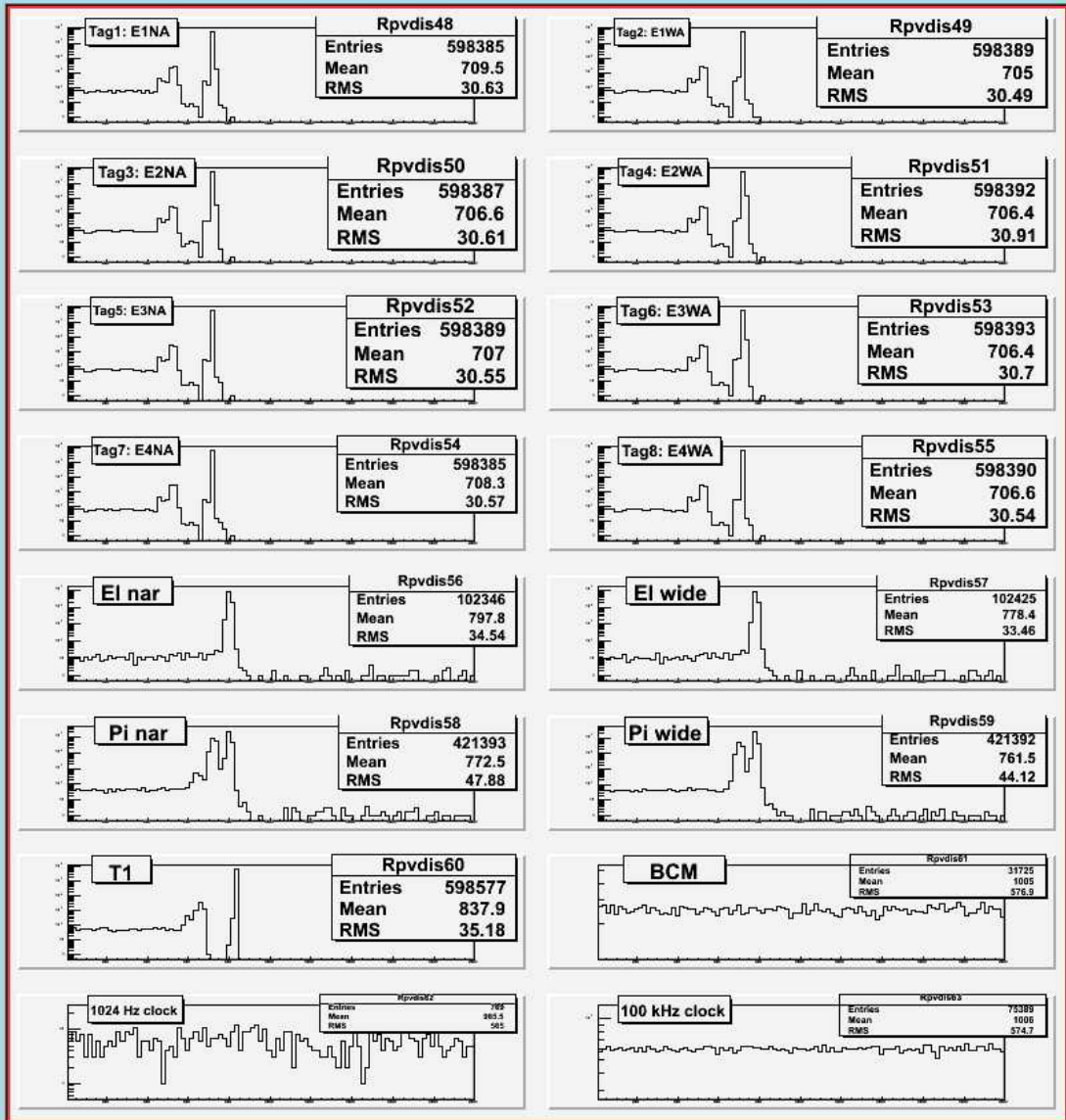


Figure 3: Fastbus TDC 33 to 48. The 40th channel (counting from 0) in the histogram is missing which means that this signal must also be missing in the 3rd NIM-ECL module ch8 (that means the scaler channel 39 (starting from zero) should also be missing). This is one way to test which channel is missing in the PVDIS setup.

- R-arm S1 TDC (0-5)
- R-arm S1 ADC (0-5)
- R-arm S2 TDC (0-5)
- R-arm S2 ADC (0-5)
- R-arm Cherenkov (0-9)
- R-arm Preshower (0-15)
- R-arm Preshower (16-31)
- R-arm Preshower (32-47)
- R-arm Shower (0-16), 15 missing
- R-arm Shower (17-33), 31 missing
- R-arm Shower (34-50), 47 missing
- R-arm Shower (51-67), 63 missing
- R-arm Shower (68-78), 79 missing
- Page 13
- Page 14
- Page 15
- Page 16
- Event Type
- R-arm VDC wires
- R-arm VDC TDC
- R-arm VDC hits



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Figure 4: Fastbus TDC 49 to 64. The first 8 histograms here are Tagger signals.

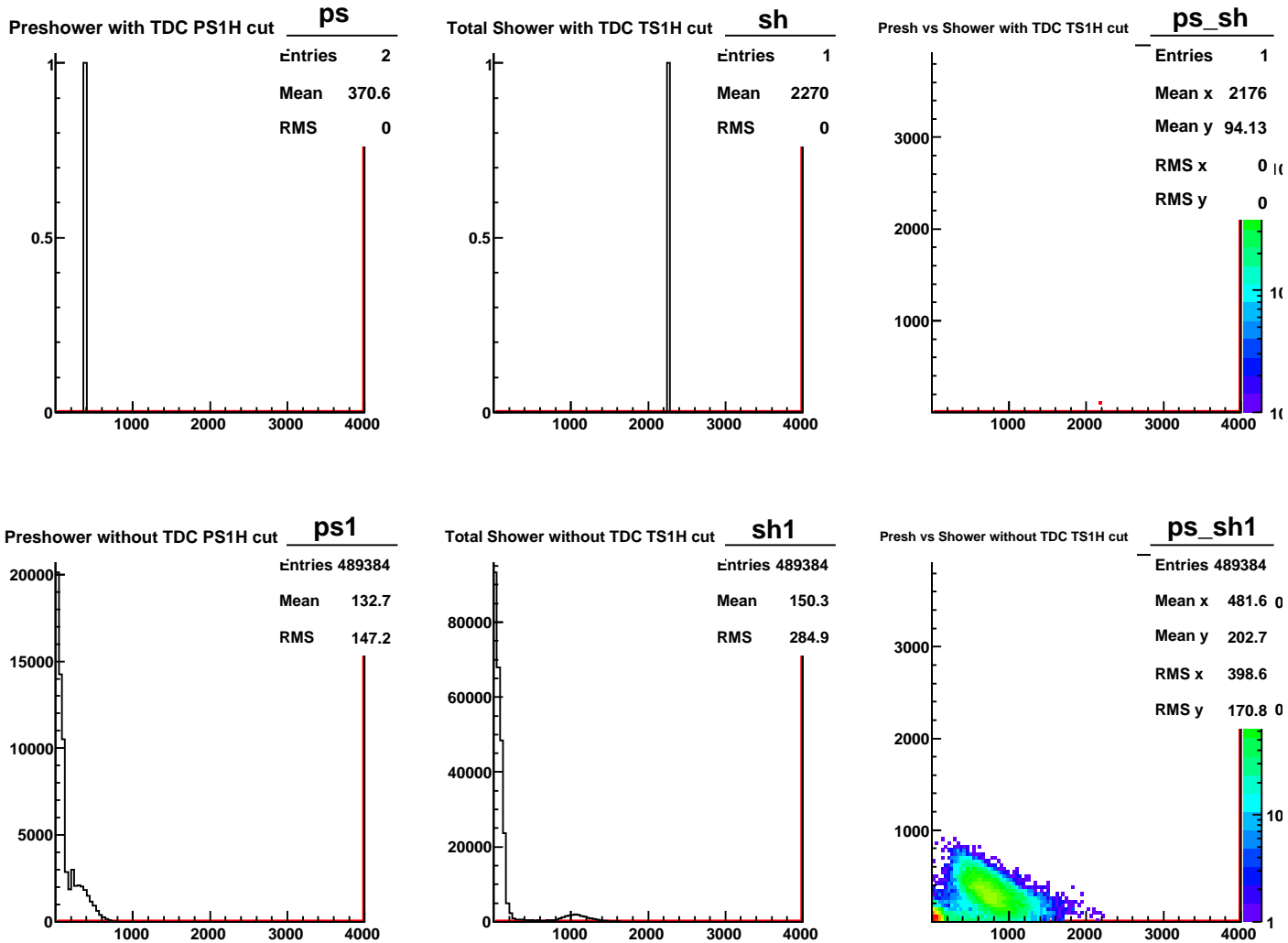


Figure 5: Group 1. I would note that the preshower and shower signal used in the histogram is only pedestal-subtracted but not gain-matched. This result may have little meaning only to a first order since the shower-preshower histograms were not aligned.

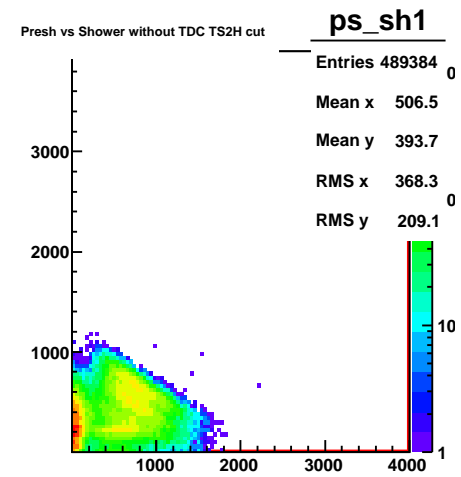
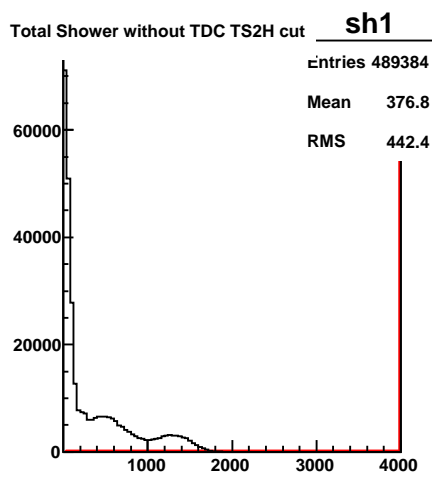
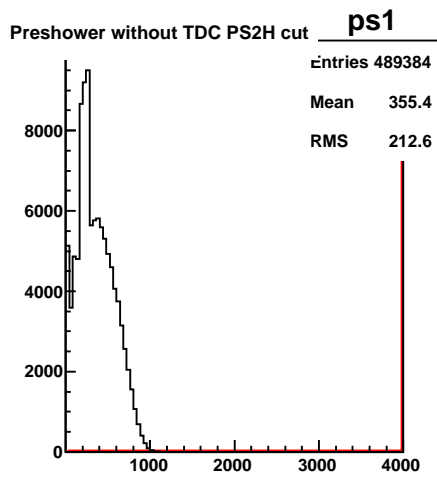
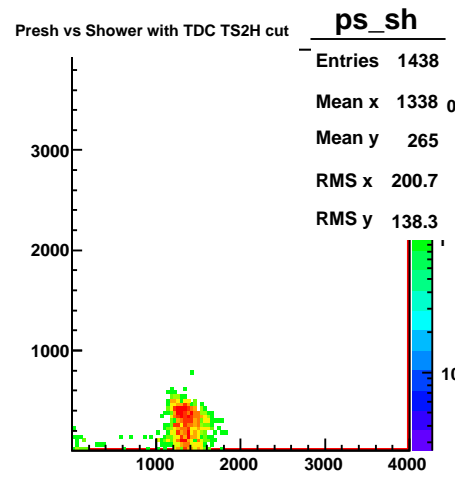
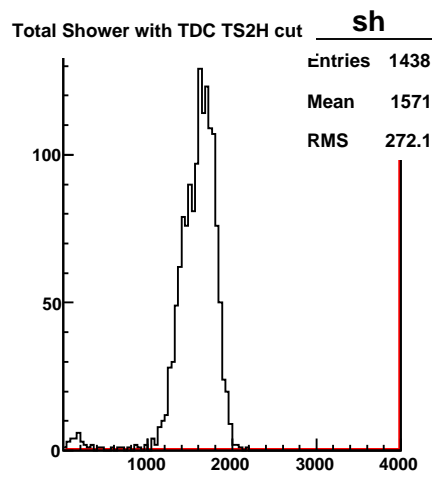
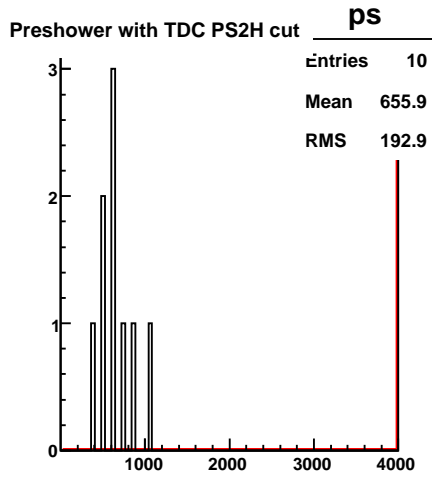


Figure 6: Group 2

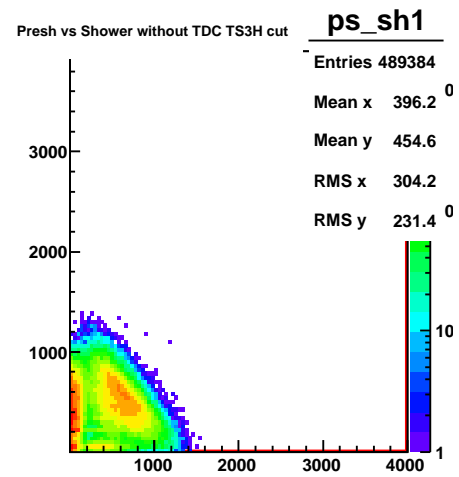
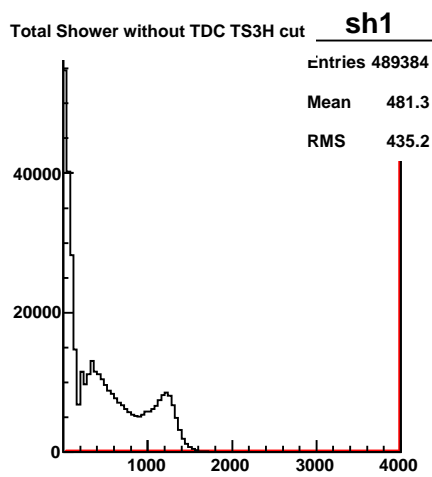
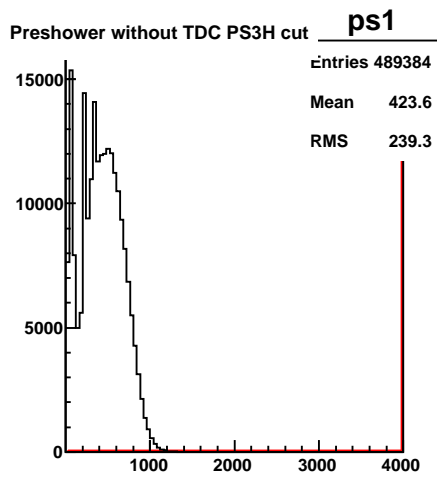
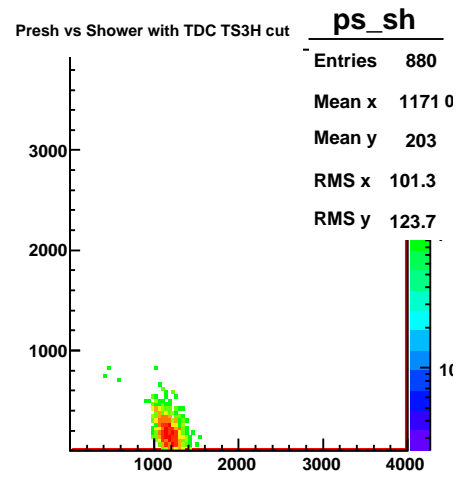
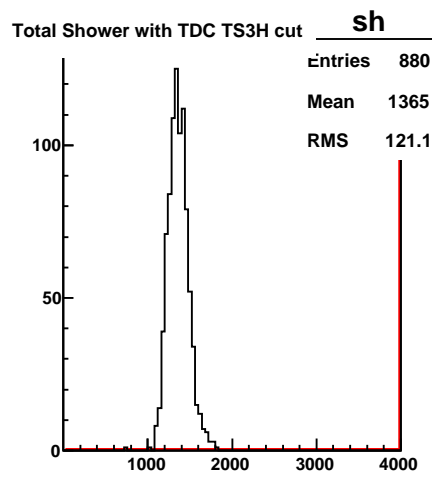
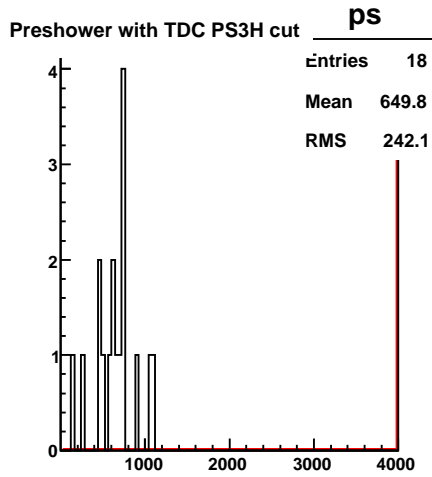


Figure 7: Group 3

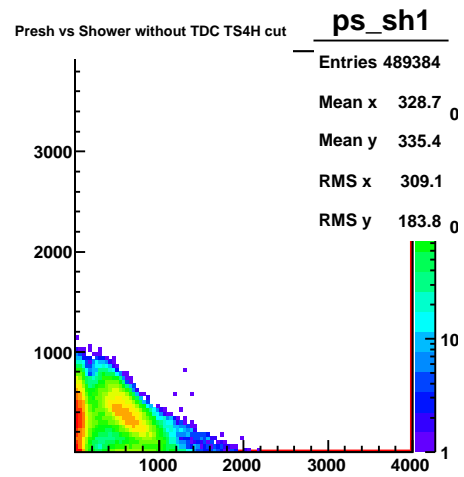
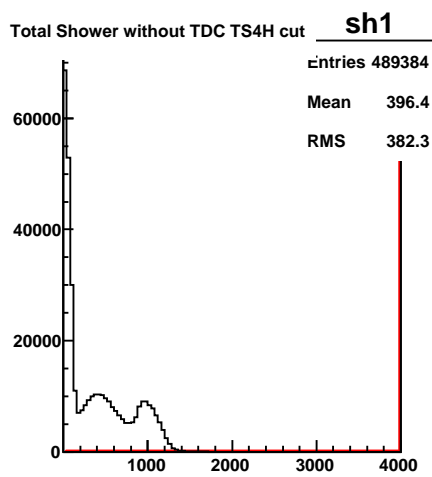
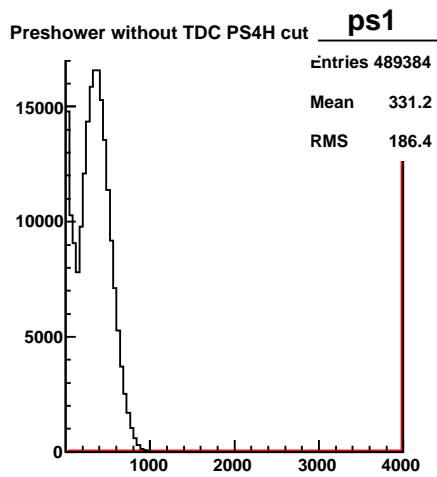
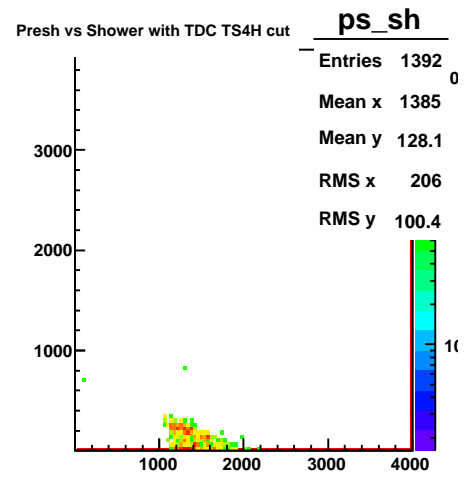
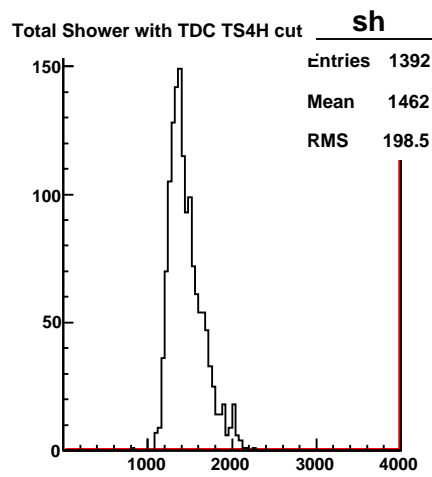
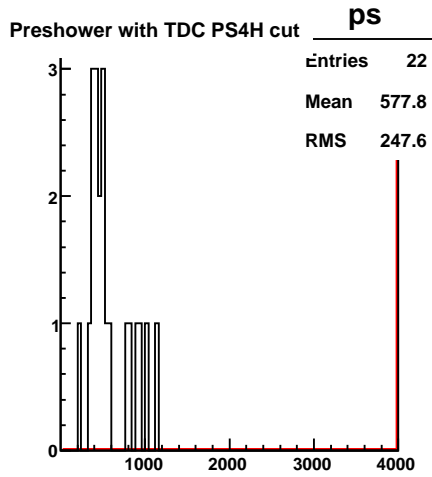


Figure 8: Group 4



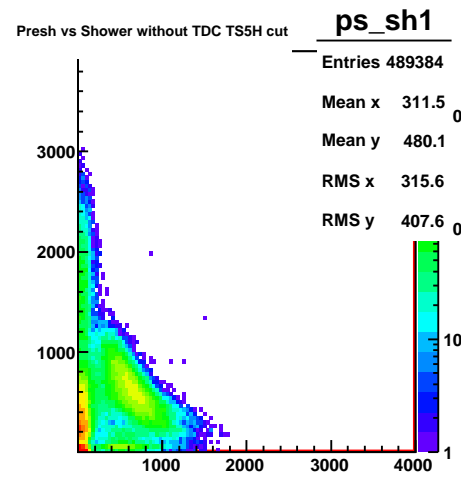
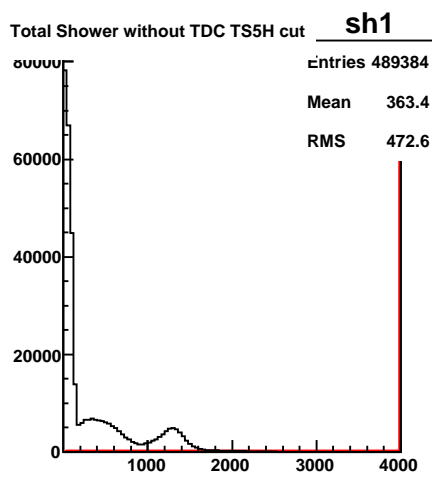
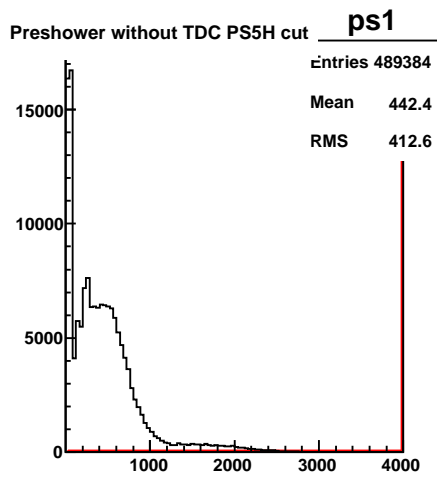
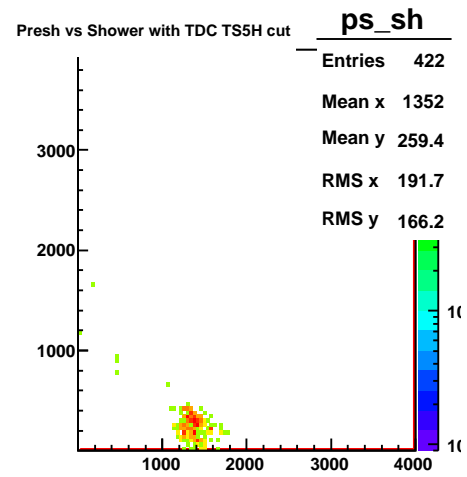
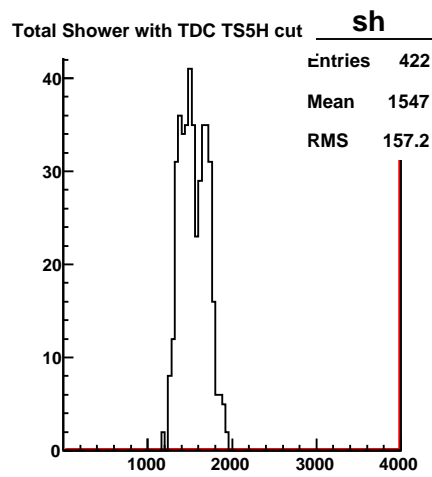
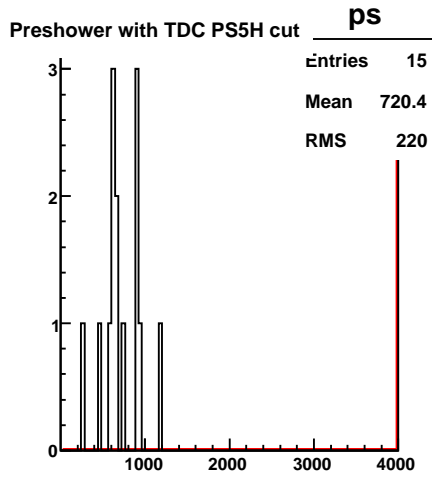


Figure 9: Group 5

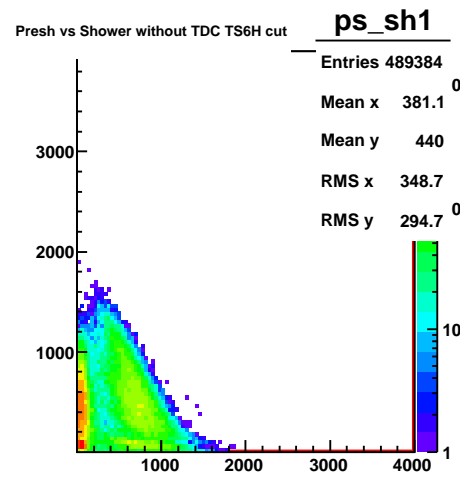
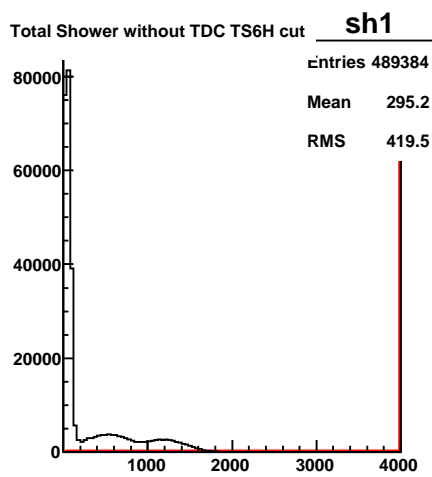
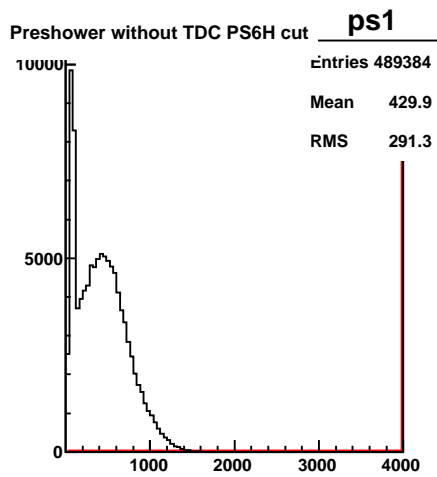
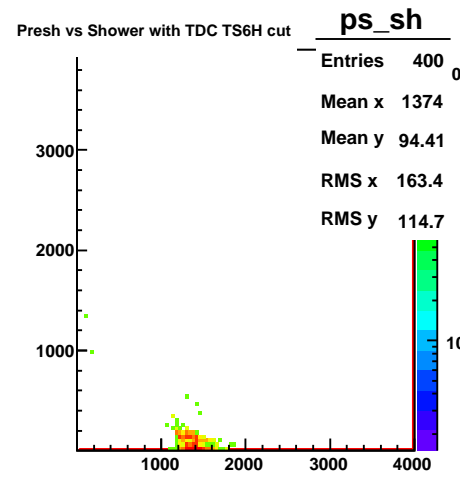
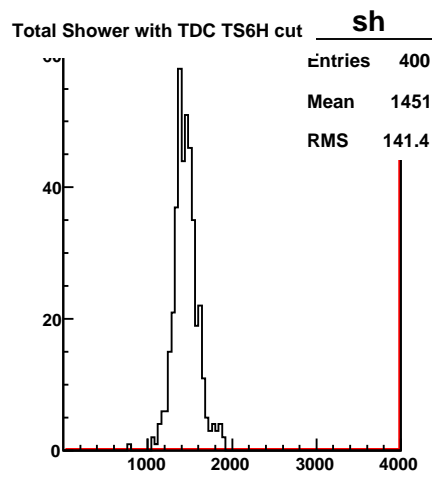
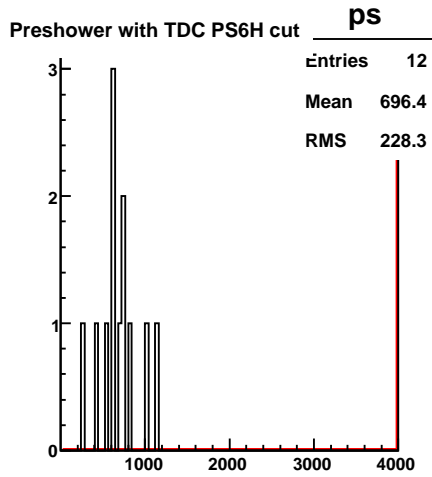


Figure 10: Group 6

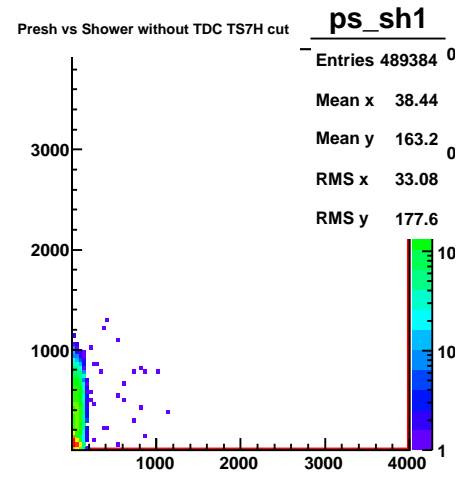
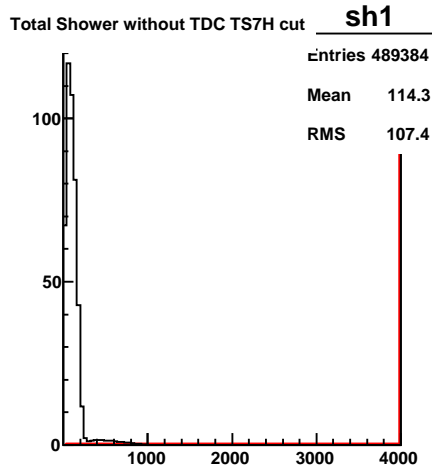
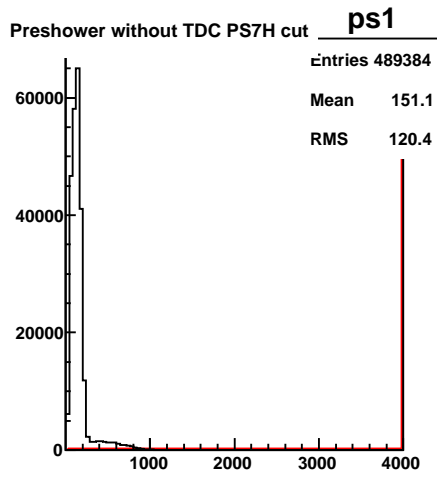
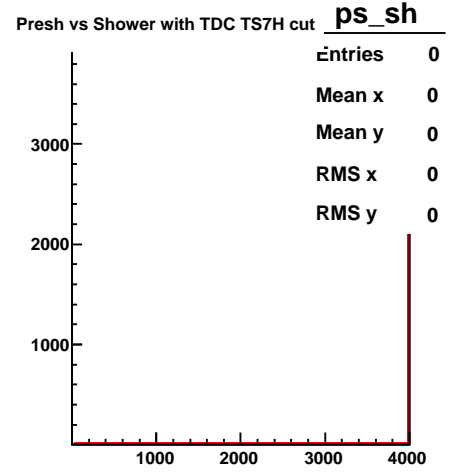
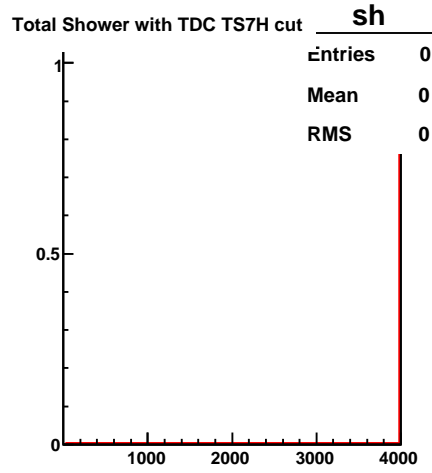
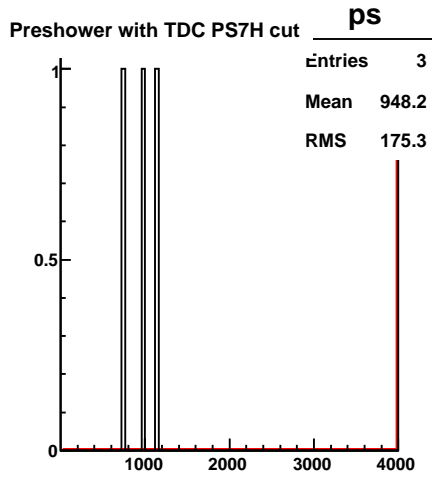


Figure 11: Group 7

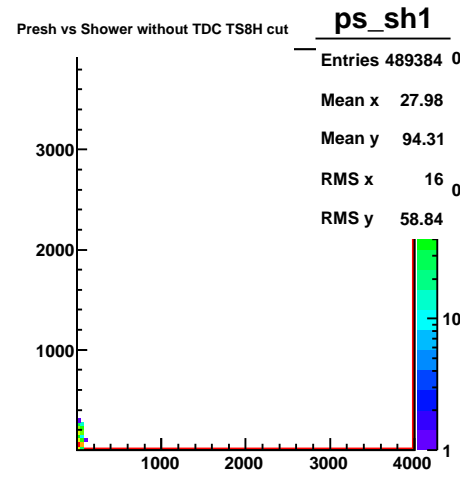
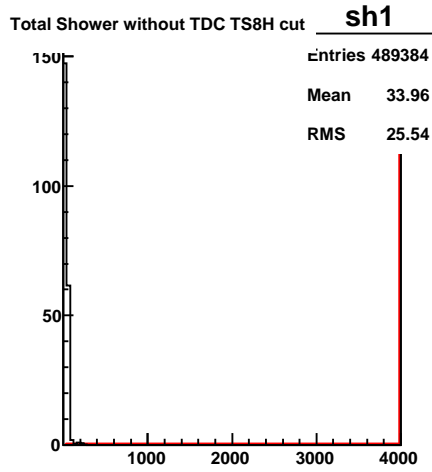
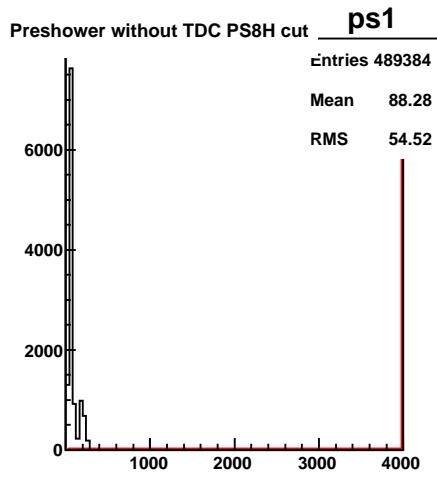
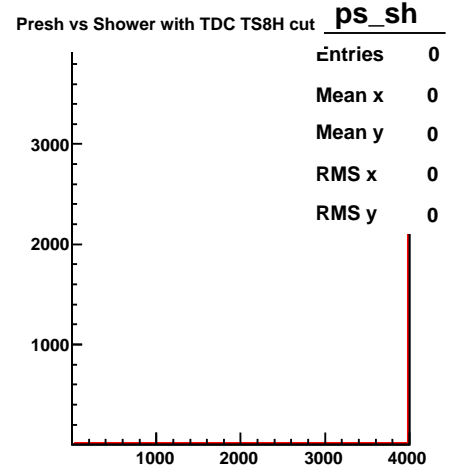
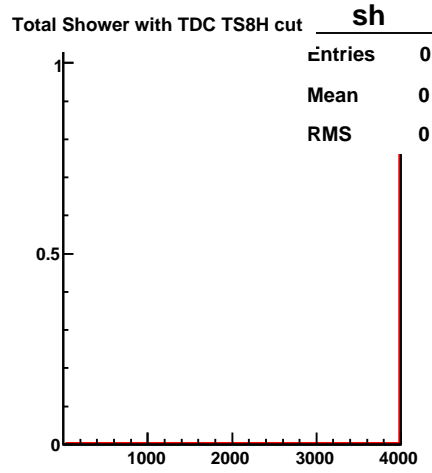
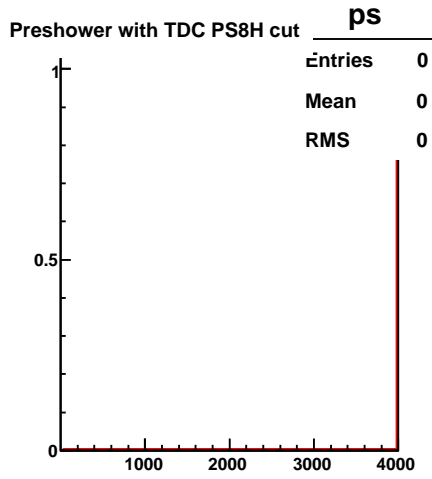


Figure 12: Group 8