100 kHz clock ch16NIM-ECL Modules Scaler Modules 2u ch32 ch96 1d ─► 1 L 1u → 2 L 2d → 3 L ch17 ch81 1d 2d 3 ch80 ch16 L R L R L R L R ch1 ch65

1d (or 1L) has TS1H to TS8H & PS1H to PS8H

4R goes to CAEN v767 TDC

1u (or 2L) has E1NA to E8NA & E1WA to E8WA

2d (or 3R) has P1WA to P8WA & P1NA to P8WA

2u (or 4L) has following channel inputs:

tagger: ch 1 to 8

ch9-12: el narr, el wide, pi narr, pi wide

T1: ch13

BCM ch14

10 kHz pulser: ch15 100 kHz clock: ch16.

Four ribbon cables from the back side of the 1st, 2nd, 3rd and 4th NIM-ECL modules go to a Fastbus TDC (top 4 positions).

2