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INTRODUCTION

Jefferson Lab is a Department of Energy facility operated by the Southeastern Universities Research Association (SURA) involved in highly sophisticated research to study the fundamental properties of sub-atomic matter. The responsibility for designing and documenting electrical and electronic wiring diagrams, chassis, layouts, circuit boards, etc. for the Accelerator, the Free Electron Laser (FEL), and other major systems for which Jefferson Lab is responsible, rests with the EE CAD Group in the Accelerator Engineering Department.

This plan is designed to identify the framework for improved group operations, continuous process improvement, and quality customer service. It will be reviewed in six months (Jan 2004) by the Group to evaluate its implementation and make any necessary revisions. It will thereafter be reviewed annually.

MISSION & GOALS

Mission: Our mission is to produce accurate and well-documented electrical, electronic, and electro-mechanical designs, as well as documents from designs, for a variety of JLab customers

Goals: To perform our mission, the Group strives to reach seven major goals:

Goal 1. Maximize technical design skills through training
Goal 2. Keep software current and distributed
Goal 3. Integrate customers into a visible and standardized process
Goal 4. Define what constitutes an acceptable job
Goal 5. Utilize well-managed outsourcing to supplement in-house resources
Goal 6. Establish realistic documentation standards and apply them consistently and equitably
Goal 7. Maintain useful work metrics

OBJECTIVES

Objectives: Each of our goals has a subordinate set of objectives for at least the next year. If we meet all of the objectives under a goal, we will have reached that goal.

Goal 1. Maximize technical design skills through training (Appendix C)

1.1 Identify qualifications and skills required for designers
1.1.1 Define ME qualification
1.1.2 Define EE qualification
1.2 Assess designers’ current skill level against requirements
1.2.1 Identify gaps
1.3 Identify training programs that teach required skills
1.4 Create development plans for each designer to address gaps with appropriate training
1.5 Create PCAD training program for customers

Goal 2. Keep software and hardware current and distributed

2.1 Upgrade or replace at least 2 of the designers’ oldest and/or least capable CPUs/RAM each year
2.2 Manage PCAD licensing for customers
2.2.1 Default permission level for non-EECAD people is “schematic”
2.2.2 Create permanent PCAD seats for all appropriate EECAD designers
2.3 Manage AutoCAD licensing for site wide customers
2.3.1 Maintain a list of current network and stand alone users
2.3.2 Create/maintain permanent AutoCAD seats for all appropriate EECAD designers

Goal 3. Integrate customers into a visible and standardized process

3.1 Manage single Library used by all
3.1.1 Work with customers to identify additional Library entries not now in the EE CAD Library
3.1.2 Update customers’ computers daily with EE CAD master libraries
3.1.3 Update customers’ computers with EE CAD standard formats
3.1.4 Require that the Library be used for all design work, including work done by customers
3.1.5 Create Library Manager role in EE CAD

Goal 4. Define what constitutes an acceptable job
4.1 All requests must include:
- A clear and legible statement of what is needed
- Pertinent mounting/installation information
- Where the job will fit into a larger system and what it will do
- Desired Delivery Date
- Point(s) of contact with email and phone
- An indication of whether outsourcing is acceptable if it cannot be done in-house
- A particular designer may be requested under certain circumstances

4.2 Requests for mechanical design work must also include:
- A complete bill of materials
- Information on any new components
- A sketch or sample ("as built")

4.3 Requests for electrical/electronic design work must also include:
- Information on any new components
- A schematic with critical nets defined and associated rules to be applied to the nets
  - If the schematic is submitted electronically, provide a library, use at least a 25 mil grid (a smaller grid will add time to the job), and use B or C size sheets (larger sheets will add time to the job)

4.4 PC Board requests must also include:
- Board size
- Preferred number of layers and stacking
- Any critical component placements
- Whether to use through-hole or surface mount
- Any special fabrication information (e.g. "no solder mask" areas, special plating)

4.5 Requests for any other type of job must include a sketch

Goal 5. Utilize well-managed outsourcing to supplement in-house resources
5.1 Publish criteria for doing electrical design work for JLab in the form of a Statement of Work (SoW)
5.2 Work with Procurement to identify
  5.2.1 vendors for outsourcing selected jobs
  5.2.2 contractors who can be brought in to work on overflow
5.3 Manage contracted work to same standard as in-house work

Goal 6. Establish realistic documentation standards and apply them consistently and equitably
6.1 Provide storage and file structure for design documentation
6.2 Publish regular reports to Management
  6.2.1 status of design projects finished vs documented
  6.2.2 turnaround times for check prints and vellums by customer
6.3 Put due date (two weeks from delivery date) on check prints and email customers who have had them beyond due date (copy to their supervisor and designer)
6.4 Email customers who have had vellums for more than two weeks (copy to their supervisor and designer)
6.7 Deliver all vellums to a person for approval; don’t drop them off

Goal 7. Maintain useful work metrics
7.1 Designers estimate work in hours for every job
7.2 Designers log their work (contract manager logs time, cost for contracted jobs) by customer, project type, name – not for external consumption
  7.2.1 log time expended on
    - research (ME & EE projects)
    - preparation (ME projects)
    - generating schematic (EE projects & block diagrams/system prints)
    - design work (ME, EE projects & block diagrams/system prints)
    - customer/contractor interface (ME, EE projects & block diagrams/system prints)
  7.2.2 log events
    - date job into Group
    - date designer starts work

3
7.3 Job Control compile weekly and monthly summaries of all work—in-house or outsourced
7.3.1 by customer for external distribution to customers’ managers
7.3.2 by designer (including original estimates) for internal use

**ASSETS**

**Management Structure:** Management is considered an asset as it provides the operational framework for the Group, its resources, and its leadership.

Lab Director: Christopher Leemann  
Accelerator Division Director: Swapan Chattopadhyay  
Accelerator Engineering Group Leader: Will Oren  
Electrical Systems Manager: Bill Merz  
Electrical Systems Deputy: Ron Lauze’  
EE Computer Assisted Design (CAD) Group Leader: Roger Flood

**Equipment:** The Group’s equipment consists almost completely of computer hardware and software. See Appendix F for a current inventory.

The Group’s designers should never be limited by the capabilities of their hardware. The hardware replacement plan attempts to provide every designer with hardware capable of using the full capabilities of the software. The CPUs and RAM on at least three machines a year should be upgraded. The priority is that the two oldest and/or least capable machines are upgraded first. Machines that are replaced are considered as server replacements before they are declared excess.

**Staff:** The staff is currently composed of five regular employees, a term employee, a student, a full-time contractor, and a part-time casual employee.

**Designers:** Roger Flood  
Lori Zukerman  
Ismael (Mel) Gonzales  
David Williams  
Kim Ryan  
Gordon Baker – on loan from Mechanical Engineering (Term)  
Miriam Angello – Student Intern  
Kevin Arenius – Contractor

**Technician:** Bob Vignato – Retired (Part time Casual)
METHOD OF OPERATION

**Work Flow:** The general operation of the EE CAD Group consists of customers bringing work, either electronically using our Web-based work request form, or in hard copy. We accept work in any form as long as it meets all of our acceptance criteria. Generally, customers bring either a design that they want us to refine, alter, etc. or an idea that they want us to turn into a design. See Appendix B.

All work is logged in by one of our “work coordinators” who must first ensure it meets all of our acceptance criteria (Appendix E). The request must provide a clear statement of what is needed as the end product as well as if (and how) it will fit into a larger system. The customer may include a request that a particular designer work on the project if, for example, the customer and designer have worked on a similar project together previously. The customer must also indicate if he/she is willing to have the job outsourced if the EE CAD staff cannot handle it. Most importantly, all acceptable jobs must include a usable schematic with mounting/installation information if applicable, a good bill of materials, and a library.

Once a job is accepted, the work coordinator assigns it a priority and a project designer, and enters that information into the log. Priorities are based on what program/department category the job is for. The categories are:

- EE AC Systems
- EE DC Systems
- EE I&C Systems
- EE Support Services
- EE RF Systems
- Physics (Halls, experiments)
- SRF Institute
- Safety Systems
- Injector (Source)
- SNS
- FEL

The relative importance of these program/department categories changes often, so the work coordinators must be aware of changes and adjust priorities accordingly.

Selection of a project designer is based on three things. First, which designers have the technical expertise to carry out the job; second, which qualified designers have workloads that can support it; and third, is the designer requested by the customer one of these? If there is no designer who fits the first two criteria and the customer has not ruled it out in the work request, the job will be outsourced.

**Roles and Responsibilities:** The roles of the EE CAD stakeholders, as well as their associated responsibilities, are listed in Appendix D. All but one of the Group’s current staff is a designer, and most of these either are, or aspire to be, qualified for both electrical/electronic work and for the more mechanical work that is involved with electrical system design. Therefore, with the exception of Job Control, the staff’s responsibilities, and the skills associated with them, are cumulative—from basic skills that apply to all, to skills that are unique to one person.

**General Design Responsibilities:**

All designers have certain basic responsibilities in common:

- Possess basic drafting skills
- Familiarity with computer-assisted design
- Understand and follow group work practices per business plan
- Proficient in basic use of Windows MS Office suite
- Ability to plan and estimate work
- Ability to communicate effectively for successful results

Designers who do mechanical work have additional responsibilities:

- Understand mechanical concepts of tolerance, loading, and dimensioning
- Recognize mechanical symbology
- Understand the mechanical fabrication process
- Proficiency in AutoCAD

Designers who do electrical/electronic work (PC boards) have similar (but not identical) responsibilities:
• Understand electrical/electronic concepts of tolerance, loading, and dimensioning
• Recognize electrical/electronic symbology
• Understand the electrical/electronic fabrication process
• Proficiency in PCAD

**Position-specific Responsibilities:**

In addition to being a Work Coordinator, the **Group Leader**
• Communicates group needs to Management on training, space, manpower, budget
• Assesses customer satisfaction
• Makes sound outsourcing decisions
• Manages group budget
• Exercises supervisory & managerial responsibilities

Finally, the individual running **Job Control** has only these responsibilities:
• Manage log system
• Keep database current & accurate
• Design and produce reports from database
• Follow up on documentation
• Review/mark up/correct minor engineering change orders

**Additional Duty Responsibilities:**

In addition to being a qualified EE and/or ME designer, the **Work Coordinators** also:
• Are proficient in planning and estimating work
• Understand changing Lab priorities
• Set internal priorities to match Lab priorities
• Know other designers’ capabilities & development needs
• Know how to deal with difficult customers/situations

In addition to being a qualified EE designer, the **Library Manager** is also responsible for
• Being proficient in computer files management
• Refreshing customers’ computers with library updates

In addition to being a designer qualified in both EE and ME work, the **Software Manager** also has additional responsibilities:
• Manage license(s)
• Assign permissions and seats
• Track usage
• Upgrade hardware and software

In addition to being either a qualified EE or ME designer, the **Final Checkers** also must:
• conscientiously check others’ work
• be available for monthly rotation

For example, all designers, including the student, have the six common responsibilities and are expected to have the skills associated with them (Appendix C). A designer who does only mechanical or only electrical/electronic work has these six common responsibilities plus the four that are unique to their specialty; totaling ten. Designers who do both mechanical and electrical/electronic work will have the common 6 plus all 8 of the specialized responsibilities; a total of 14.

The 5 work coordinator responsibilities fall on qualified designers who already have either 10 or 14 responsibilities as designers. The Group Leader, who is one of the work coordinators, has an additional set of unique responsibilities that brings his total into the twenties.

Additional functions like Library Manager and Software Manager add unique responsibilities to the totals of the designers assigned to perform them.

Since Job Control is not run by a designer, the responsibilities of this function are both unique and complete unto
themselves.

We recognize that customers too have their part to play in how well we can serve them. We serve our customers and the Lab best when customers adhere to their responsibilities:

- Provide work requests that meet the Group’s published acceptance criteria
- Indicate designer preference & if outsourcing is OK
- Log off software seat when not using it
- Return check prints promptly
- Sign vellums promptly
- Provide courteous support to designers assigned to their work

Professional Development

Skills Assessment: Each position in the Group is associated with one or more qualifications (sets of related skills). These are listed in Appendix C along with the proficiency level required for the position. Annually, the Group Leader will assess the actual proficiency of the employees filling each position against what is required. Any “gaps” that are identified are documented in the employee’s performance appraisal as expectations for the coming year.

Training: The individual group member and the Group Leader work with the Training and Performance Office to identify ways to close the skill gap and help the employee meet the related performance objective. Formal short courses, both on and off site, may be supplemented by online training, mentoring, OJT, reading published materials, conferences, etc. and are funded by the Group.

Standards of Performance

Product Quality: Our highest priority is to produce designs that meet or exceed customer expectations, that work when they are fabricated and installed, and that are well-documented so they can be reproduced or modified with as little effort and as much accuracy as possible.

Customer Service: Customer service is essential and is our highest priority after product quality. Efforts to satisfy our customers include but are not limited to:

- Timeliness
- Adherence to specifications
- Flexibility within guidelines of Business Plan
- Keeping customer informed
- Requesting feedback on completed work

Metrics: “If it doesn’t get measured, it doesn’t get done.” That quote may be a little extreme, but it often isn’t far from the truth. We measure our own performance in a variety of ways and track our customers’ interactions with us as well. Job Control takes the data provided by the designers, turns it into reports that can improve performance, and ensures it gets into the right hands. We log two kinds of internal data and produce two regular reports.

Logs: Jobs are logged in when they are received by a work coordinator and logged out by the designer (or contract manager for outsourced work). However, we also log several milestones in between. The work coordinator logs the date the job is assigned to a designer or is outsourced. The designer logs the date that check print(s) go out and when they come back; when the job goes out for internal checking and when the check is complete; and when the vellum is taken to the customer and when it is signed. Designers also track the hours that go into each step in a job:

- research (ME & EE projects)
- preparation (ME projects)
- generating schematic (EE projects & block diagrams/system prints)
- design work (ME, EE projects & block diagrams/system prints)
- customer/contractor interface (ME, EE projects & block diagrams/system prints)

Reports: Job Control takes these logs, as well as the estimates the designers submit when they first get a job, and produces a variety of periodic reports.
<table>
<thead>
<tr>
<th>Data Reported; Primary Sort</th>
<th>Frequency</th>
<th>Produced for</th>
</tr>
</thead>
<tbody>
<tr>
<td>Estimates vs actual time; by designer</td>
<td>quarterly</td>
<td>Internal use only</td>
</tr>
<tr>
<td>Estimates vs actual time; by customer</td>
<td>quarterly</td>
<td>Internal use only</td>
</tr>
<tr>
<td>Estimates vs actual time; by design process step</td>
<td>quarterly</td>
<td>Internal use only</td>
</tr>
<tr>
<td>Turnaround time for check prints; by customer</td>
<td>monthly</td>
<td>Flood, Lauze’, Merz, W. Oren</td>
</tr>
<tr>
<td>Turnaround time for vellums; by customer</td>
<td>monthly</td>
<td>Flood, Lauze’, Merz, W. Oren</td>
</tr>
<tr>
<td>Job requests rejected; by customer</td>
<td>quarterly</td>
<td>Flood, Lauze’, Merz, W. Oren</td>
</tr>
<tr>
<td>Workload; by designer</td>
<td>weekly</td>
<td>Flood, Lauze’</td>
</tr>
<tr>
<td>Workload; by customer</td>
<td>weekly</td>
<td>Flood, Lauze’</td>
</tr>
<tr>
<td>Workload; by program/project category</td>
<td>weekly</td>
<td>Flood, Lauze’</td>
</tr>
</tbody>
</table>

**Communication:** Communication is essential to maintaining a pleasant and productive workplace. The most important goal when communicating is for both parties to ensure the receiver understands the message the way the sender intended.

1. Concerns about safety, environment, health, work processes, or other work related issues should be brought to the attention of a Work Coordinator.
2. Concerns or questions about difficult customer interactions, personnel issues, office rules & practices, needed resources, or intra-group relations should be brought to the attention of the Group Leader.
3. Concerns will be resolved and questions answered in a timely fashion. Issues will not be left unresolved except with the agreement of all parties involved.
4. No idea will be dismissed out of hand. Decisions that affect others will be made only after those affected are consulted (except when restricted by statute or policy). However, once a decision is made and those affected are informed, everyone will both follow it and support it.
5. Disagreements between group members should first be discussed between the principals, and only elevated if unresolved. In most cases, personal information regarding group members and their interaction should be kept within the group.
6. The most important consideration in any decision is its effect on the group’s ability to perform its mission. Group members must always ask themselves if what they want to do will improve their ability to accomplish the mission. For example, if it will help them personally but reduce service to the customer, it is not likely to be supported.

**Office Rules:** These mutually agreed upon rules apply to everyone in the EE CAD Group; however, non-exempt (hourly) employees may not work more than 40 hours per week without approval of overtime by the Group Leader.

1. Work Hours:
   - Start: 7:00 am
   - Break: 10 minutes any time
   - Lunch: 1 hour between 11:00 am and 1:00 pm
   - Break: 10 minutes any time
   - Quit: 4:00 pm

2. Staff member absences during the day
   In cases of prolonged absence, computers must be locked while unattended

3. Staff member vacation planning
   Requests for time off are required as far in advance as the total vacation time requested. For example one week notice for one week of vacation, unless an unexpected circumstance or emergency arises.

4. End of day close-down
   All files will be saved, applications closed, and computers either shut down, rebooted, or locked before a Group member departs for the day.

5. Logging
   - All work will be logged in the Group Electronic Log
• No work will be performed on Lab time that is not logged
• Log entries for work estimates will be made no later than one day after the job is assigned
• Log entries for actual hours worked may be collected in any form but, to keep Group-wide data current, hours must be entered into the Group Electronic Log no later than one day after a project milestone (e.g. Research, Preparation, Preparing Schematic) has been completed
• You are not expected to account for all your work hours in the Log (i.e. Log totals for any designer are not realistically expected to add up to eight hours each day).

6. Telephones and email

• We are a customer service organization. Calls should be answered when they are first received.
• If some work-related activity precludes answering the phone when it rings, check voicemail and return calls within one hour after becoming available.
• Do not let the day end with a voicemail from a customer unanswered, or an email from a customer unacknowledged. Even if you don’t have the answer, let them know you’re aware of their question.

7. Steps for Group Leader to follow to resolve issues

• Review the issue
• Investigate all aspects of the situation
• Try to resolve as soon as possible
• Make appropriate decisions, communicate what needs to be done with all parties involved, and ask if they understand and will comply

8. Steps for noncompliance *

• Verbal Warning
• Written Warning
• Written Notice
• Disciplinary Suspension

* It depends upon the nature of the non-compliance: e.g. safety violations, physical assault, etc., would result in immediate disciplinary suspension
<table>
<thead>
<tr>
<th>Division/Department (Contacts)</th>
<th>Types of Work Requested</th>
</tr>
</thead>
</table>
| **Accelerator EES** (E. Martin, W. Merz) | AC voltage monitoring  
RF/DC-related Hi-V and current read-backs  
Analog Block re-design  
Wiring diagrams  
System prints |
| **Accelerator EESRF** (W. Woodworth, R. Terrell, R. Nichols, T. Powers, C. Cox, T. Plawski, H. Dong, S. Cooper) | High-speed multi-layer schematic designs, layouts, and library support  
Simple thru-hole two to four layer PCBs  
System Prints  
Wiring Diagrams  
Chassis  
Mixed digital & RF controlled impedance  
Multi-layer PCBs |
| **Accelerator SRF** (W. Sommer, J. Gordon,) | Simple thru-hole two to four layer PCBs  
System Prints  
Wiring Diagrams  
Chassis |
| **Accelerator Safety Systems** (H. Robertson, J. Jefferson) | Documentation  
Wiring, block, and system diagrams  
Site equipment layouts  
Chassis fabrication details  
Mechanical parts detailing |
Wiring Schedule  
Wiring, block, and system diagrams  
Chassis fabrication  
Mechanical parts detailing  
Schematics  
Medium complexity four layer mixed surface mount & thru-hole vacuum system PCBs  
Front panel high-speed multi-layer schematic designs  
Layouts  
Library support  
Enclosure designs  
Assembly & fabrication |
| **Accelerator EESDC** (M. Augustine, C. Seaton, D. Gelhaar, T. Seeberger, S. Wood) | Schematics  
Simple thru-hole two to four layer PCBs  
Wiring diagrams  
System prints |
| **Accelerator Injector Group** (J. Hansknecht) | Wiring and block diagrams  
PCBs  
Front panels |
Chassis wiring diagrams & fabrication  
Mechanical parts detailing  
Schematics  
Simple thru-hole two to four layer PCBs |
| **Physics—Liaison and Hall C** (H. Areti, C. Yan, N. Sinkine) | Special projects related to Injector and Hall support  
Engineering design and PCB layouts |
<table>
<thead>
<tr>
<th>Company/Individual and Contact Information</th>
<th>Services/Products Provided</th>
</tr>
</thead>
</table>
| **Sierra Proto Express**  
(800)763-7503  
Attn: Doc Walker  
https://www.2justforyou.com/NASApp/sierraproject/jsp/tabs_welcome_home.jsp | Wide variety of products from simplest 2-layer boards to complex multi-layer PCBs |
| **PCSM**  
115 Jack Guynn Drive  
Galax, VA 24333  
276.236.4921  
www.pcsmpcb.com | A prototype, low volume quick turn PCB manufacturer in Southwest VA. They can produce up to 24 layer boards and have the capabilities to do the following: Controlled Impedance, Blind & buried vias, BGA, SMT & Micro Via designs. |
| **Phoenix Designs**  
4057 Darling Court  
Lilburn, GA 30047  
770.923.3465  
Process: acceptance to delivery

Job request received by Work Coord

Goal 4 criteria met?

Y

Out-source?

N

Return to customer

Find contractor

Log entry

Inform customer

Log entry

Designer contacts customer

Log entry

Estimate to Bob

Job stats to Bob

Bob publishes weekly stats

Log entry

Y

N

Customer response?

Log entry

OK

None for 2 weeks

Email to customer w/ cc to supr

Log entry

B

N

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry

Log entry
<table>
<thead>
<tr>
<th>Qualification</th>
<th>Required Skill</th>
<th>Prof*</th>
<th>Method</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic Designer</td>
<td>Perform basic drafting tasks</td>
<td>Able</td>
<td>College/VoTech courses</td>
</tr>
<tr>
<td></td>
<td>Be familiar with CAD concepts</td>
<td>Able</td>
<td>College/VoTech courses</td>
</tr>
<tr>
<td></td>
<td>Understand and follow group work practices outlined in Business Plan</td>
<td>Able</td>
<td>Read Business Plan</td>
</tr>
<tr>
<td></td>
<td>Maintain proficiency in basic use of MS Windows Office suite</td>
<td>Able</td>
<td>Application-specific courses</td>
</tr>
<tr>
<td></td>
<td>Effectively plan and estimate work</td>
<td>Able</td>
<td>Experience, OJT</td>
</tr>
<tr>
<td></td>
<td>Communicate successfully with customers</td>
<td>Able</td>
<td>Short courses in Communication, Interpersonal Skills, Human Behavior</td>
</tr>
<tr>
<td>EE Designer</td>
<td>Understand electrical/electronic concepts of tolerance, loading, dimensioning</td>
<td>Able</td>
<td>College/VoTech courses, experience, OJT, published material</td>
</tr>
<tr>
<td>Requires Basic Designer qualification</td>
<td>Recognize electrical/electronic symbology</td>
<td>Able</td>
<td>College/VoTech courses, experience, OJT, published material</td>
</tr>
<tr>
<td></td>
<td>Understand the electrical/electronic fabrication process</td>
<td>Able</td>
<td>Experience, OJT, published material</td>
</tr>
<tr>
<td></td>
<td>Use PCAD to produce design projects</td>
<td>Able</td>
<td>Application-specific courses</td>
</tr>
<tr>
<td>ME Designer</td>
<td>Understand mechanical concepts of tolerance, loading, dimensioning</td>
<td>Able</td>
<td>College/VoTech courses, experience, OJT, published material</td>
</tr>
<tr>
<td>Requires Basic Designer qualification</td>
<td>Recognize mechanical symbology</td>
<td>Able</td>
<td>Experience, OJT, published material</td>
</tr>
<tr>
<td></td>
<td>Use AutoCAD to produce design projects</td>
<td>Able</td>
<td>Application-specific courses</td>
</tr>
<tr>
<td>Library Manager</td>
<td>Set up and manage computer files effectively</td>
<td>Able</td>
<td>Course in MS Windows</td>
</tr>
<tr>
<td>Requires EE Designer qualification</td>
<td>Be able to update connected computers with current libraries</td>
<td>Able</td>
<td>OJT</td>
</tr>
<tr>
<td>Software Manager</td>
<td>Know how to keep licenses current</td>
<td>Able</td>
<td>Tutorial, published material</td>
</tr>
<tr>
<td></td>
<td>Assign permissions and seats correctly</td>
<td>Able</td>
<td>Tutorial, Published material, OJT</td>
</tr>
<tr>
<td></td>
<td>Track usage accurately</td>
<td>Able</td>
<td>Tutorial, Published material, OJT</td>
</tr>
<tr>
<td></td>
<td>Upgrade hardware and software in a timely manner</td>
<td>Able</td>
<td>Published material</td>
</tr>
<tr>
<td>Work Coordinator</td>
<td>Be able to plan and estimate work</td>
<td>Able</td>
<td>Experience, OJT, Project Management course</td>
</tr>
<tr>
<td>Requires EE Designer qualification</td>
<td>Understand changing Lab priorities and be able to set internal priorities to match them</td>
<td>Able</td>
<td>Lab reports, publications, documents, meetings, announcements</td>
</tr>
<tr>
<td></td>
<td>Know designers' capabilities &amp; development needs</td>
<td>Able</td>
<td>Experience</td>
</tr>
<tr>
<td></td>
<td>Know how to deal with difficult customers/situations</td>
<td>Able</td>
<td>Short course</td>
</tr>
<tr>
<td>Group Leader</td>
<td>Be able to solicit and accurately assess customer satisfaction</td>
<td>Able</td>
<td>Short course</td>
</tr>
<tr>
<td>Requires Work Coordinator qualification</td>
<td>Effectively communicate group needs to Mgt on training, space, manpower, and budget issues</td>
<td>Able</td>
<td>Short course in Negotiating, mentoring with successful manager</td>
</tr>
<tr>
<td></td>
<td>Make sound outsourcing decisions</td>
<td>Able</td>
<td>JLab pubs, mentoring with Procurement</td>
</tr>
<tr>
<td></td>
<td>Know Lab budget and accounting processes well enough to manage the group's budget effectively</td>
<td>Able</td>
<td>JLab publications, mentoring with CFO staff</td>
</tr>
<tr>
<td></td>
<td>Apply specific knowledge and abilities required of a supervisor at JLab</td>
<td>Able</td>
<td>Short courses, Supervisor’s “How To” Guide</td>
</tr>
</tbody>
</table>
* Proficiency levels:

1 – Unskilled (no knowledge or ability)

2 – Minimally Able (aware of skill, can demonstrate some knowledge, but demonstrates little ability to use it)

3 – Able (Fully able to use the skill effectively)

4 – Teach (So proficient in the skill that he/she can teach it to others)
### Student Intern

**Roles**
- Use basic skills to produce simple design products
- Learn professional and JLab processes and practices
- Assist other Group members

**Responsibilities**
- Possess basic drafting skills
- Be familiar with computer-assisted design
- Understand & follow group work practices in Business Plan
- Maintain proficiency in basic use of MS Windows Office suite
- Effectively plan and estimate work
- Communicate successfully with customers

### ME Designer

**Roles**
- Use advanced skills to produce a variety of mechanical design products
- Work with customers to meet or exceed their expectations

**Responsibilities**
- Understand the concepts of tolerance, loading, and dimensioning as they apply to mechanical design
- Recognize mechanical design symbology
- Understand the mechanical fabrication process
- Maintain proficiency in AutoCAD

### EE Designer

**Roles**
- Use advanced skills to produce a variety of electrical and electronic design products
- Work with customers to meet or exceed their expectations

**Responsibilities**
- Understand the concepts of tolerance, loading, and dimensioning as they apply to electrical/electronic design
- Recognize electrical/electronic design symbology
- Understand the electrical/electronic fabrication processes
- Maintain proficiency in PCAD

### Work Coordinator/Software Manager

**Roles**
- Manage software licenses assigned to the Group
- Assign work to designers

**Responsibilities**
- Know requirements of software license(s)
- Equitably manage the assignment of permissions and seats
- Track usage accurately
- Distribute hardware and software upgrades in a timely manner
- Understand (and have access to) changes in Lab priorities
- Set internal priorities to match Lab priorities
- Plan, assign, and estimate work based on
  - Current program priorities (SNS, FEL, CEBAF, etc.)
- Designers’ capabilities & development needs
- Customer-designer experience and rapport
- Deal effectively with difficult customers/situations

### Library Manager

<table>
<thead>
<tr>
<th>Role</th>
<th>Responsibilities</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maintain master component libraries</td>
<td>Be proficient in computer files management</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Library Manager</th>
<th>Maintain master component libraries</th>
<th>Be proficient in computer files management</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Keep master libraries current and complete</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Update customers’ computers with master libraries</td>
</tr>
</tbody>
</table>

### Job Control

<table>
<thead>
<tr>
<th>Roles</th>
<th>Responsibilities</th>
</tr>
</thead>
<tbody>
<tr>
<td>Manage Electronic Log system</td>
<td>Keep database current &amp; accurate</td>
</tr>
<tr>
<td>Follow up on documentation</td>
<td>Design and produce recurring and ad hoc reports from database</td>
</tr>
<tr>
<td>Support full-time designers</td>
<td>Send reminder emails to customers and supervisors with late check prints or vellums</td>
</tr>
<tr>
<td></td>
<td>Review and mark up minor change orders</td>
</tr>
</tbody>
</table>

### Group Leader

<table>
<thead>
<tr>
<th>Roles</th>
<th>Responsibilities</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lead the Group according to the Business Plan</td>
<td>Communicate group needs to Management on training, space, manning, budget</td>
</tr>
<tr>
<td>Ensure customers’ needs are being met or exceeded</td>
<td>Assess customer satisfaction</td>
</tr>
<tr>
<td>Make decisions on outsourcing</td>
<td>Make sound outsourcing decisions</td>
</tr>
<tr>
<td></td>
<td>Manage group’s funds</td>
</tr>
<tr>
<td></td>
<td>Manage group members’ performance (supervisory &amp; managerial duties)</td>
</tr>
</tbody>
</table>

### Customers

<table>
<thead>
<tr>
<th>Role</th>
<th>Responsibilities</th>
</tr>
</thead>
<tbody>
<tr>
<td>Develop parts, components, and systems for use in the Accelerator, FEL, etc.</td>
<td>Use EE CAD for all designs that will be installed in JLab systems</td>
</tr>
<tr>
<td></td>
<td>Provide work requests that meet EE CAD criteria</td>
</tr>
<tr>
<td></td>
<td>Include proper documentation (users manual, specs on new parts, etc.) as part of design</td>
</tr>
<tr>
<td></td>
<td>Log off software seat when not using it</td>
</tr>
<tr>
<td></td>
<td>Return check prints within 2 weeks</td>
</tr>
<tr>
<td></td>
<td>Sign vellums within 2 weeks</td>
</tr>
</tbody>
</table>

16
Acceptance Criteria include the criteria for accepting a job and the standards for documenting the resulting product.

New Jobs

The EE CAD Group will accept new jobs electronically or in hard copy as long as they meet the following criteria. The preferred method for submitting a work request is the Web-based Work Request Form (https://www1.jlab.org/ul/apps/eecad/submit.cfm) which you can fill out yourself or have a member of the Group help fill it out for you.

All requests must include:
- A clear and legible statement of what is needed
- Pertinent mounting/installation information
- Where the job will fit into a larger system and what it will do
- Desired Delivery Date
- Point(s) of contact with email and phone
- An indication of whether outsourcing is acceptable if it cannot be done in-house

In addition, you may request a particular designer if that person has experience working with you on this type of job.

Additional requirements

Requests for mechanical design work must also include:
- A complete bill of materials
- Information on any new components
- A sketch or sample ("as built")

Requests for electrical/electronic design work must also include:
- Information on any new components
- A schematic with critical nets defined and associated rules to be applied to the nets
  - If the schematic is submitted electronically,
    - Provide a library
    - Use at least a 25 mil grid (a smaller grid will add time to the job)
    - Use B or C size sheets (larger sheets will add time to the job)

PC Board requests must also include:
- Board size
- Preferred number of layers and stacking
- Any critical component placements
- Whether to use through-hole or surface mount
- Any special fabrication information (e.g. “no solder mask” areas, special plating)

Requests for any other type of job must include a sketch.
Hardware

Computers:

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. P4- 1.8GHz 526M RAM (Vignato)</td>
</tr>
<tr>
<td>2. P4- 2.8GHz 1G RAM (Williams)</td>
</tr>
<tr>
<td>3. P4- 2.2GHz 1G RAM (Baker)</td>
</tr>
<tr>
<td>4. P4- 1.7GHz 512M RAM (Arenius)</td>
</tr>
<tr>
<td>5. P4- 3.2GHz 1G RAM (Ryan)</td>
</tr>
<tr>
<td>6. P4- 2.8GHz 1G RAM (Gonzales)</td>
</tr>
<tr>
<td>7. Apple Macintosh (Ryan)</td>
</tr>
<tr>
<td>8. P4- 1.5GHz 384M RAM (Angello)</td>
</tr>
<tr>
<td>9. P4- 2.8GHz 1G RAM (Flood)</td>
</tr>
<tr>
<td>10. P4- 2.5GHz 768M RAM (Zukerman)</td>
</tr>
<tr>
<td>11. 486 machine ??M RAM (Gonzales)</td>
</tr>
<tr>
<td>12. P4- 1GHz 512M RAM--PCAD Full suite server</td>
</tr>
<tr>
<td>(Zukerman)</td>
</tr>
<tr>
<td>13. P3- 850MHz 384M RAM—ACAD &amp; PCAD Schematic</td>
</tr>
<tr>
<td>suite server (Zukerman)</td>
</tr>
<tr>
<td>14. P1-133MHz laptop 24M RAM (Zukerman)</td>
</tr>
<tr>
<td>15. P4 Toshiba laptop (Flood)</td>
</tr>
</tbody>
</table>

Monitors:

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. 17&quot; CRT (Vignato)</td>
</tr>
<tr>
<td>2. 20&quot; Flat Panel (Zukerman)</td>
</tr>
<tr>
<td>3. 20&quot; Flat Panel (Zukerman)</td>
</tr>
<tr>
<td>4. 20&quot; Flat Panel (Williams)</td>
</tr>
<tr>
<td>5. 20&quot; Flat Panel (Williams)</td>
</tr>
<tr>
<td>6. 20&quot; Flat Panel (Ryan)</td>
</tr>
<tr>
<td>7. 20&quot; Flat Panel (Ryan)</td>
</tr>
<tr>
<td>8. Generic Apple (Ryan)</td>
</tr>
<tr>
<td>9. 20&quot; Flat Panel (Gonzales)</td>
</tr>
<tr>
<td>10. 20&quot; Flat Panel (Baker)</td>
</tr>
<tr>
<td>11. 21&quot; CRT (Angello)</td>
</tr>
<tr>
<td>12. 21&quot; CRT (Flood)</td>
</tr>
<tr>
<td>13. 21&quot; CRT (Flood)</td>
</tr>
<tr>
<td>14. 20&quot; Flat Panel (Arenius)</td>
</tr>
</tbody>
</table>

Peripherals:

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Digital Camera (Zukerman)</td>
</tr>
<tr>
<td>2. Network hub (Zukerman)</td>
</tr>
<tr>
<td>3. Belkin switch box (Zukerman)</td>
</tr>
<tr>
<td>4. Two Digitizers (Gonzales)</td>
</tr>
<tr>
<td>5. Flatbed scanner (Gonzales)</td>
</tr>
<tr>
<td>6. HP Laser Jet 4MV (Zukerman)</td>
</tr>
<tr>
<td>7. HP Laserjet 5L (Vignato)</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. AutoCAD Mechanical Desktop/Inventor V7 -</td>
</tr>
<tr>
<td>expires 5/5/04 (Williams)</td>
</tr>
<tr>
<td>2. AutoCAD Mechanical Desktop/Inventor V7-</td>
</tr>
<tr>
<td>expires 5/5/04 (Gonzales)</td>
</tr>
<tr>
<td>3. AutoCAD network seat (Baker)</td>
</tr>
<tr>
<td>4. AutoCAD network seat (Ryan)</td>
</tr>
<tr>
<td>5. AutoCAD network seat (Arenius)</td>
</tr>
<tr>
<td>6. AutoCAD network seat (Flood)</td>
</tr>
<tr>
<td>7. AutoCAD network seat (Angello)</td>
</tr>
<tr>
<td>8. AutoCAD network seat (Zukerman)</td>
</tr>
<tr>
<td>9. AutoCAD network seat (Vignato)</td>
</tr>
<tr>
<td>10. PCAD Network seat (Angello)</td>
</tr>
<tr>
<td>11. PCAD Network seat (Flood)</td>
</tr>
<tr>
<td>12. PCAD Stand Alone license (Williams)</td>
</tr>
<tr>
<td>13. PCAD Stand Alone license (Zukerman)</td>
</tr>
<tr>
<td>14. PCAD Stand Alone license (Ryan)</td>
</tr>
<tr>
<td>15. PCAD Stand Alone license (Arenius)</td>
</tr>
<tr>
<td>16. PCAD Network licenses (8 full suite, 8</td>
</tr>
<tr>
<td>schematic suite only) Version 2002 on server-</td>
</tr>
<tr>
<td>expires 9/30/04 (Zukerman)</td>
</tr>
<tr>
<td>17. AutoCAD Network Licenses (11 total)</td>
</tr>
<tr>
<td>Version 2004 on server-expires 5/5/04</td>
</tr>
<tr>
<td>(Zukerman)</td>
</tr>
<tr>
<td>18. Douglas CAD: MAC PCB design software (Ryan)</td>
</tr>
<tr>
<td>19. Adobe Acrobat v6 (Williams)</td>
</tr>
<tr>
<td>20. Adobe Acrobat v6 (Ryan)</td>
</tr>
<tr>
<td>21. Adobe Acrobat v6 (Gonzales)</td>
</tr>
<tr>
<td>22. Adobe Acrobat v6 (Zukerman)</td>
</tr>
<tr>
<td>23. Adobe Acrobat v6 (Flood)</td>
</tr>
<tr>
<td>24. Adobe Acrobat v6 (Arenius)</td>
</tr>
<tr>
<td>25. Adobe Acrobat v6 (Angello)</td>
</tr>
<tr>
<td>26. Adobe Acrobat v6 (Baker)</td>
</tr>
<tr>
<td>27. MSOffice 2000 (Williams)</td>
</tr>
<tr>
<td>28. MSOffice 2000 (Baker)</td>
</tr>
<tr>
<td>29. MSOffice 2000 (Arenius)</td>
</tr>
<tr>
<td>30. MSOffice XP Pro (Ryan)</td>
</tr>
<tr>
<td>31. MSOffice 2000 (Gonzales)</td>
</tr>
<tr>
<td>32. MSOffice 2000 (Angello)</td>
</tr>
<tr>
<td>33. MSOffice 2000 (Flood)</td>
</tr>
<tr>
<td>34. MSOffice 2000 (Zukerman)</td>
</tr>
<tr>
<td>35. MSOffice 2000 (Vignato)</td>
</tr>
</tbody>
</table>

All designers are running MS Windows 2000 except Ryan who is running MS XP Pro.