LCLS-II 4.5K Cold Box Controls Software Review

Roles and Responsibilities

For the 4.5K Cold Box PLC and Local HMI code ALATUS is responsible for defining the requirements and Jefferson Lab is responsible for the implementation and bench testing of the code.

Roles and responsibilities scope outside of this review includes downstream system checkout.

For the scope listed in the Plan and Status for Code Integration at SLAC presentation slide 14 and in general follow.

Plan:

ALATUS is responsible for the checkout (commissioning) plan. ALATUS, SLAC, and JLab staff will implement the plan. SLAC has the responsibility for providing operators during the checkout.

Code:

ALATUS continues to be responsible for defining the code requirements. It is expected that there will be changes required during functional and loop checks that will be defined by ALATUS.

Jefferson Lab will continue to be responsible the implementation of the code. JLab or SLAC staff will accomplish the editing of the code. SLAC and JLab will decide on who will edit the code based on what is best for the project and consistent with available resources.

SLAC is responsible for the Epics code implementation and changes. JLab is responsible for providing the interface document for SLAC to use for the Epics interface. This includes any changes developed during the checkout.

Hardware:

Each organization is responsible for the hardware they supplied. ALATUS is responsible for the Cold Box and all subsystems contained in the cold box. JLab is responsible for PLC and minor cabinets supplied. SLAC is responsible for the interconnecting wiring and network.

Utilities:

All utilities, air, water, helium, LN2 are SLAC responsibility.