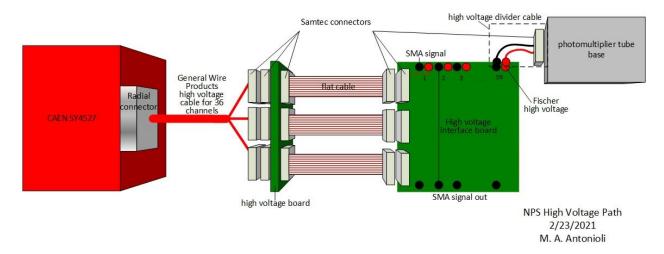
DSG-NPS R&D Meeting Minutes

Date: February 23, 2021 **Time:** 11:00AM – 12:30 PM

<u>Attendees</u>: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Reviewed HV path diagram developed by Aaron Brown and Mary Ann Antonioli



2. Hardware Interlock System development

- 2.1. Brian Eng set up the Keysight system in the DSG Control Room; connected it to the DSGCOMP10 PC on the Hall B subnet
- 2.2. Discussed contour plot made by Aaron Brown for dew point as a function of temperature and relative humidity; based on plot, dew point interlock now set at ≤ 4 °C
- 2.3. Discussed location of shielded enclosure; Aaron Brown showed diagram with two possible locations for <u>shielded enclosure</u>

3. Module Testing

- 3.1. Ramp testing of CAEN A1535 and A7435 HV modules complete 3.1.1. Modules 556 and 766 failed; these modules will not be trip tested
- 3.2. Mary Ann Antonioli will place all testing analysis plots on the DSG Hall C Technical Documentation webpage

4. Cable fabrication/testing

- 4.1. Mindy Leffel completed five Radiall-to-SAMTEC-connectors HV cables
- 4.2. The spare HV board and flat cables from IPN Orsay have cleared U.S. Customs and should arrive at JLab within a week
- 4.3. Marc McMullen is developing test chassis for Radiall-to-SAMTEC-connectors HV cables 4.3.1. Discussed AutoCAD PCB schematic showing safety loop routed through all three SAMTEC connectors