

DSG-NPS R&D Meeting Minutes

Date: November 23, 2021

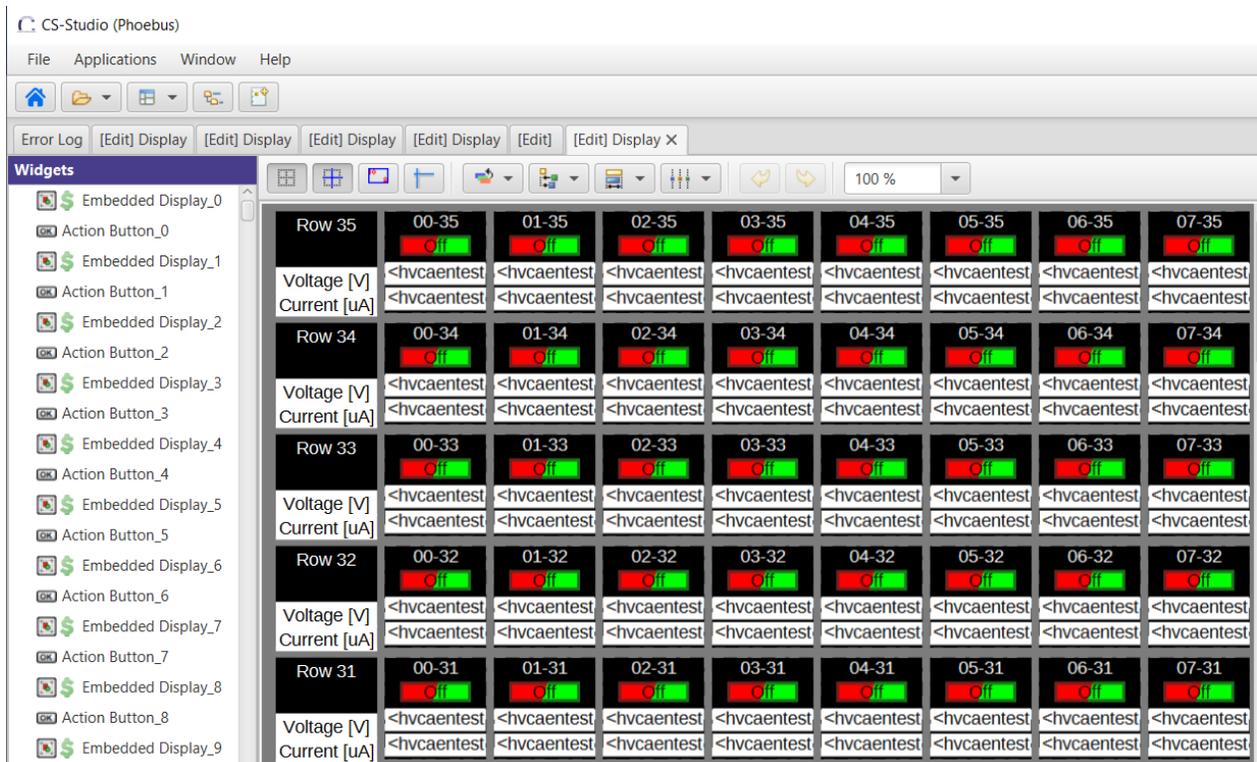
Time: 11:00 AM – 12:00 PM

Attendees: Aaron Brown, Pablo Campero, Mindy Leffel, Tyler Lemon, and Marc McMullen

1. Hardware Interlock System Development

Mary Ann Antonioli, Peter Bonneau, Aaron Brown

1. Reviewed *Hall C NPS* Phoebus screen
 - Screen features an embedded display for each PMT channel with a power status LED and readback voltage and current
 - Each embedded display is overlaid with a transparent action button that when clicked opens a pop-up window for PMT status

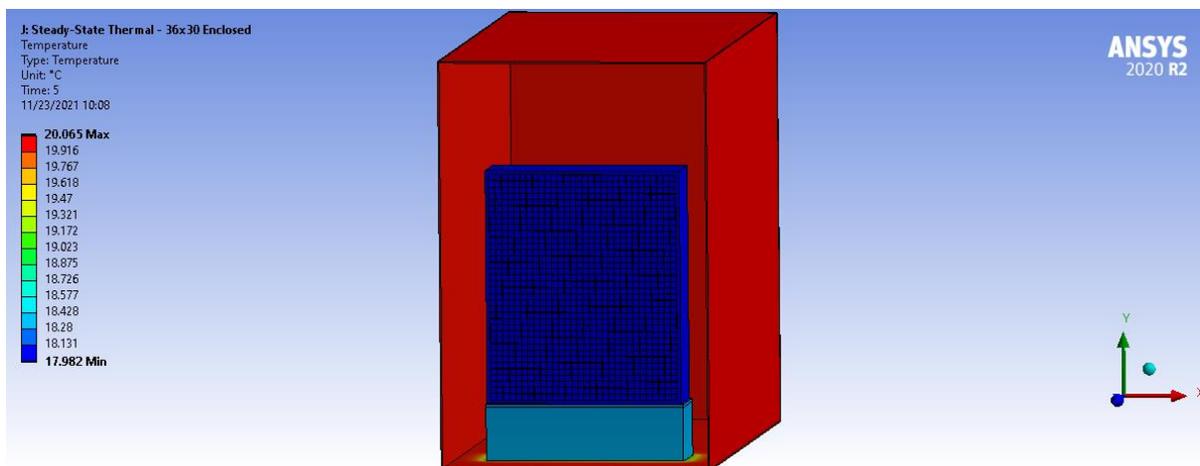


Screenshot of *Hall C NPS* Phoebus screen

2. Ansys Thermal Analysis

Aaron Brown

1. Reviewed thermal simulation with a model of the 36x30 PbWO₄ crystal array in a polyethylene box
 - Outside of enclosure the ambient temperature was set to 22°C; inside of enclosure to 18°C
 - Maximum crystal temperature: 18°C (ambient temperature inside of enclosure)



Screenshot of 36x30 PbWO₄ crystal array enclosed in polyethylene box

3. ESR Pre-shaping Progress

George Jacobs and Mindy Leffel

1. 320 of 600 foils completed (~53%)