

NPS Collaboration Meeting Minutes

Date: October 28, 2021

Time: 09:00AM – 10:05AM

Attendees: Arshak Asaturyan, Fernando Barbosa, Vladimir Berdnikov, Aaron Brown, Rolf Ent, Tanja Horn, Charles Hyde, Steven Lassiter, Paulo Medeiros, Hamlet Mkrtchyan, Carlos Munoz, Julie Roche, Brad Sawatzky, Petr Stepanov, Vardan Tadevosyan, and Bogdan Wojtsekhowski

1. High Voltage Dividers

Fernando Barbosa

1. Discussed grounding options for low voltage connector for amplifier on high voltage divider PCB

2. Low Voltage Cabling/Grounding

Carlos Munoz, Brad Sawatzky

1. Additional PCBs to route low voltage to high voltage divider PCB amplifiers have been ordered – should arrive at the JLab by end of the year or early January
2. At least 1080 flat cables need to be procured or fabricated to provide low voltage to the amplifiers
3. Mpod low voltage power supply crates and modules have arrived at JLab

3. DSG Update

Aaron Brown

1. Presented [NPS Collaborators' Meeting](#) DSG Update
 - Reviewed simulations done with 36x30 PbWO₄ crystal array with carbon fiber and mu metal dividers and various heat loads from 0 W to 700 W
 - Ambient temperature determines the maximum temperature of the crystal array
 - Ambient temperature in Hall C is typically between 18°C and 20°C - discussed the use of cooling air from the two heat exchangers

4. Next meeting: 2021/12/02