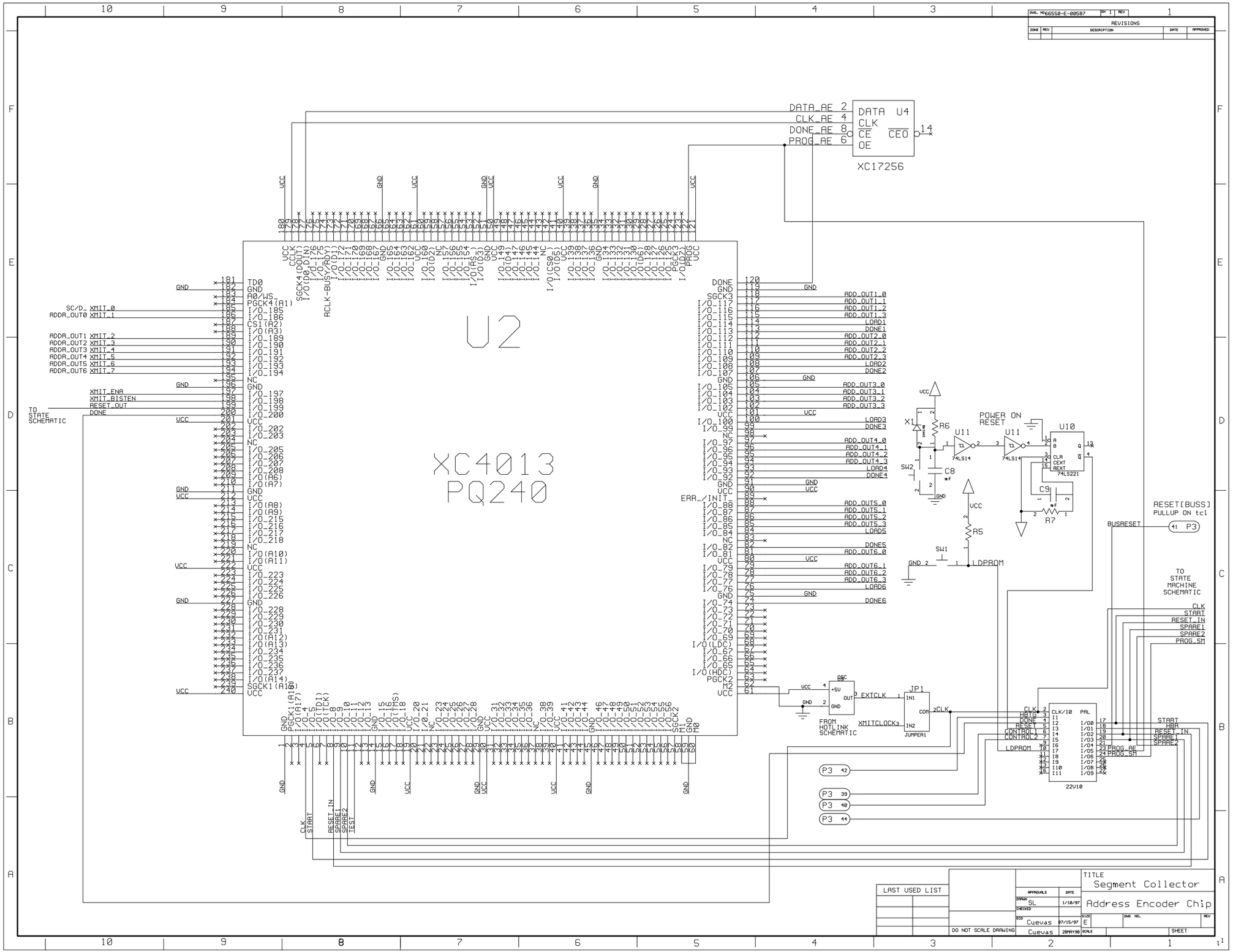
DWG. NO. 66550-E-00587
REVISIONS
DATA_AE
CLK_AE
DONE_AE
PROG_AE
DATA
CLK
CE
OE
CEO
U4
XC17256
U2
XC4013
PQ240
TO STATE SCHEMATIC
SC/D_ XMIT_0
ADDR_OUT0 XMIT_1
ADDR_OUT1 XMIT_2
ADDR_OUT2 XMIT_3
ADDR_OUT3 XMIT_4
ADDR_OUT4 XMIT_5
ADDR_OUT5 XMIT_6
ADDR_OUT6 XMIT_7
XMIT_ENA
XMIT_BISTEN
RESET_OUT
DONE
ADD_OUT1_0
ADD_OUT1_1
ADD_OUT1_2
ADD_OUT1_3
LOAD1
DONE1
ADD_OUT2_0
ADD_OUT2_1
ADD_OUT2_2
ADD_OUT2_3
LOAD2
DONE2
ADD_OUT3_0
ADD_OUT3_1
ADD_OUT3_2
ADD_OUT3_3
LOAD3
DONE3
ADD_OUT4_0
ADD_OUT4_1
ADD_OUT4_2
ADD_OUT4_3
LOAD4
DONE4
ADD_OUT5_0
ADD_OUT5_1
ADD_OUT5_2
ADD_OUT5_3
LOAD5
DONE5
ADD_OUT6_0
ADD_OUT6_1
ADD_OUT6_2
ADD_OUT6_3
LOAD6
DONE6
POWER ON RESET
U10
U11
74LS14
74LS221
R6
R5
R7
C8
C9
SW1
SW2
LDPROM
RESET[BUSS]
PULLUP ON tci
BUSRESET
P3 41
TO STATE MACHINE SCHEMATIC
CLK
START
RESET_IN
SPARE1
SPARE2
PROG_SM
FROM HOTLINK SCHEMATIC
XMITCLOCK3
JP1
JUMPER1
EXTCLK
2CLK
CLK/10 PAL
22V10
P3 42
P3 39
P3 40
P3 44
LAST USED LIST
DO NOT SCALE DRAWING
APPROVALS
DATE
DRAWN SL 1/10/97
CHECKED
ECO Cuevas 07/15/97
Cuevas 28MAY98
TITLE
Segment Collector
Address Encoder Chip
SIZE E
DWG NO.
REV
SCALE
SHEET