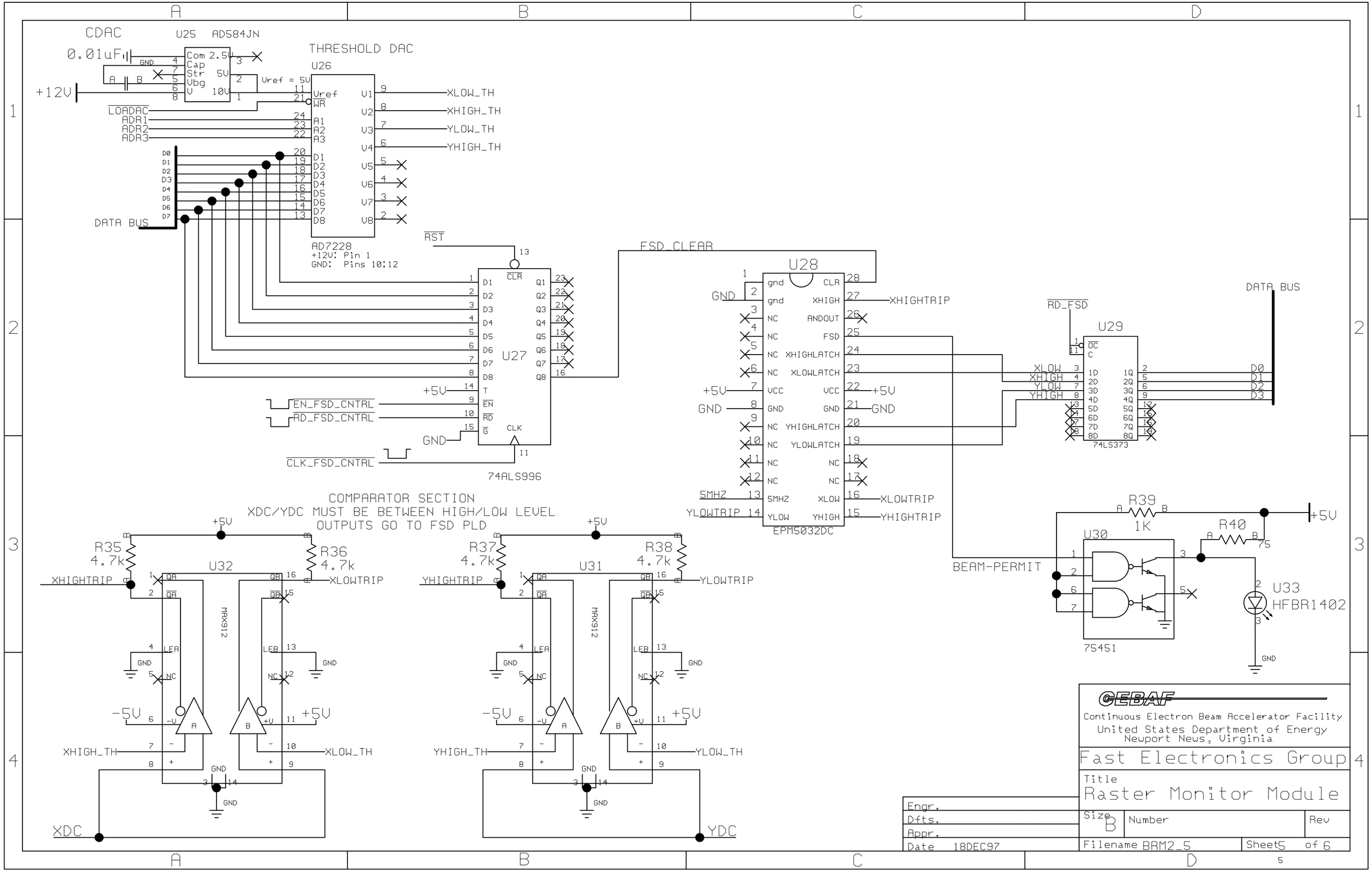
CDAC
U25 AD584JN
0.01uF
+12V
THRESHOLD DAC
U26
Vref = 5V
LOADAC
ADR1
ADR2
ADR3
DATA BUS
XLOW_TH
XHIGH_TH
YLOW_TH
YHIGH_TH
AD7228
+12V: Pin 1
GND: Pins 10;12
RST
FSD_CLEAR
U27
74ALS996
EN_FSD_CNTRL
RD_FSD_CNTRL
CLK_FSD_CNTRL
U28
EPM5032DC
XHIGHTRIP
XLOWTRIP
YHIGHTRIP
5MHZ
YLOWTRIP
RD_FSD
U29
74LS373
DATA BUS
R39
1K
R40
75
U30
75451
BEAM-PERMIT
U33
HFBR1402
COMPARATOR SECTION
XDC/YDC MUST BE BETWEEN HIGH/LOW LEVEL
OUTPUTS GO TO FSD PLD
R35
4.7k
R36
4.7k
R37
4.7k
R38
4.7k
U32
U31
MAX912
XHIGHTRIP
XLOWTRIP
YHIGHTRIP
YLOWTRIP
XHIGH_TH
XLOW_TH
YHIGH_TH
YLOW_TH
XDC
YDC
CEBAF
Continuous Electron Beam Accelerator Facility
United States Department of Energy
Newport News, Virginia
Fast Electronics Group
Title
Raster Monitor Module
Engr.
Dfts.
Appr.
Date 18DEC97
Size B
Number
Rev
Filename BRM2_5
Sheet 5 of 6