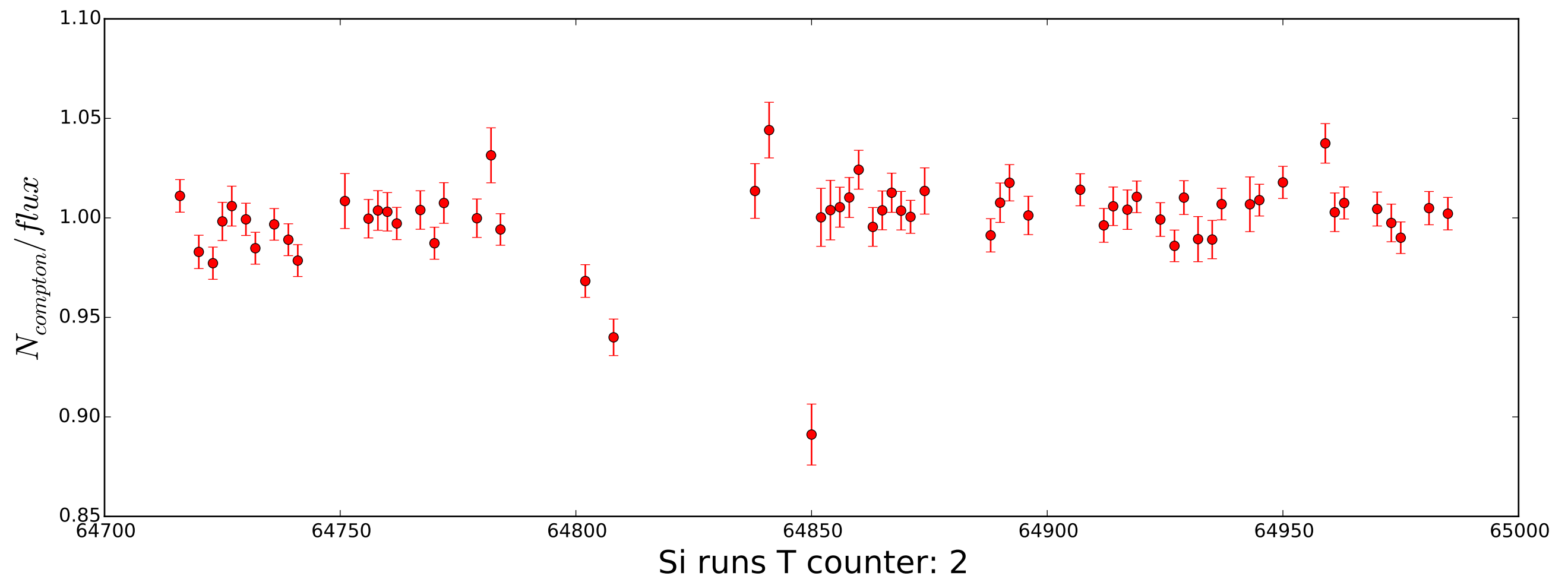
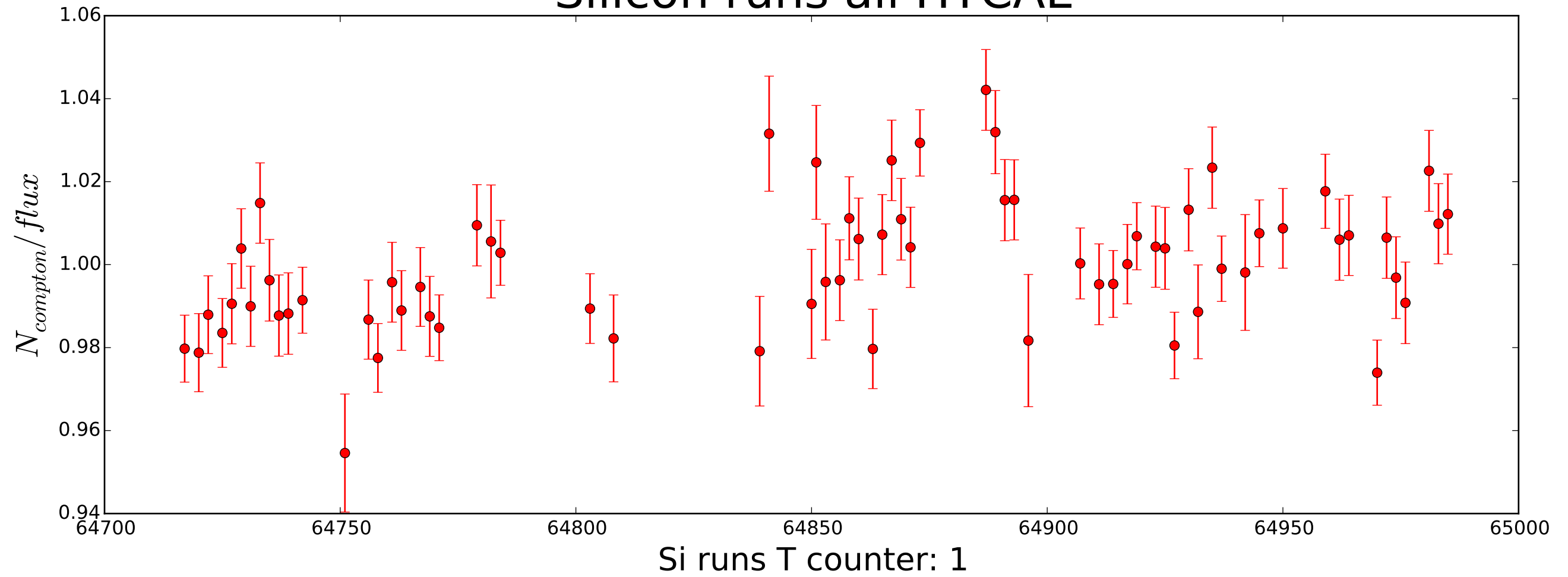
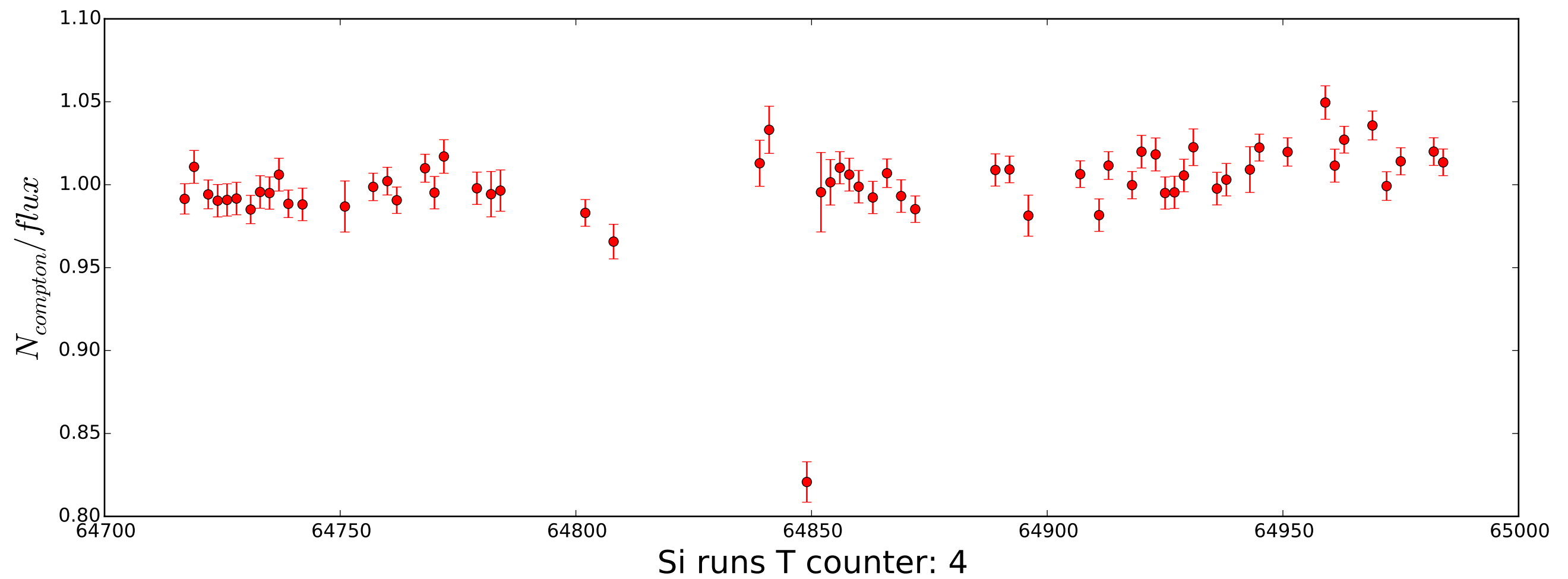
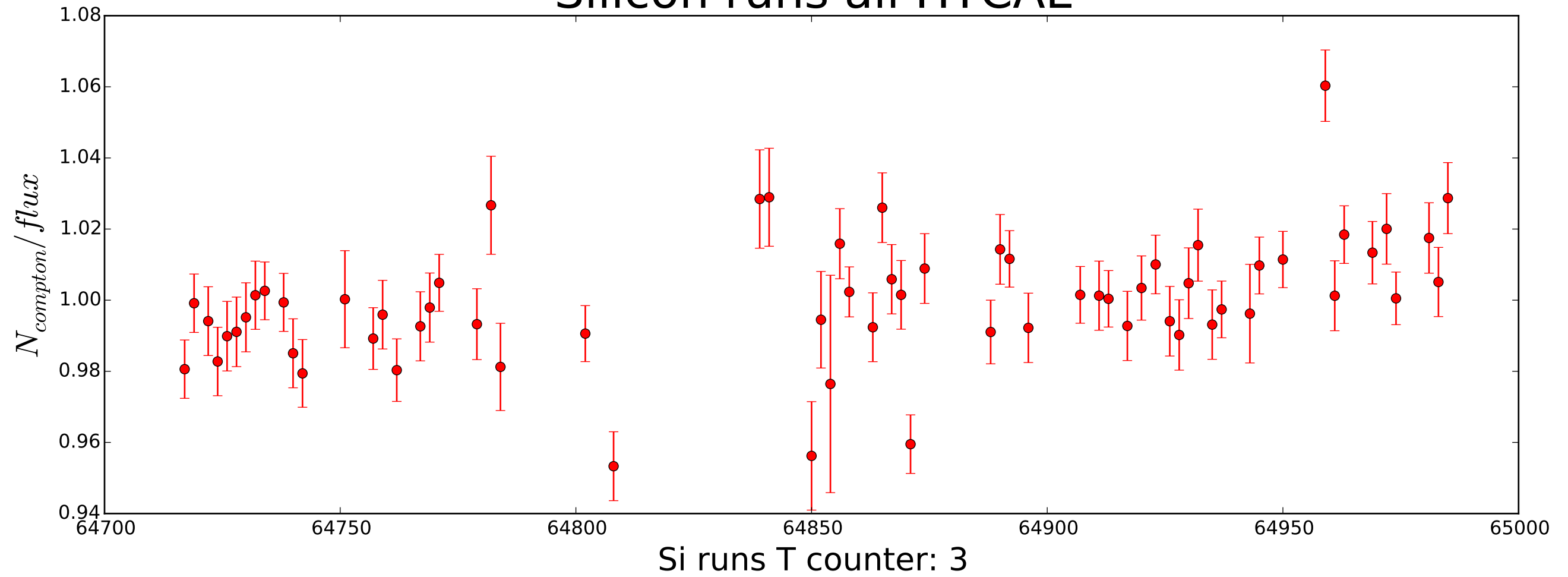


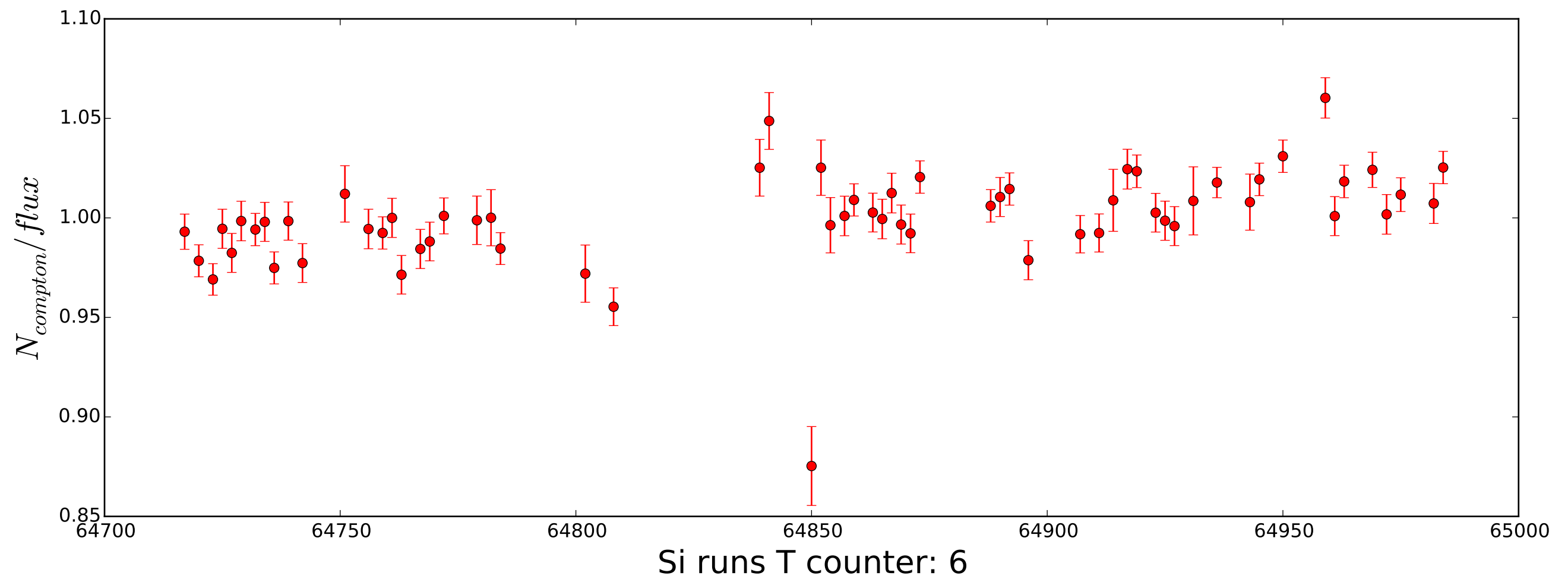
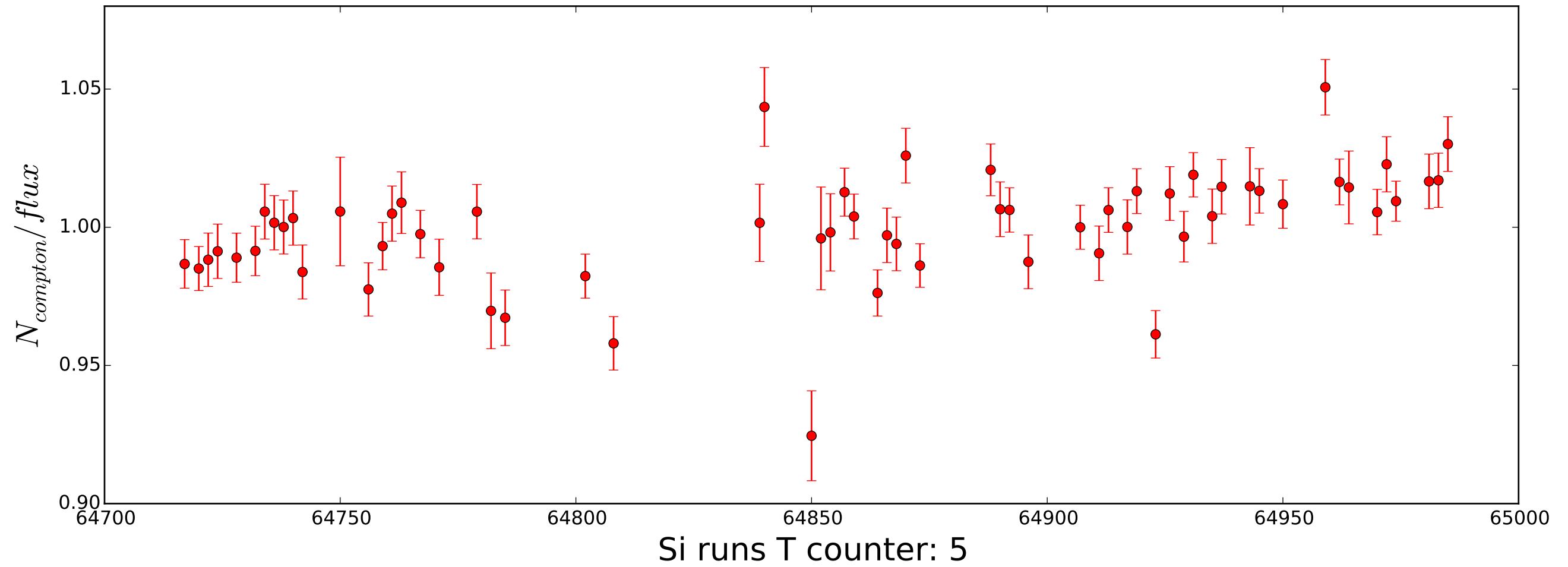
Silicon runs all HYCAL



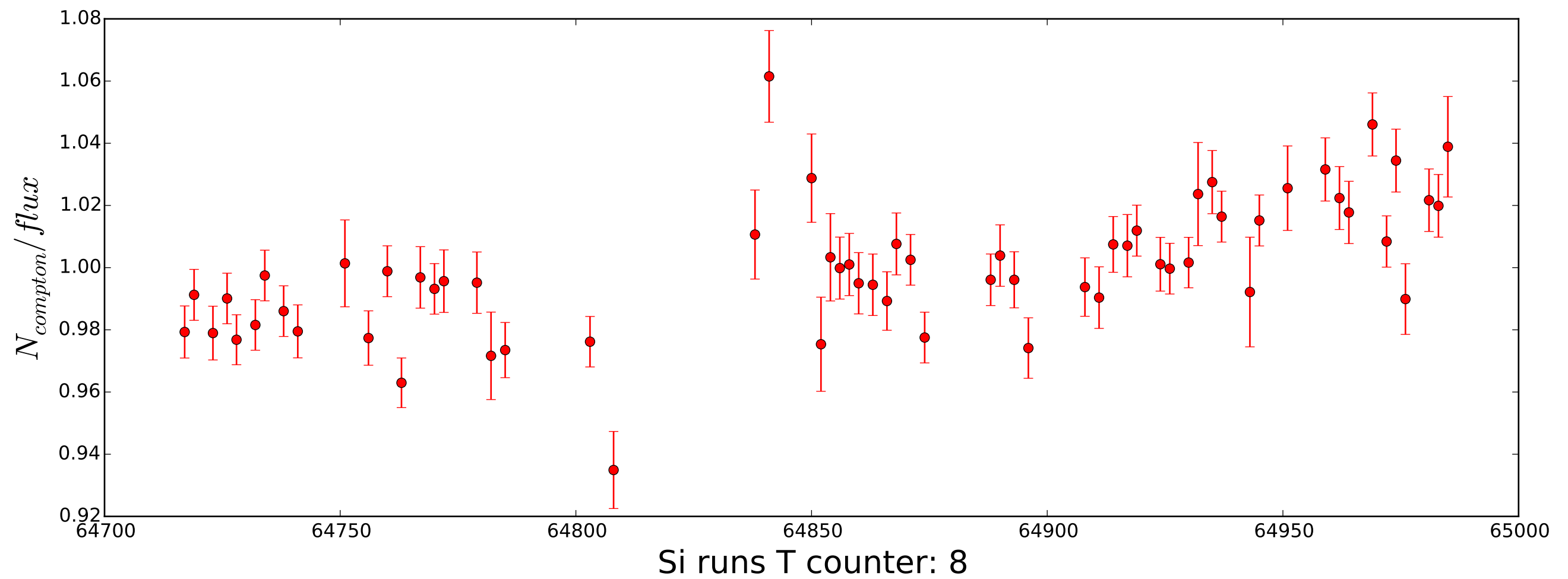
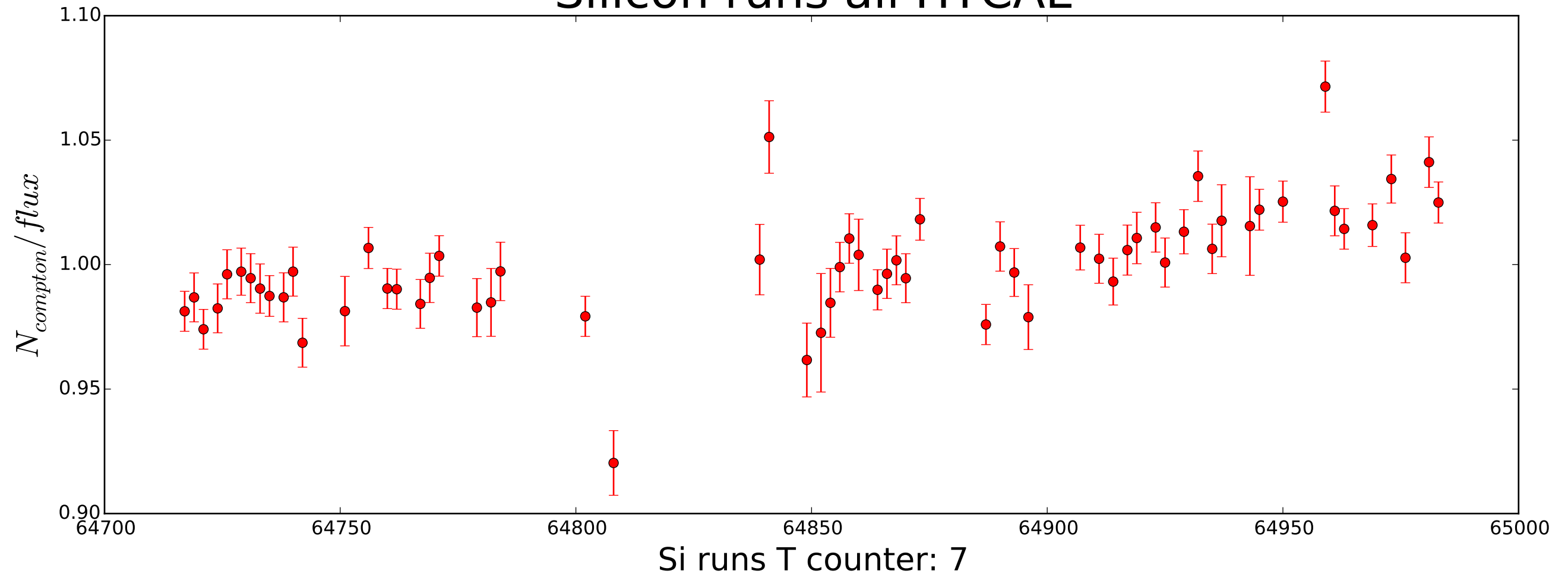
Silicon runs all HYCAL



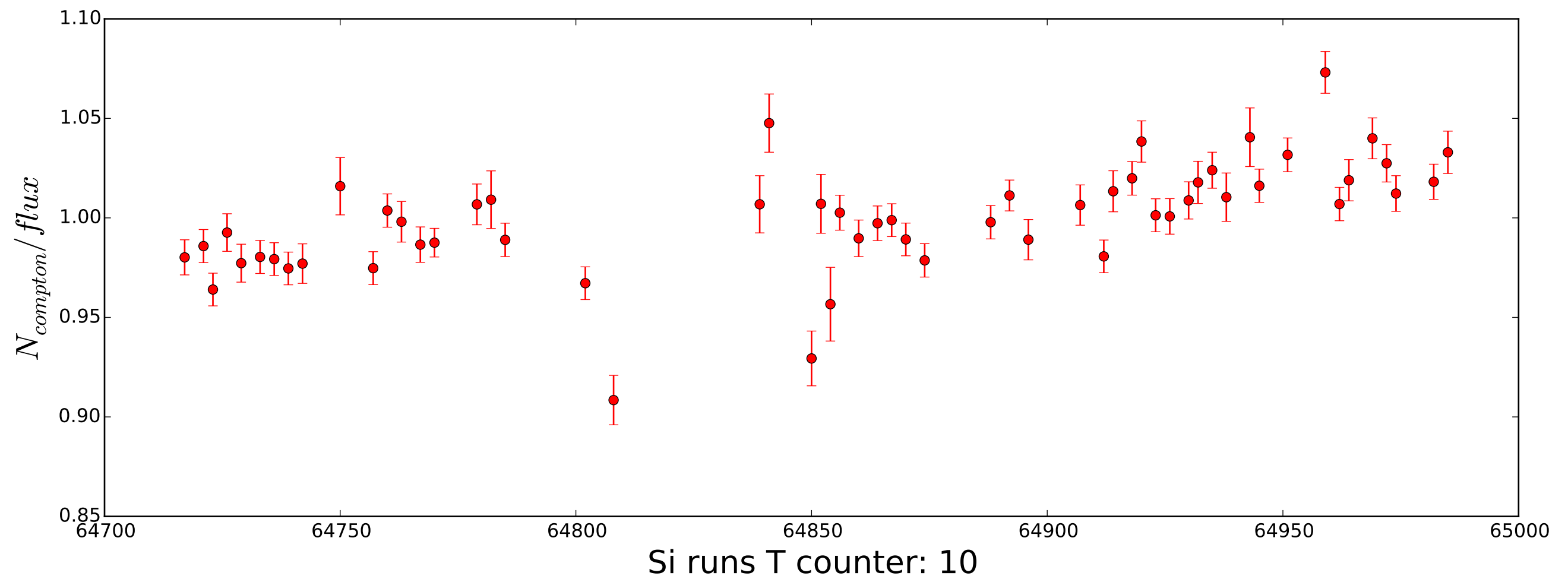
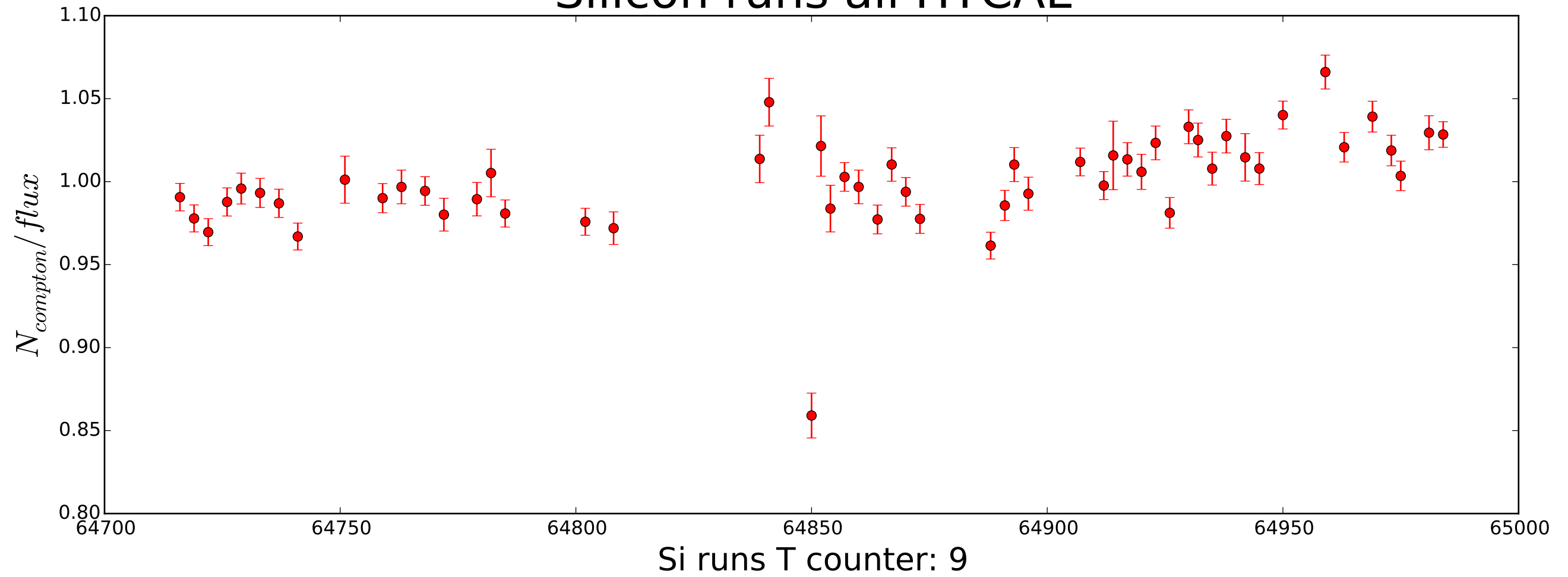
Silicon runs all HYCAL



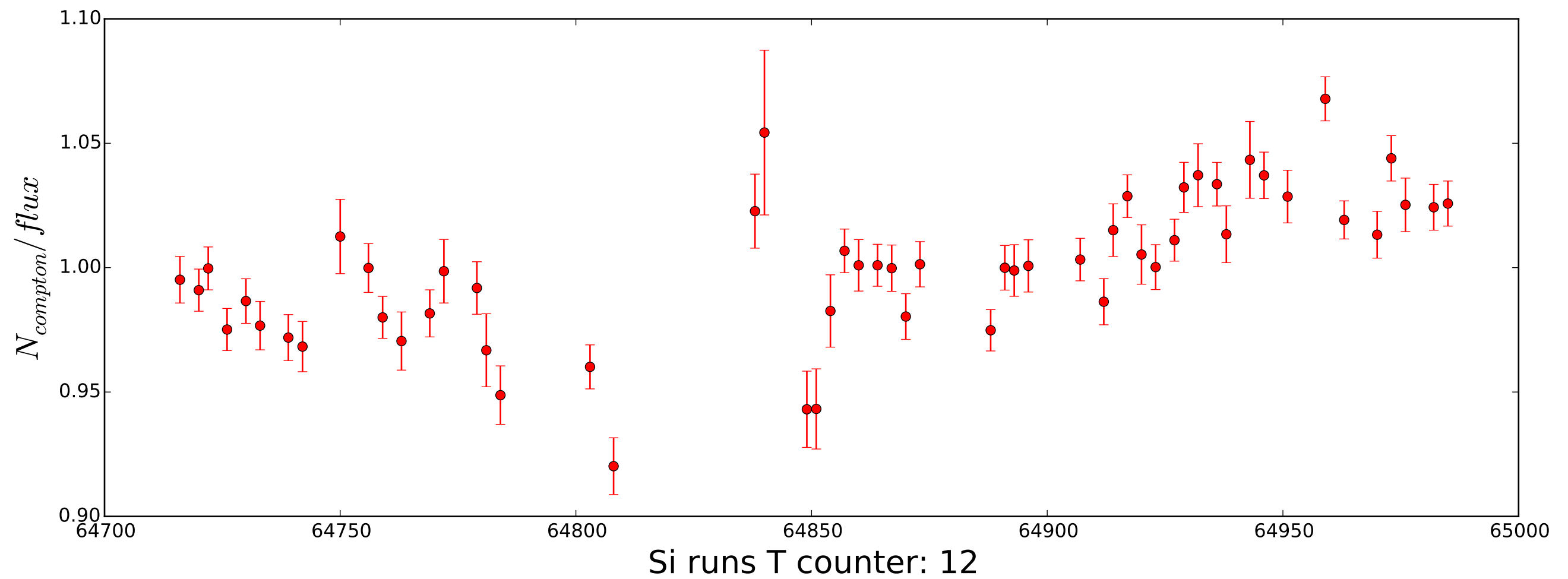
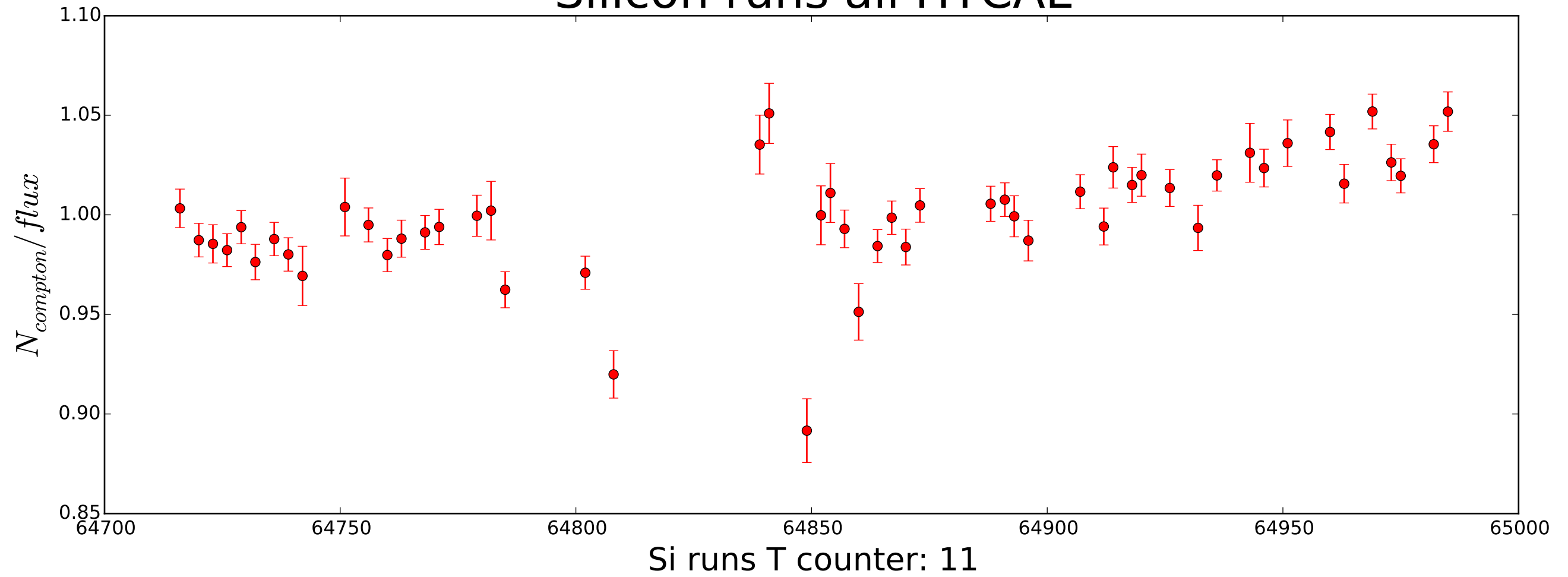
Silicon runs all HYCAL



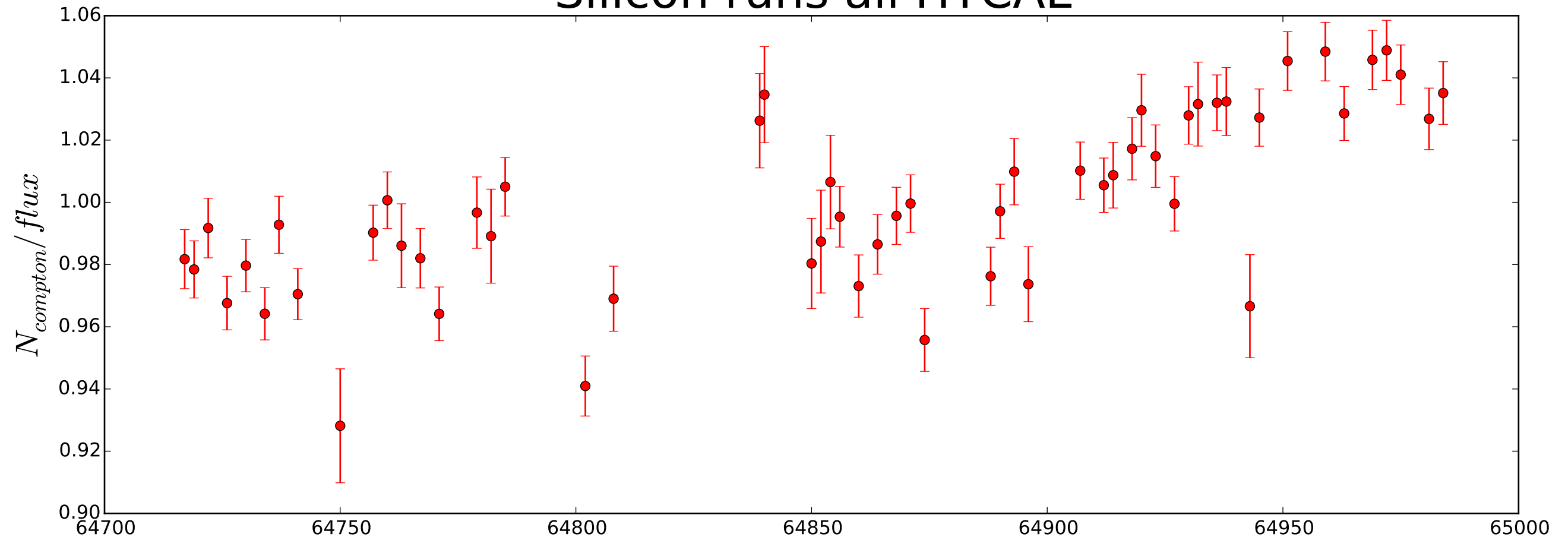
Silicon runs all HYCAL



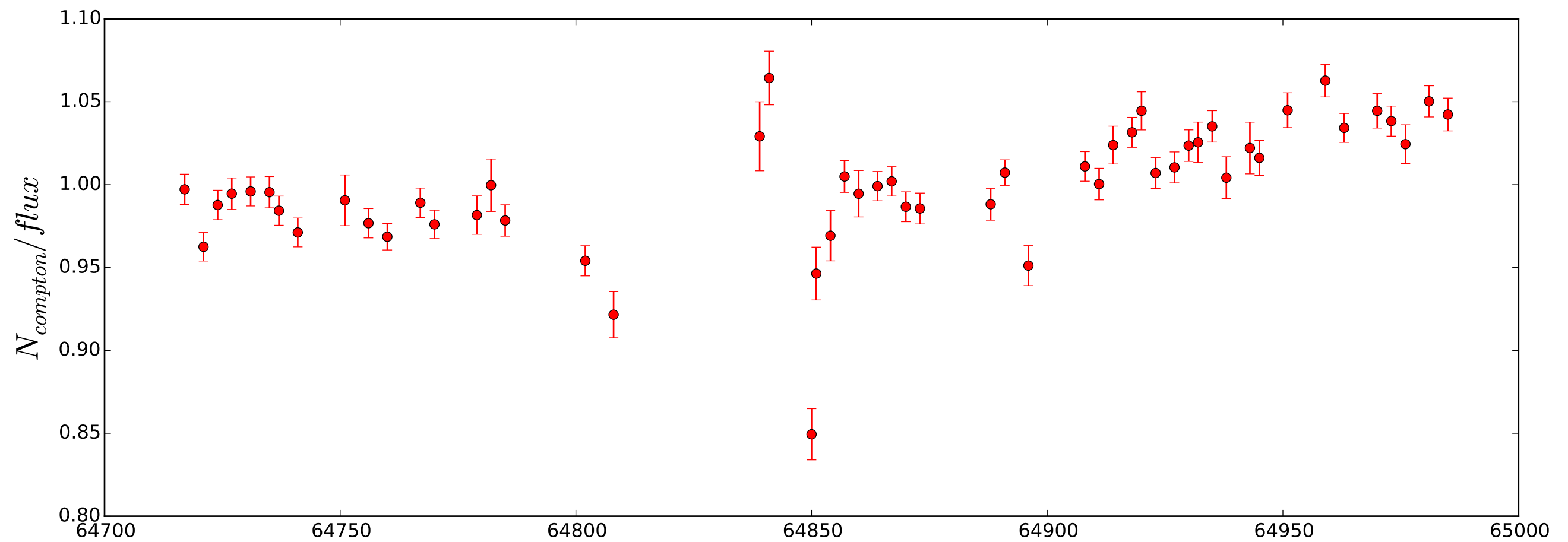
Silicon runs all HYCAL



Silicon runs all HYCAL

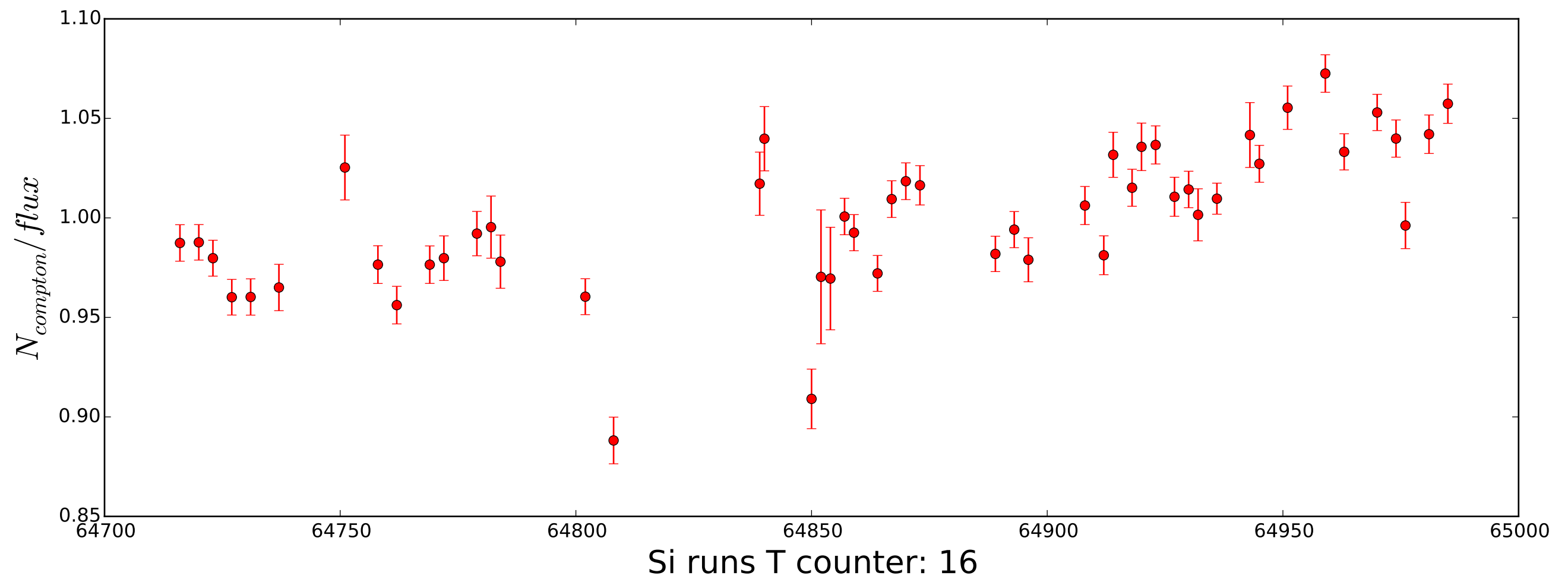
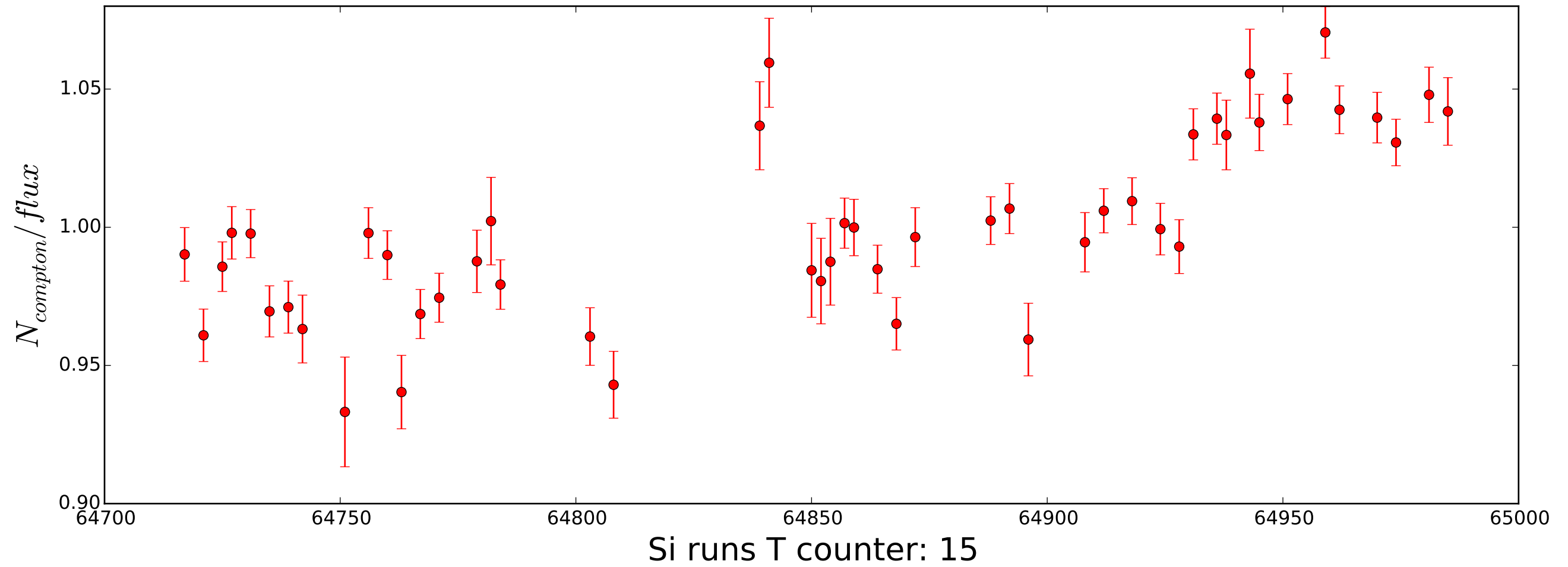


Si runs T counter: 13



Si runs T counter: 14

Silicon runs all HYCAL



Silicon runs all HYCAL

