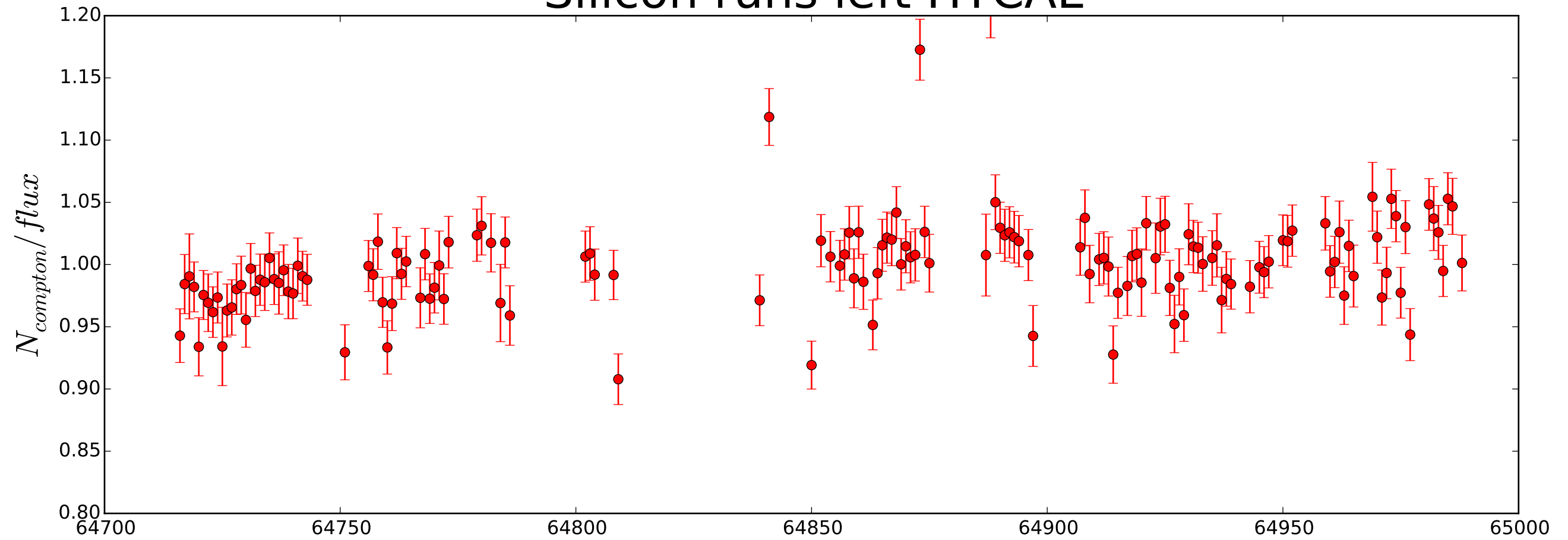
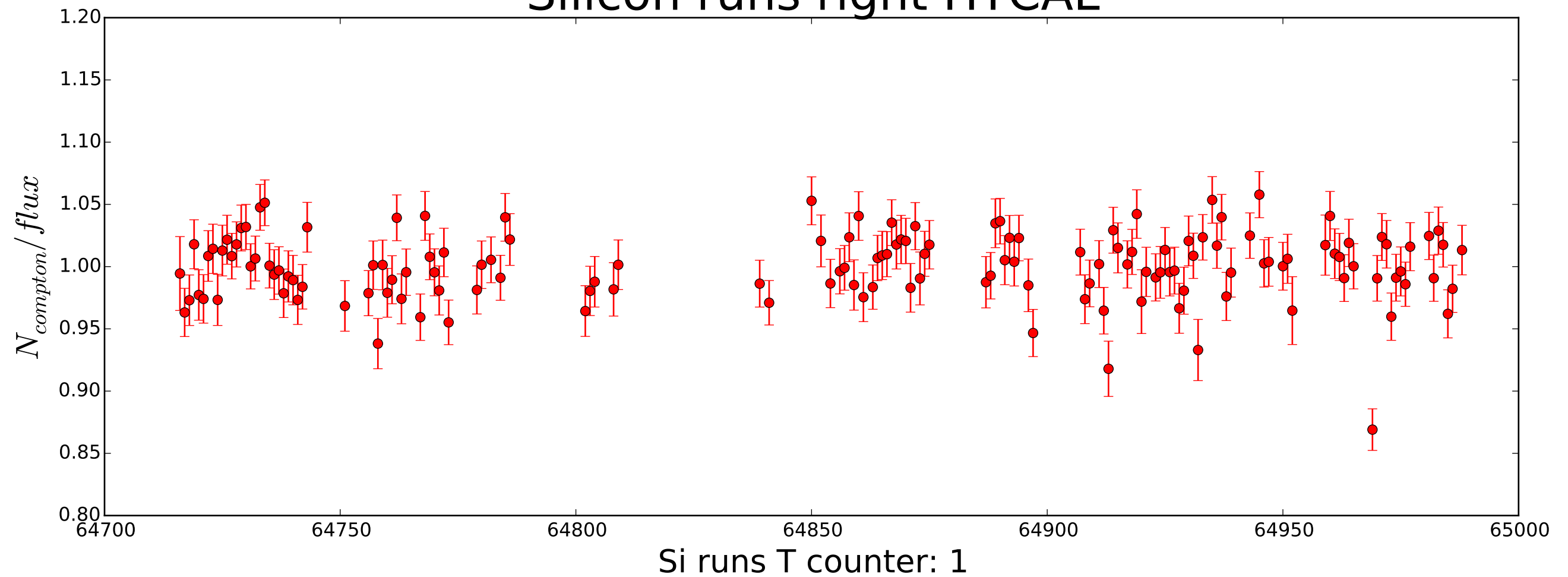


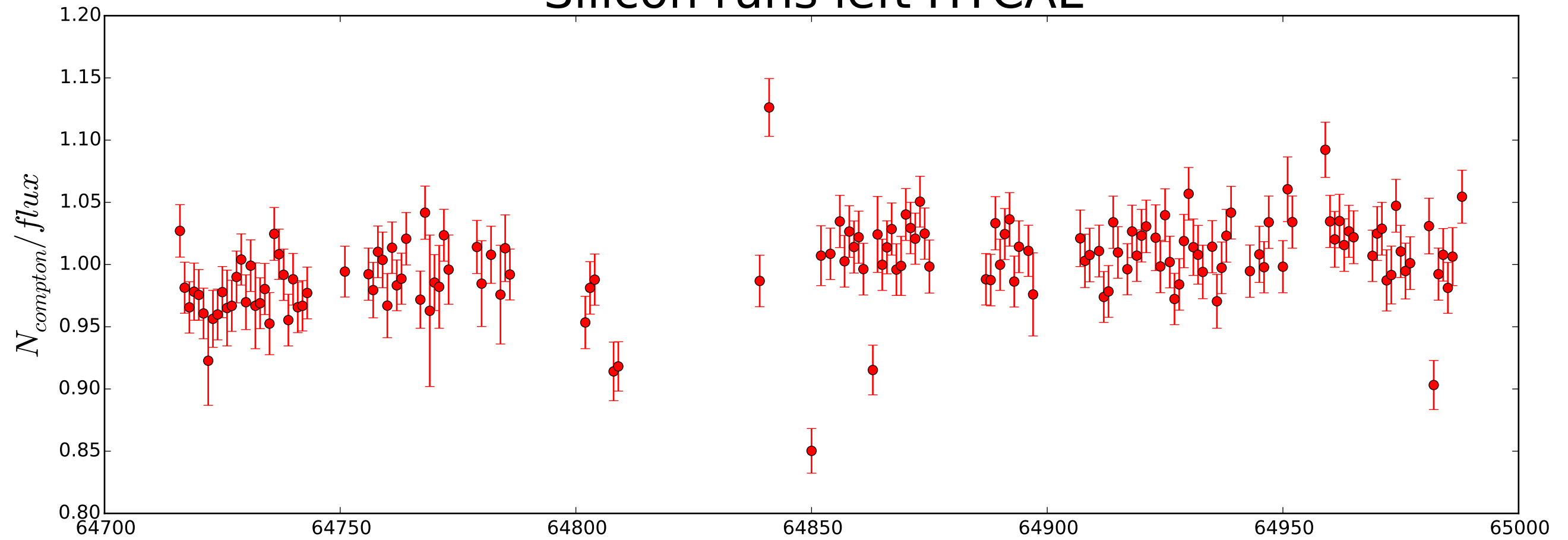
Silicon runs left HYCAL



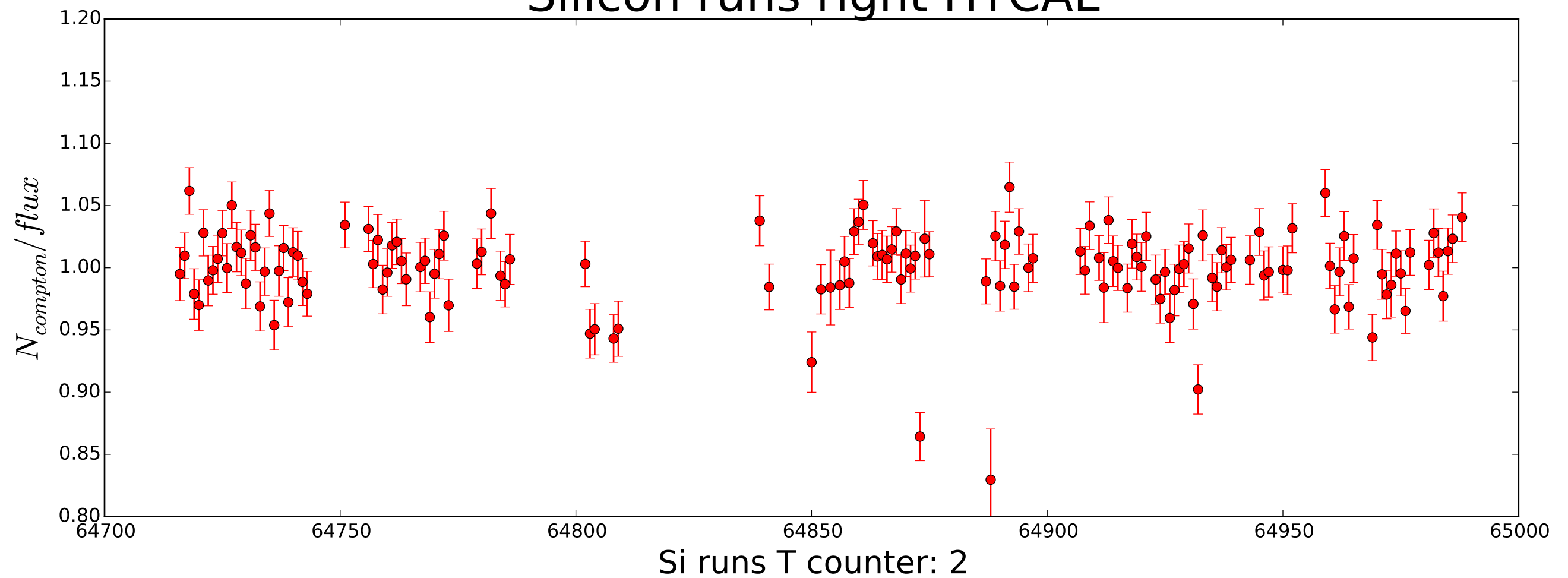
Si runs T counter: 1 Silicon runs right HYCAL



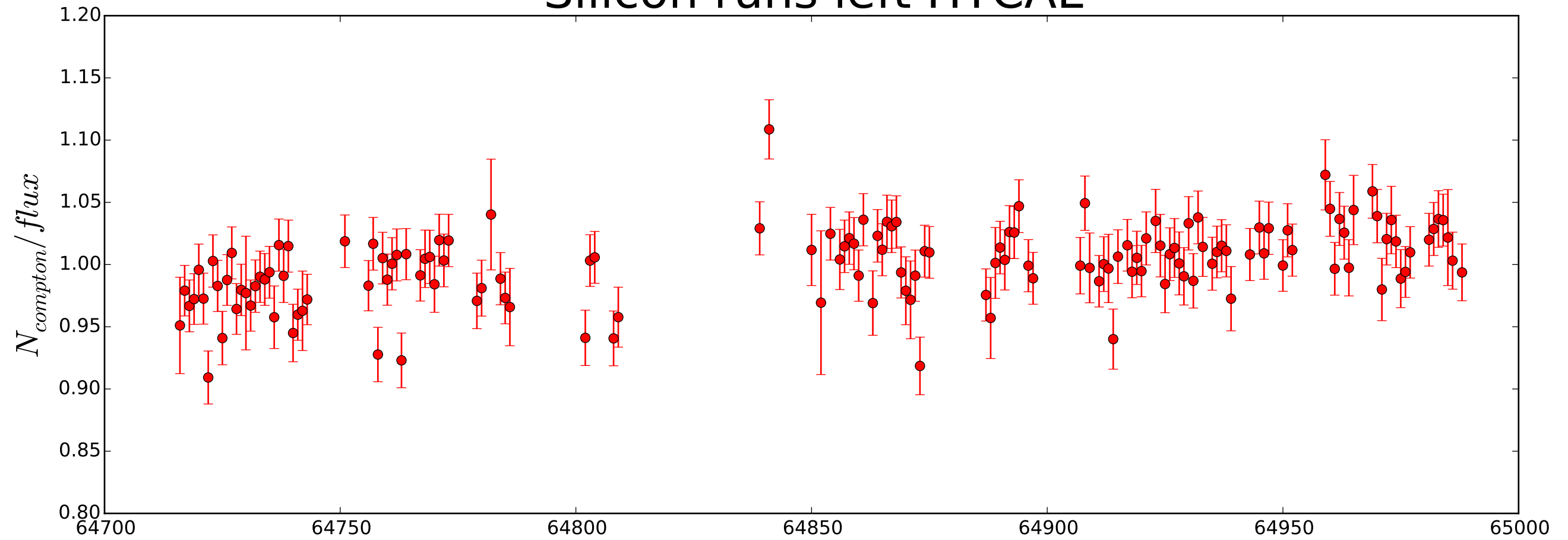
Silicon runs left HYCAL



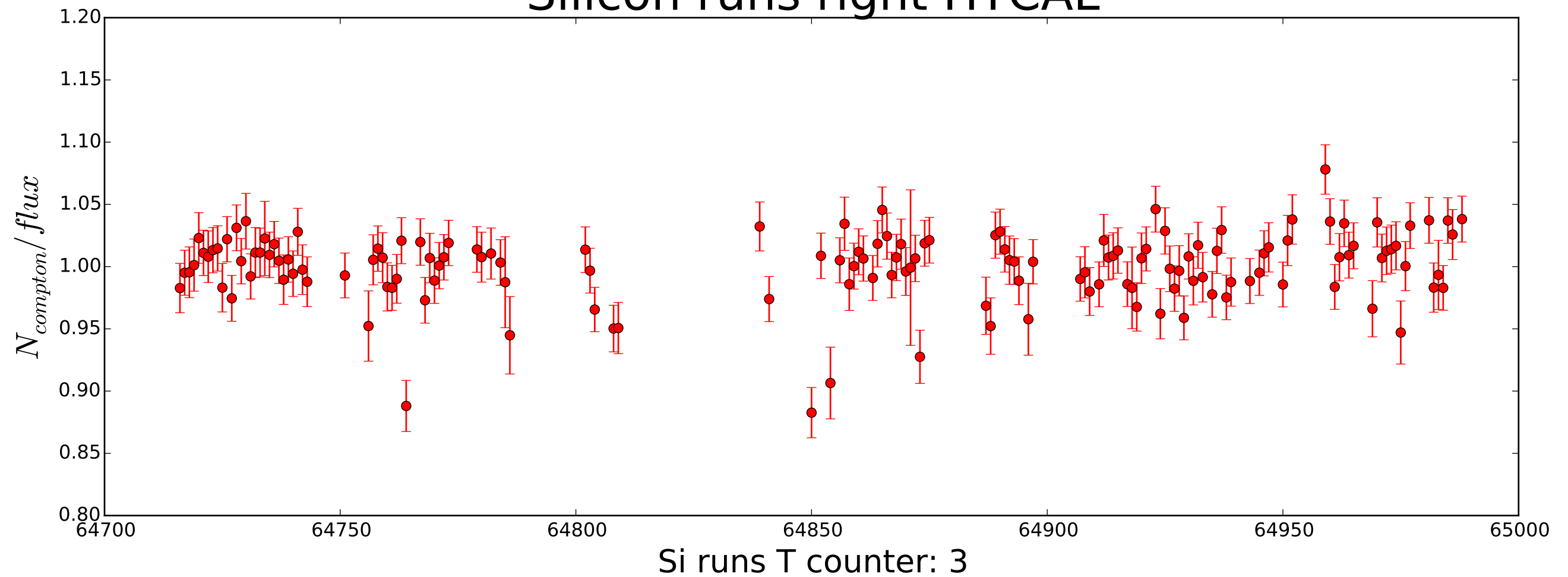
Silicon runs right HYCAL



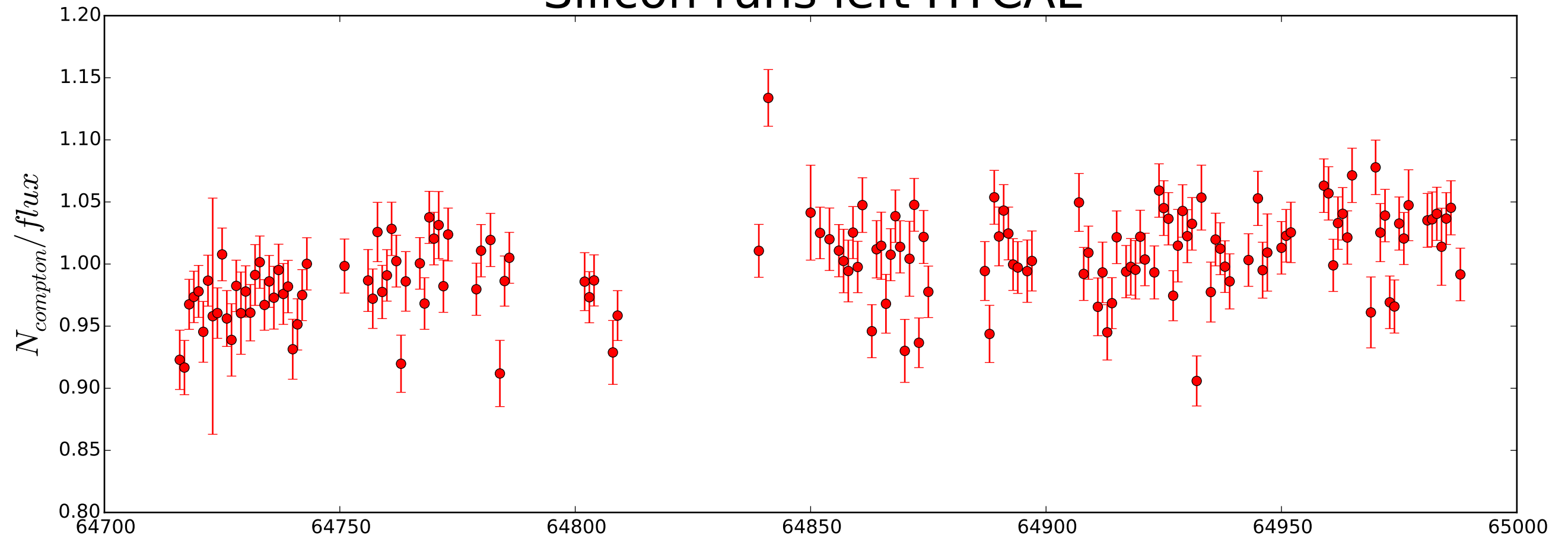
Silicon runs left HYCAL



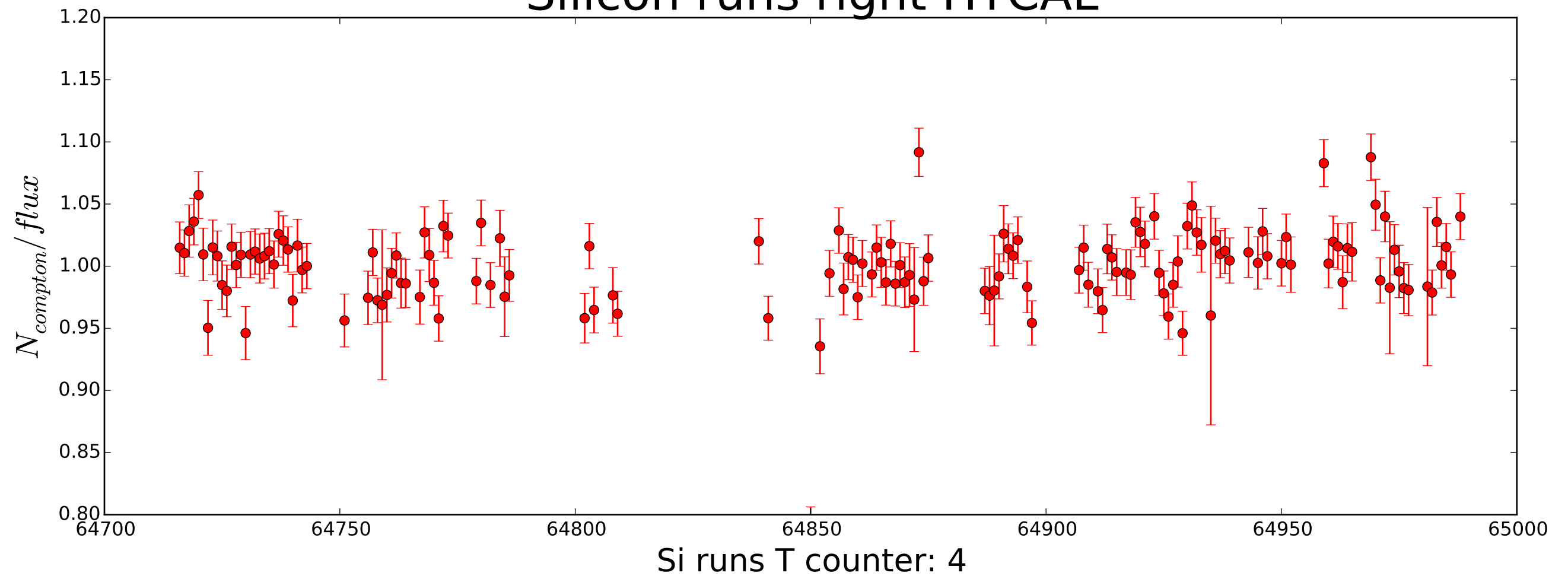
Silicon runs right HYCAL



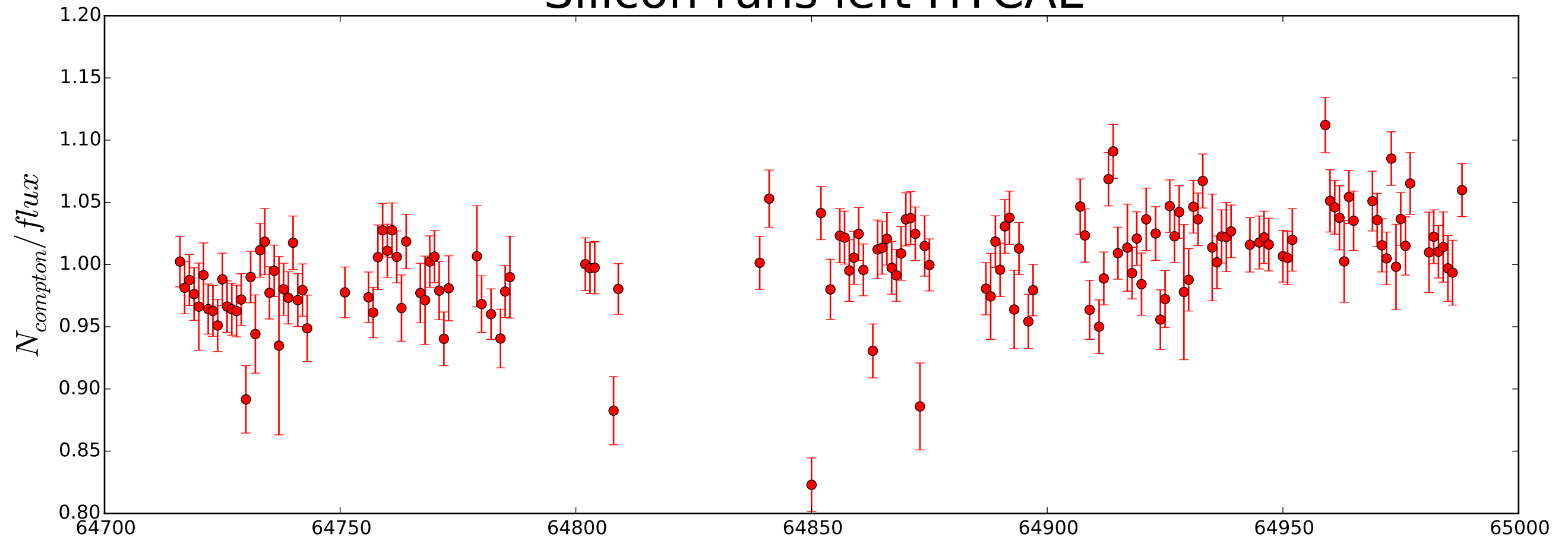
Silicon runs left HYCAL



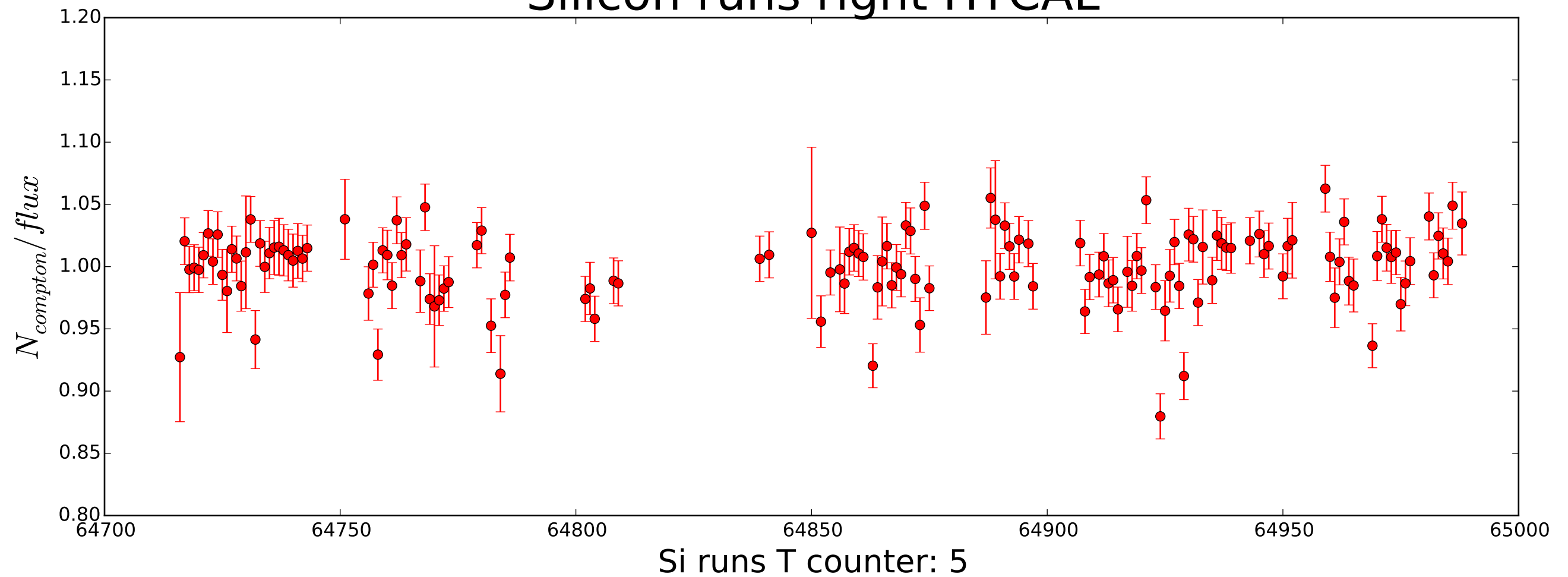
Silicon runs right HYCAL



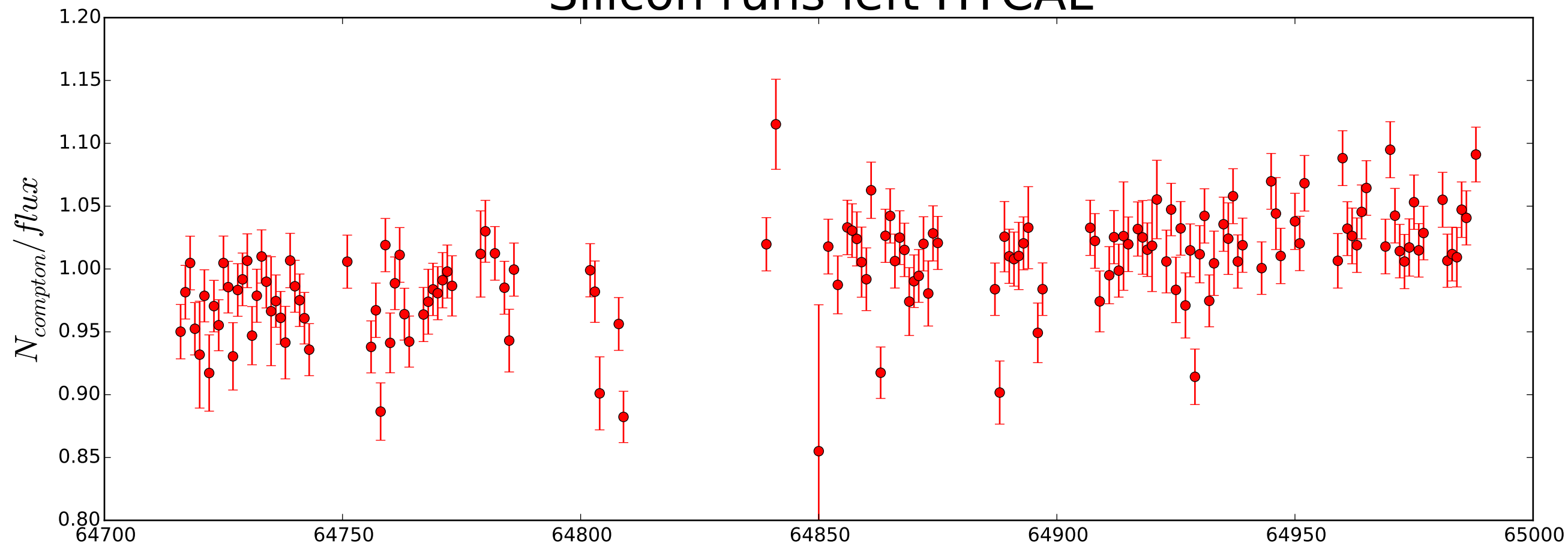
Silicon runs left HYCAL



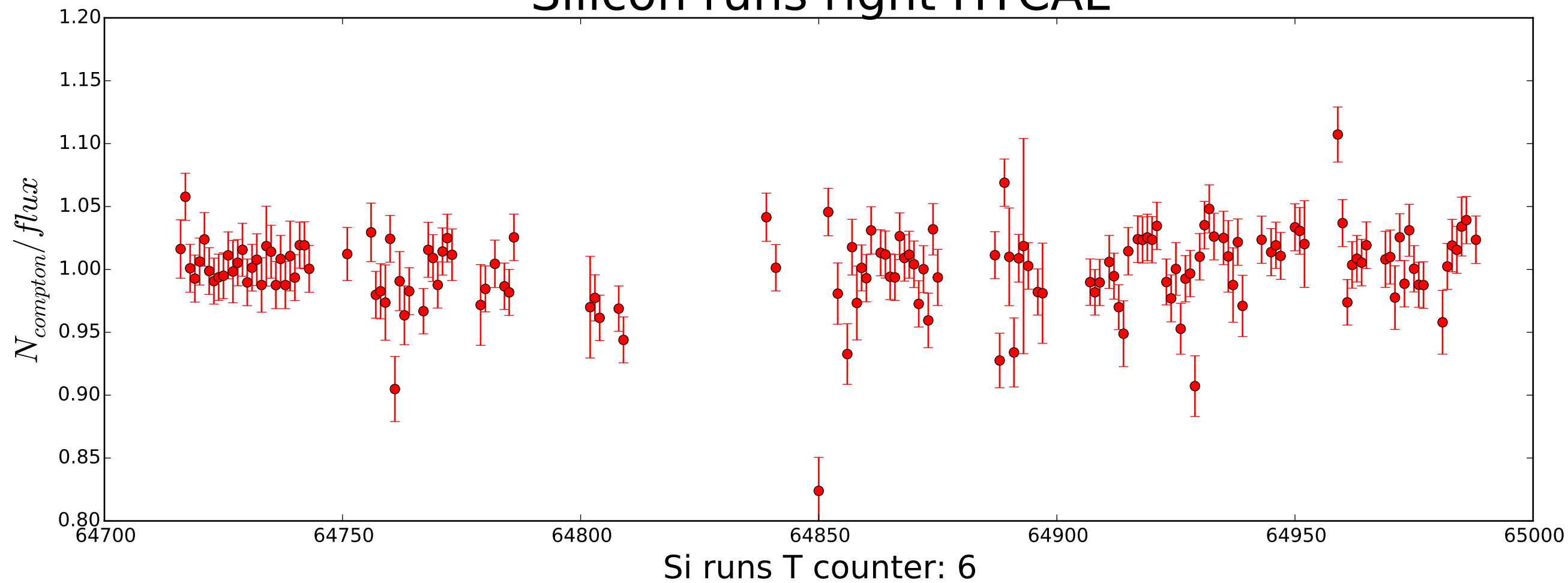
Si runs T counter: 5 Silicon runs right HYCAL



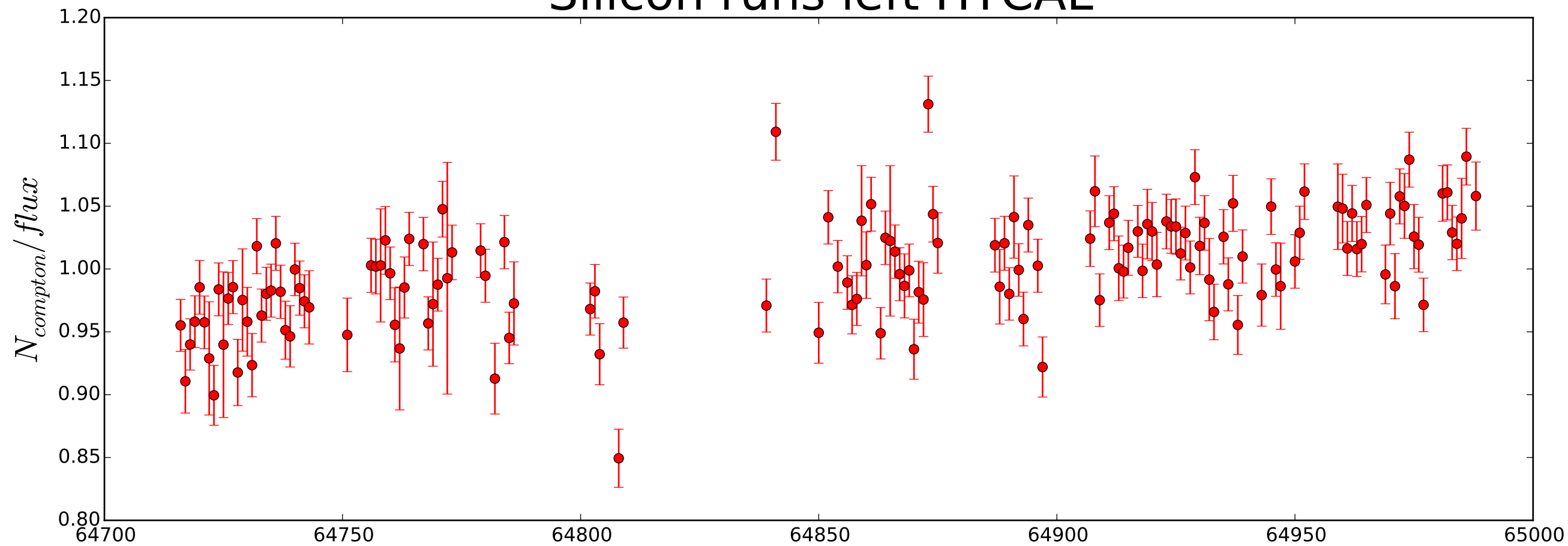
Silicon runs left HYCAL



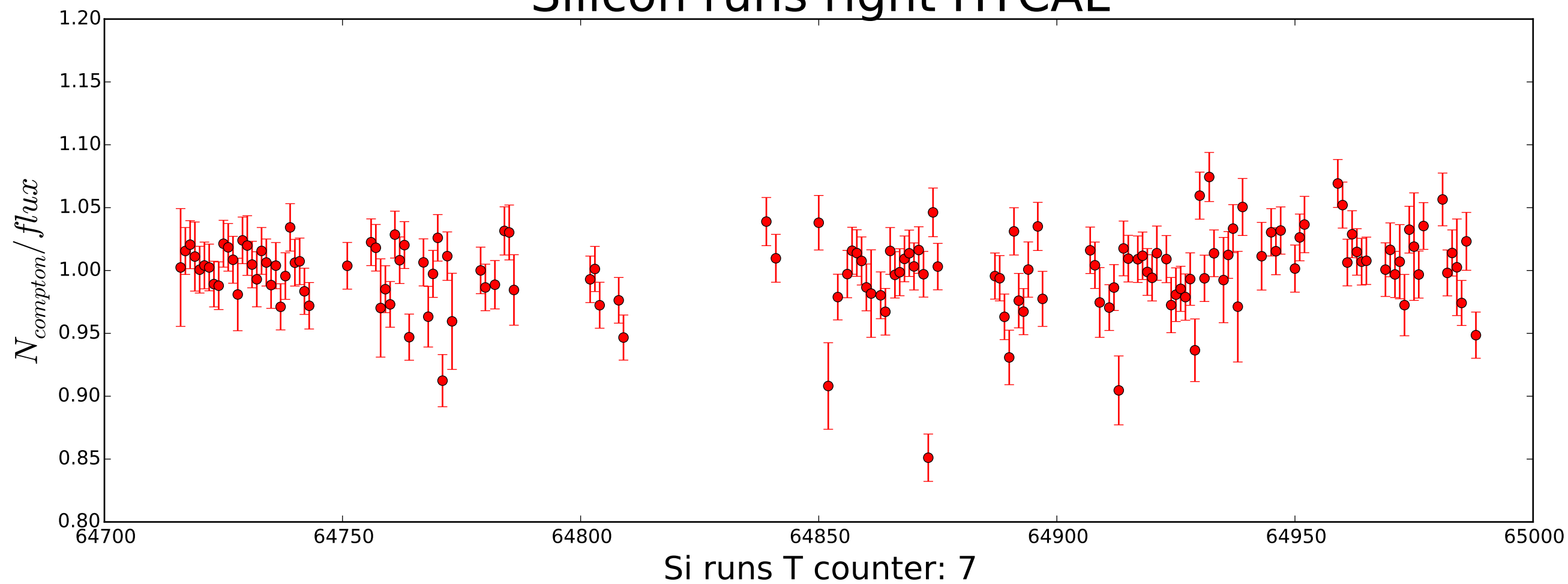
Silicon runs right HYCAL



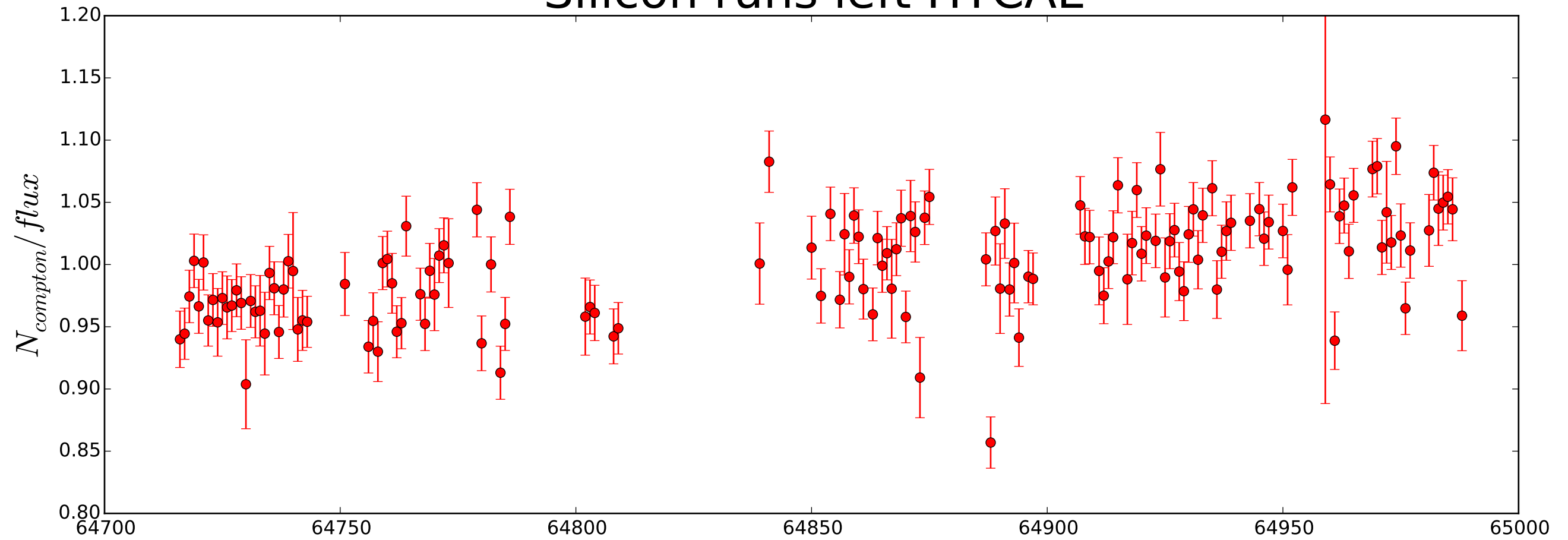
Silicon runs left HYCAL



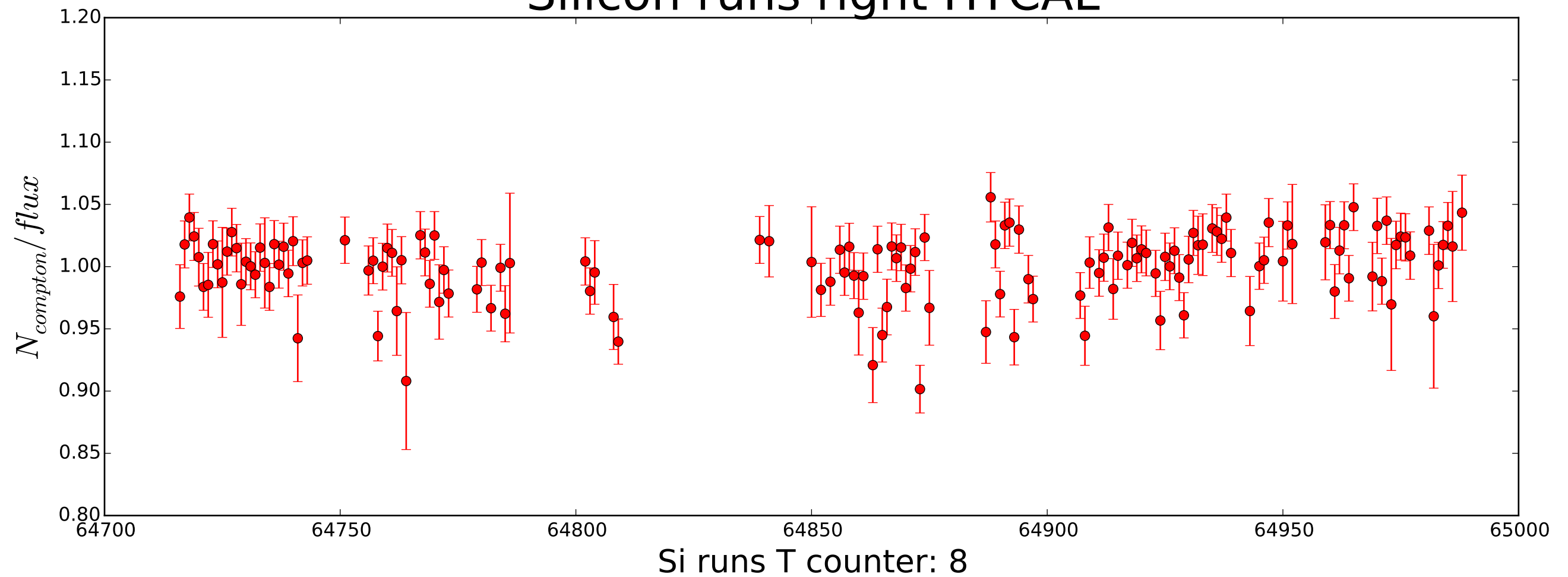
Silicon runs right HYCAL



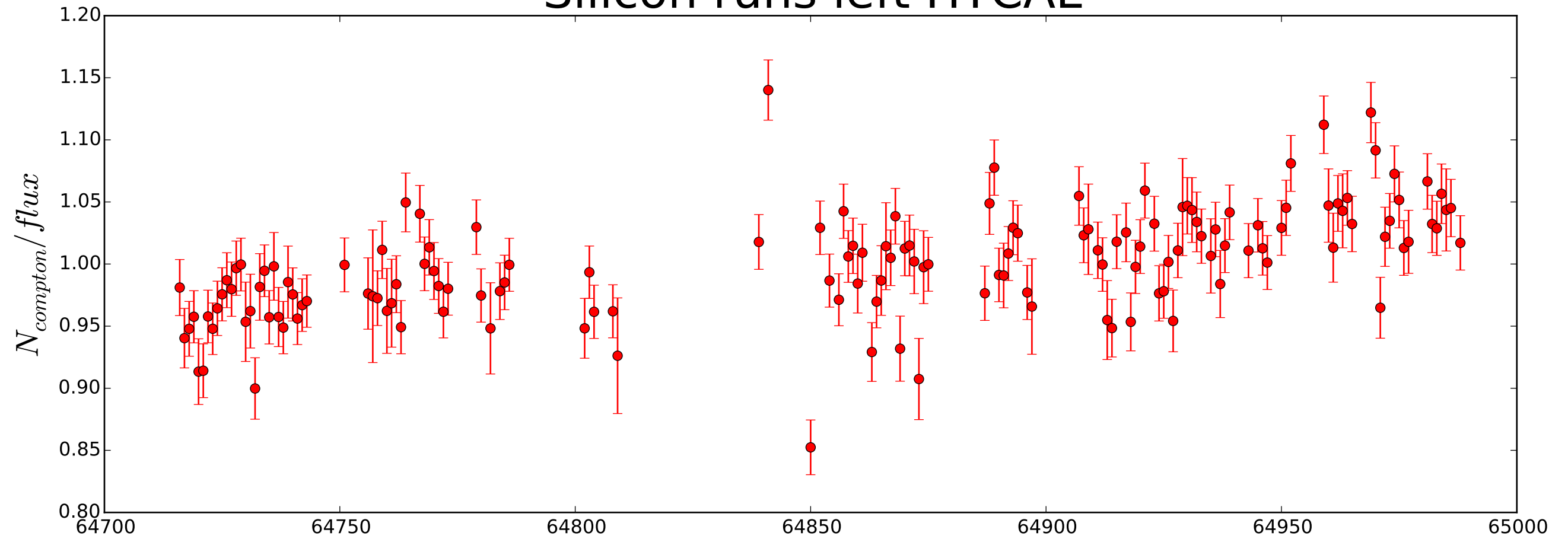
Silicon runs left HYCAL



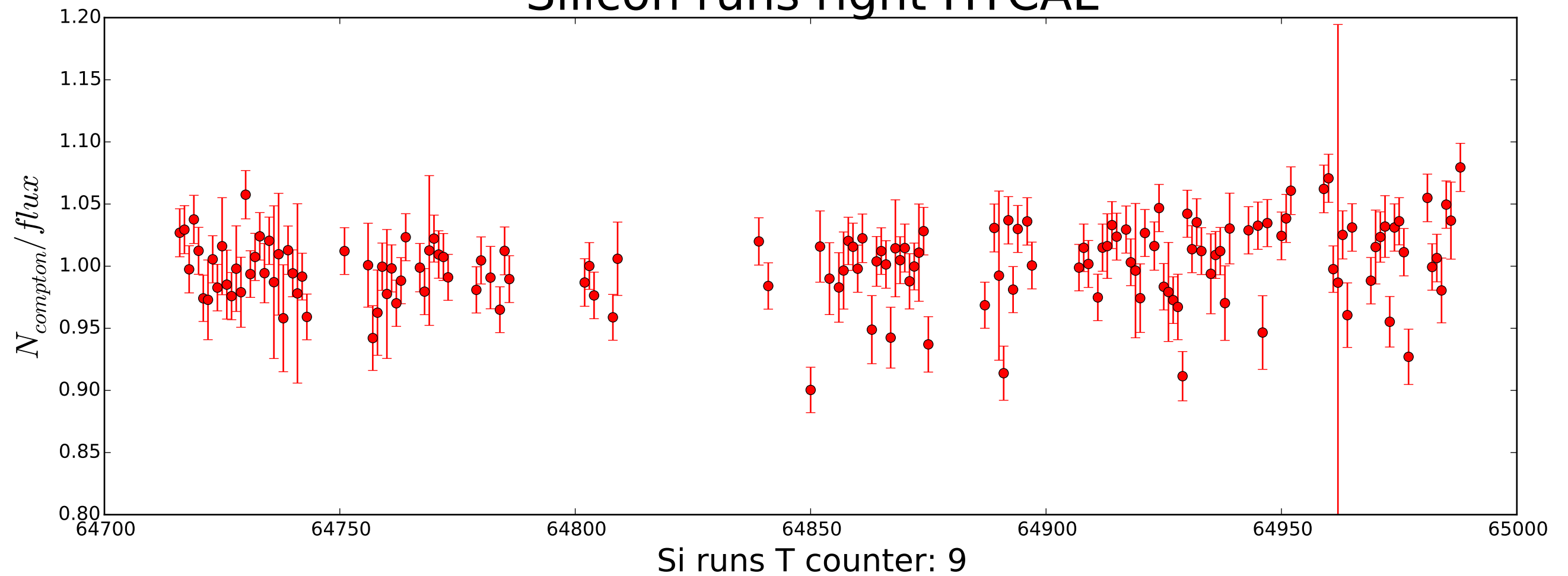
Si runs T counter: 8 Silicon runs right HYCAL



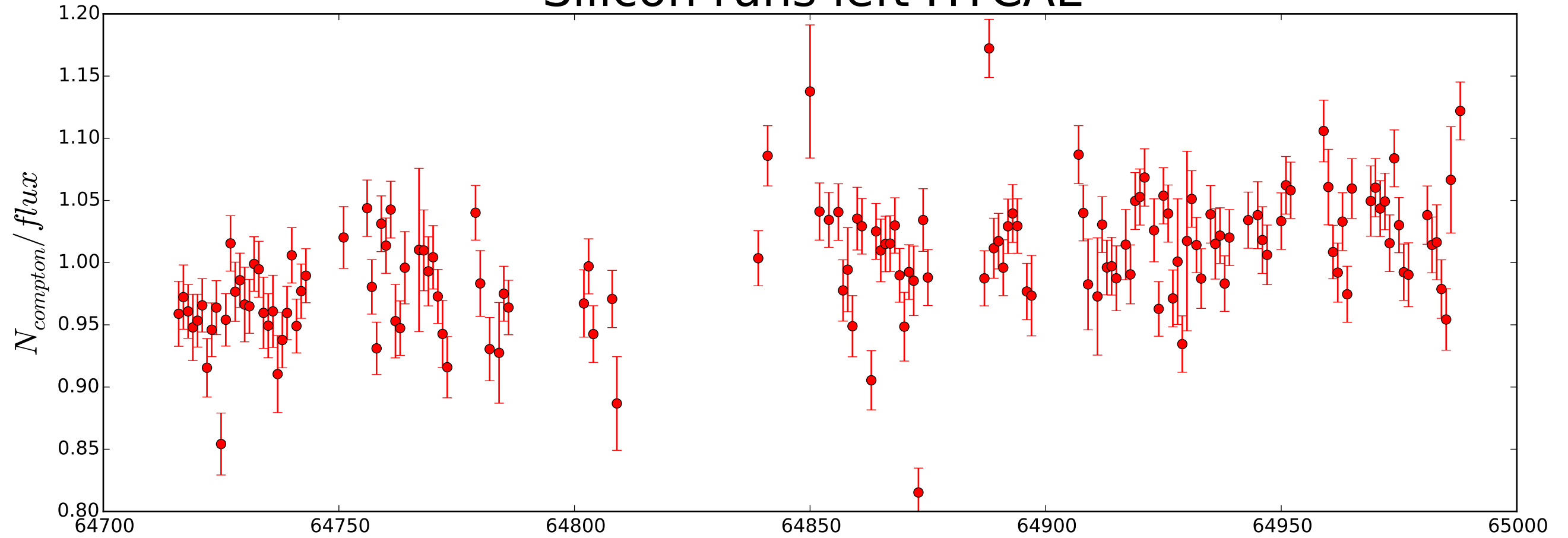
Silicon runs left HYCAL



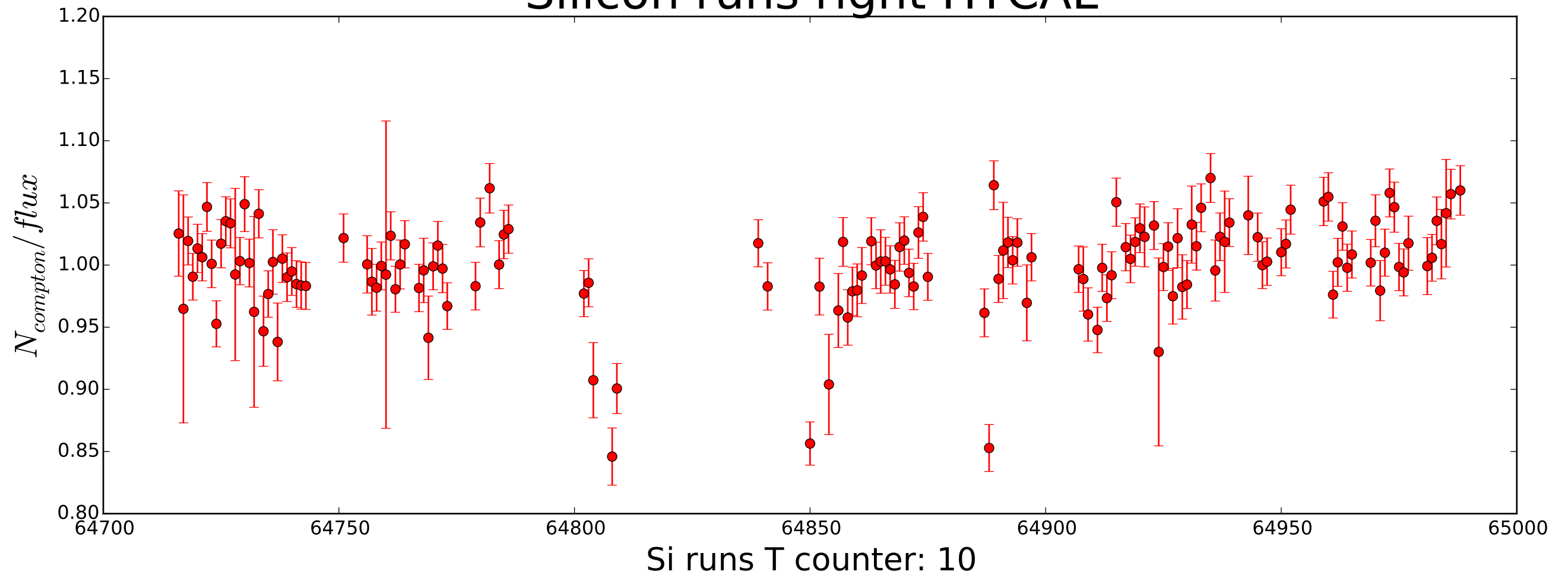
Si runs T counter: 9 Silicon runs right HYCAL



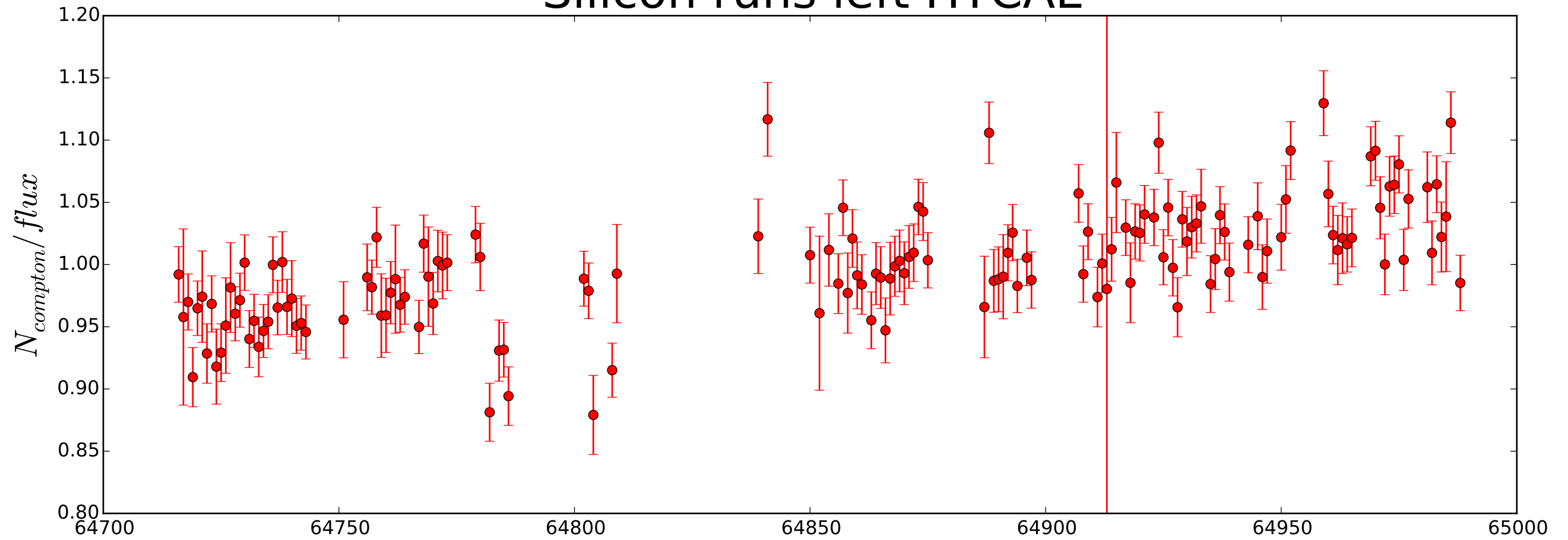
Silicon runs left HYCAL



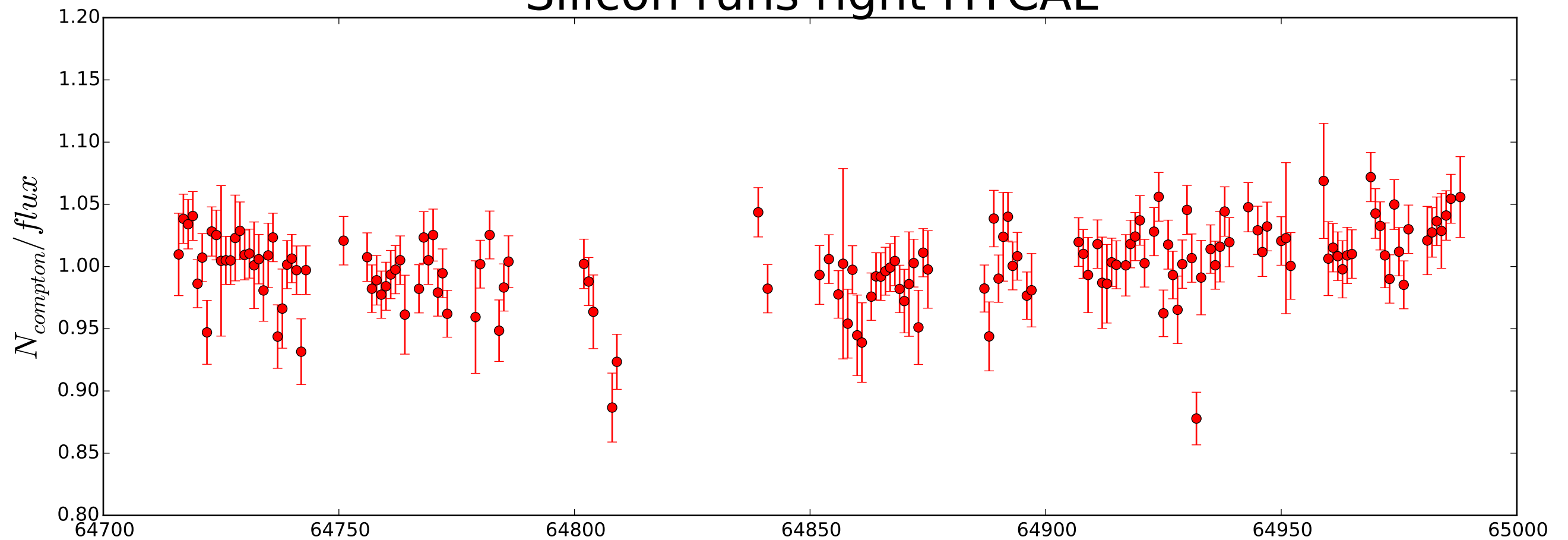
Silicon runs right HYCAL



Silicon runs left HYCAL

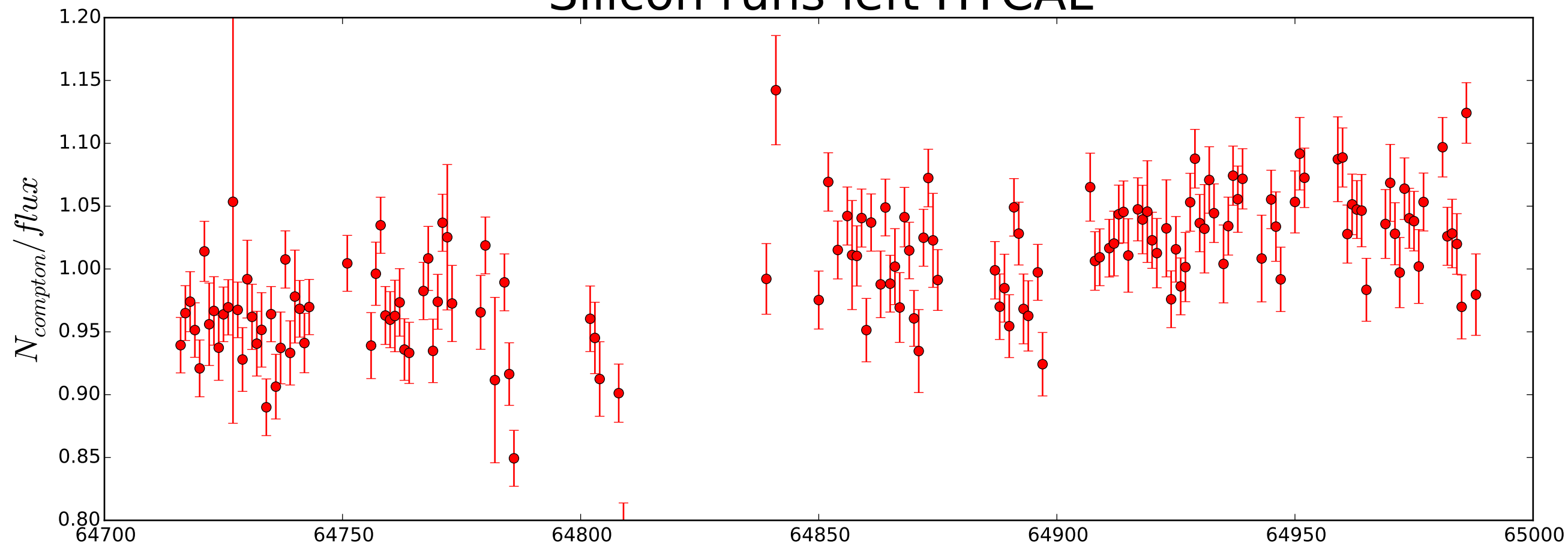


Silicon runs right HYCAL

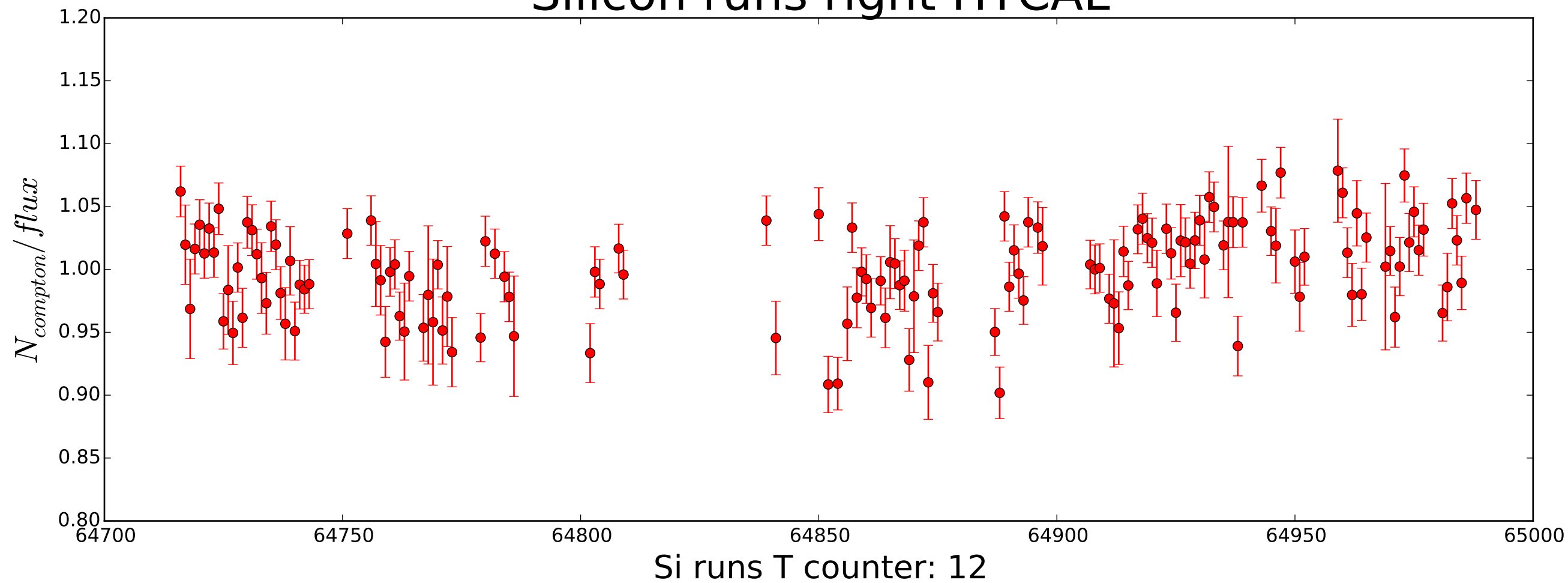


Si runs T counter: 11

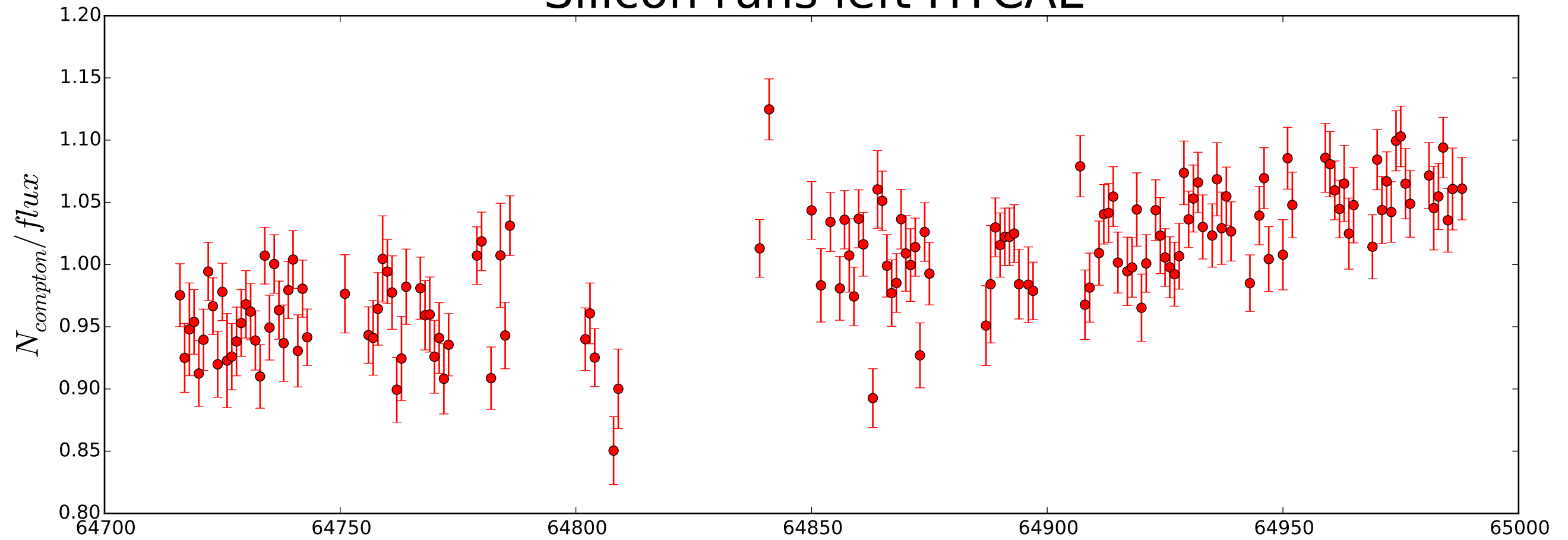
Silicon runs left HYCAL



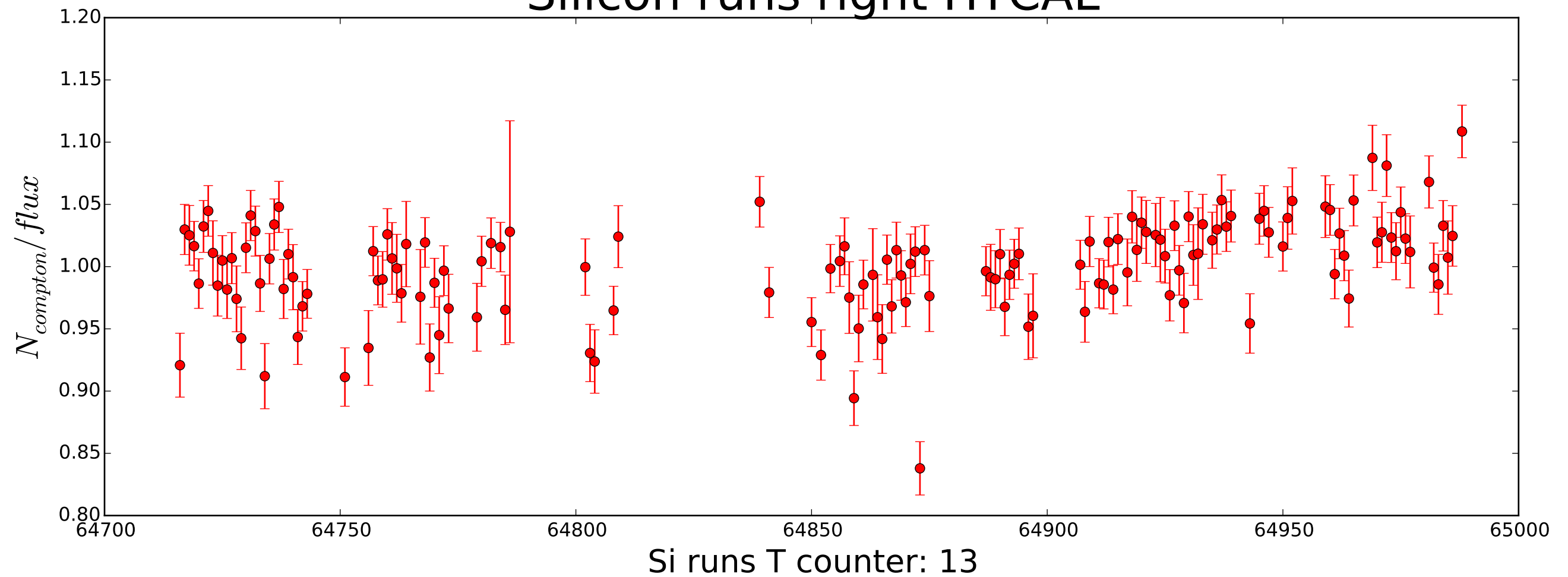
Si runs T counter: 12 Silicon runs right HYCAL



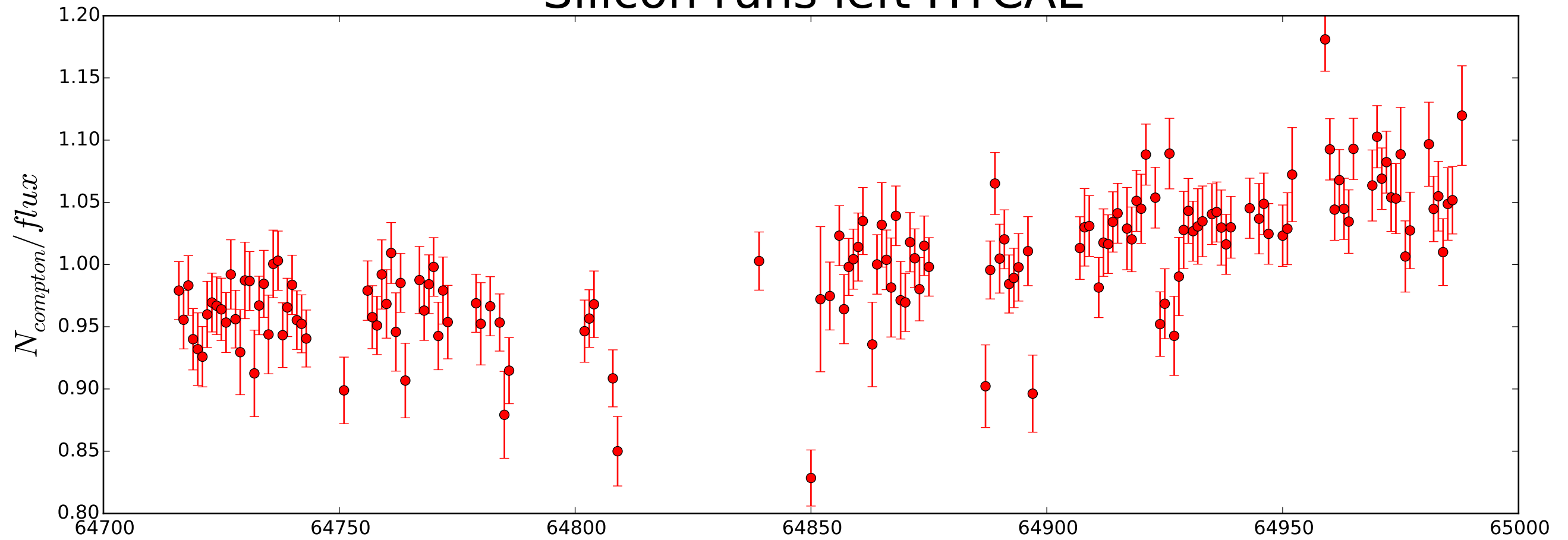
Silicon runs left HYCAL



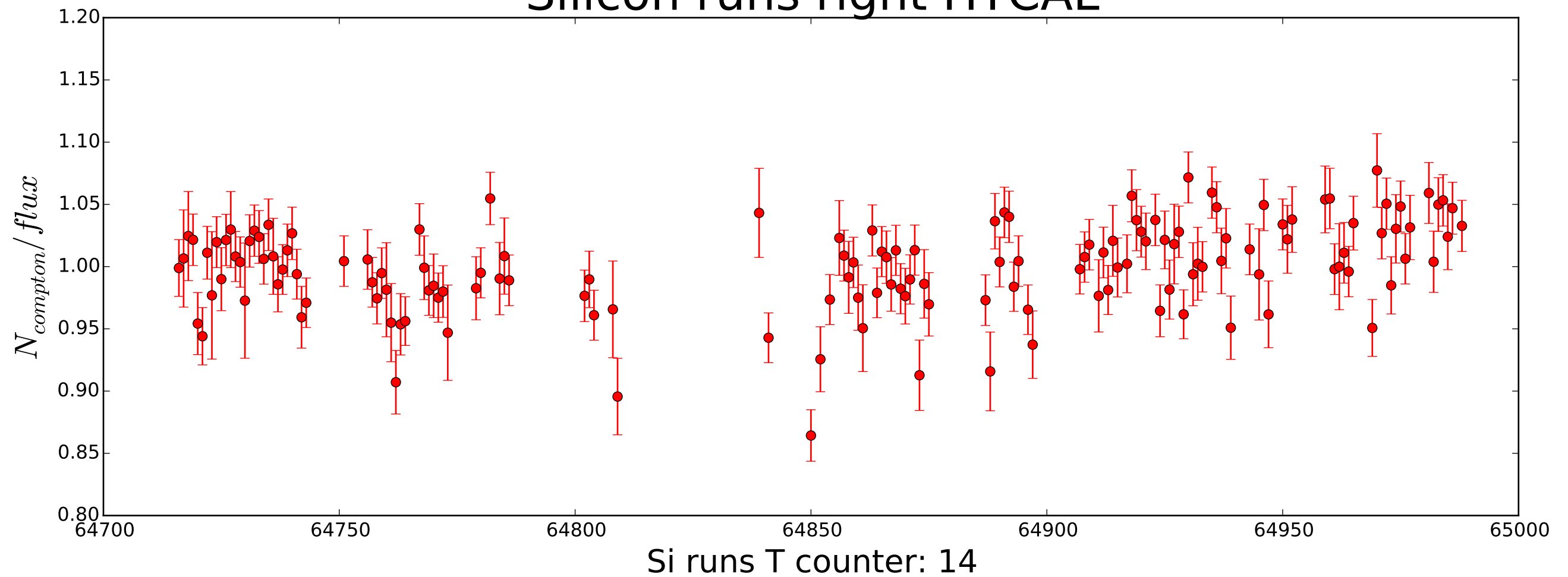
Si runs T counter: 13 Silicon runs right HYCAL



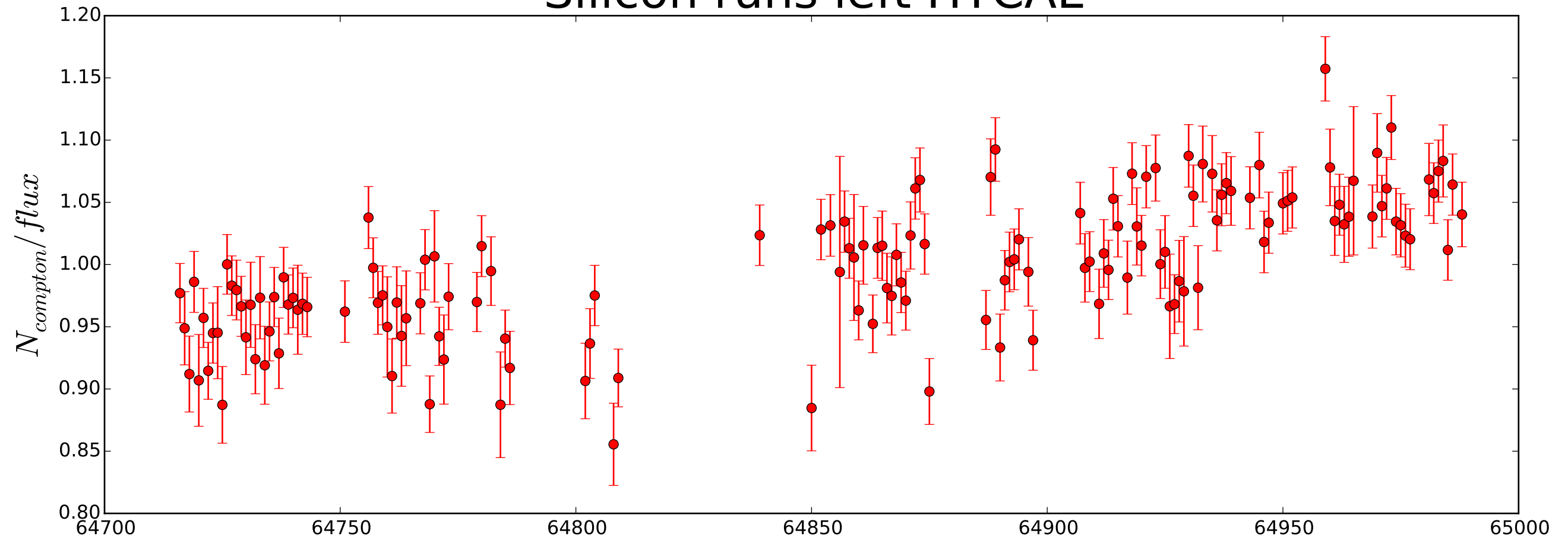
Silicon runs left HYCAL



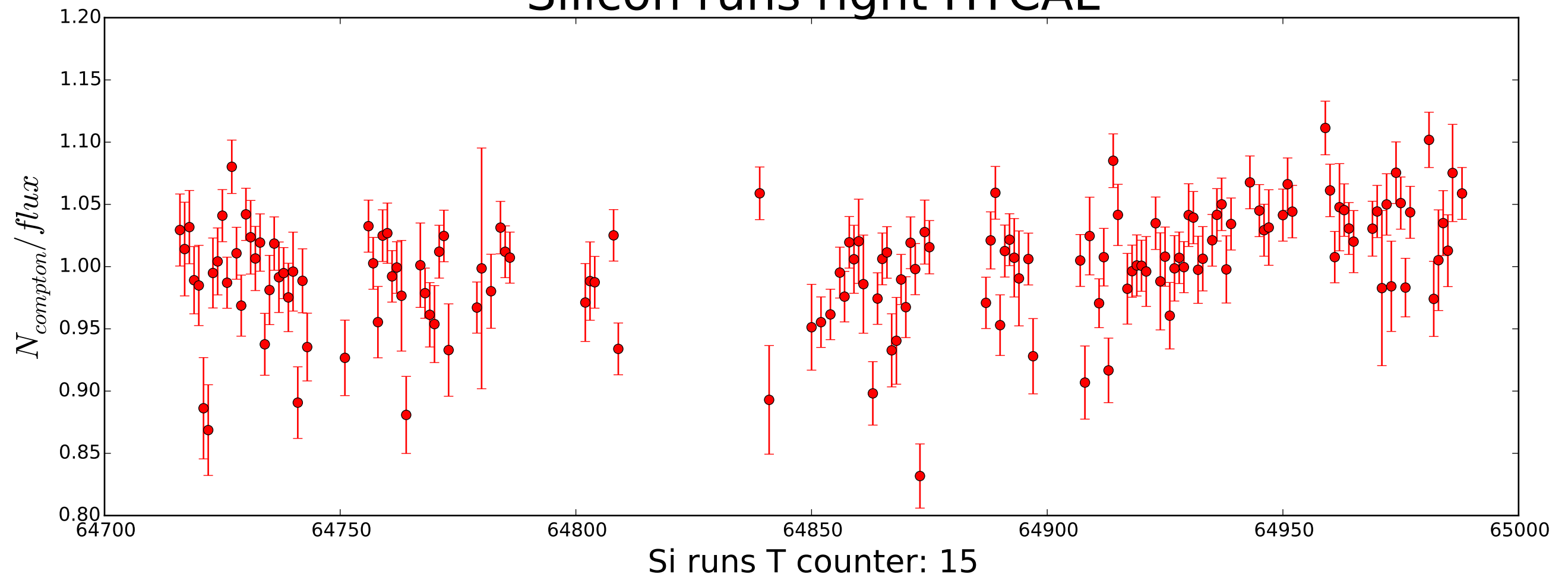
Si runs T counter: 14 Silicon runs right HYCAL



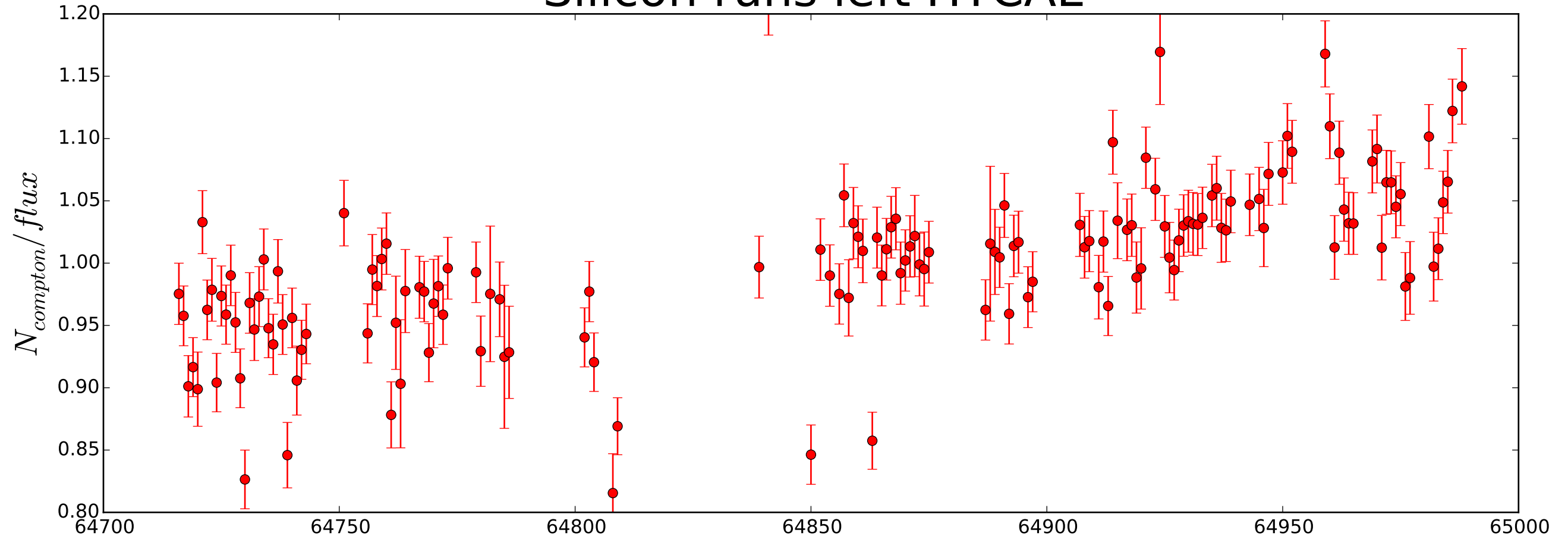
Silicon runs left HYCAL



Si runs T counter: 15 Silicon runs right HYCAL

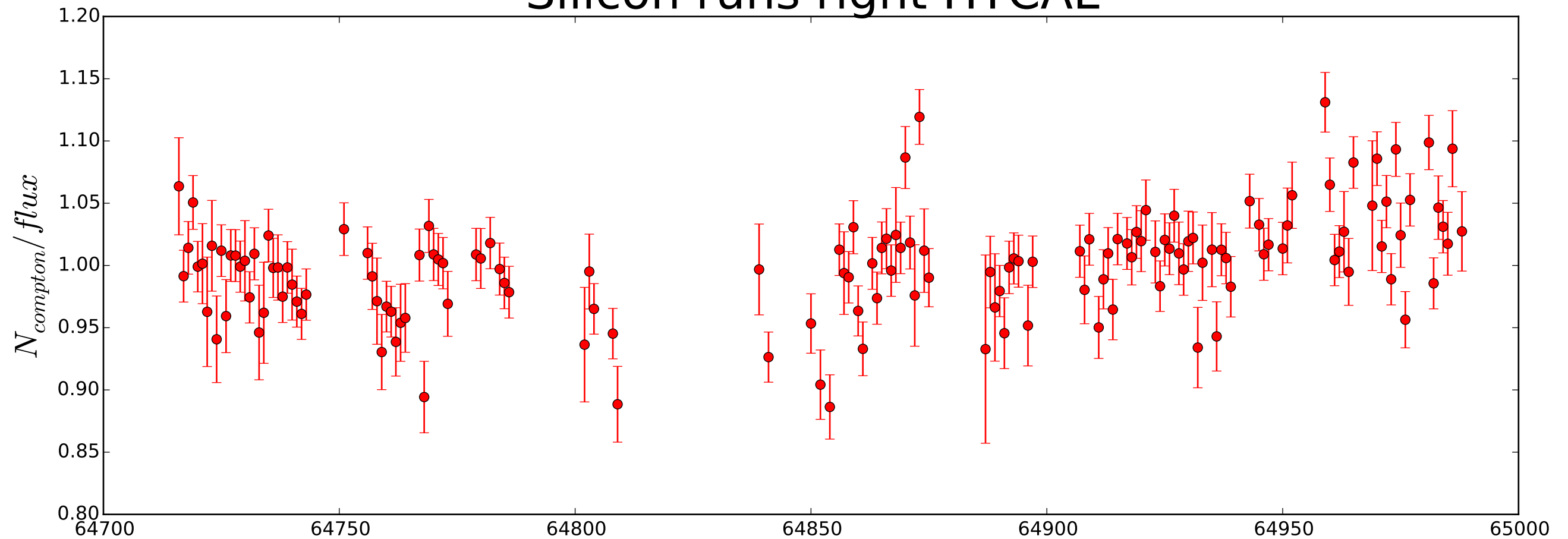


Silicon runs left HYCAL



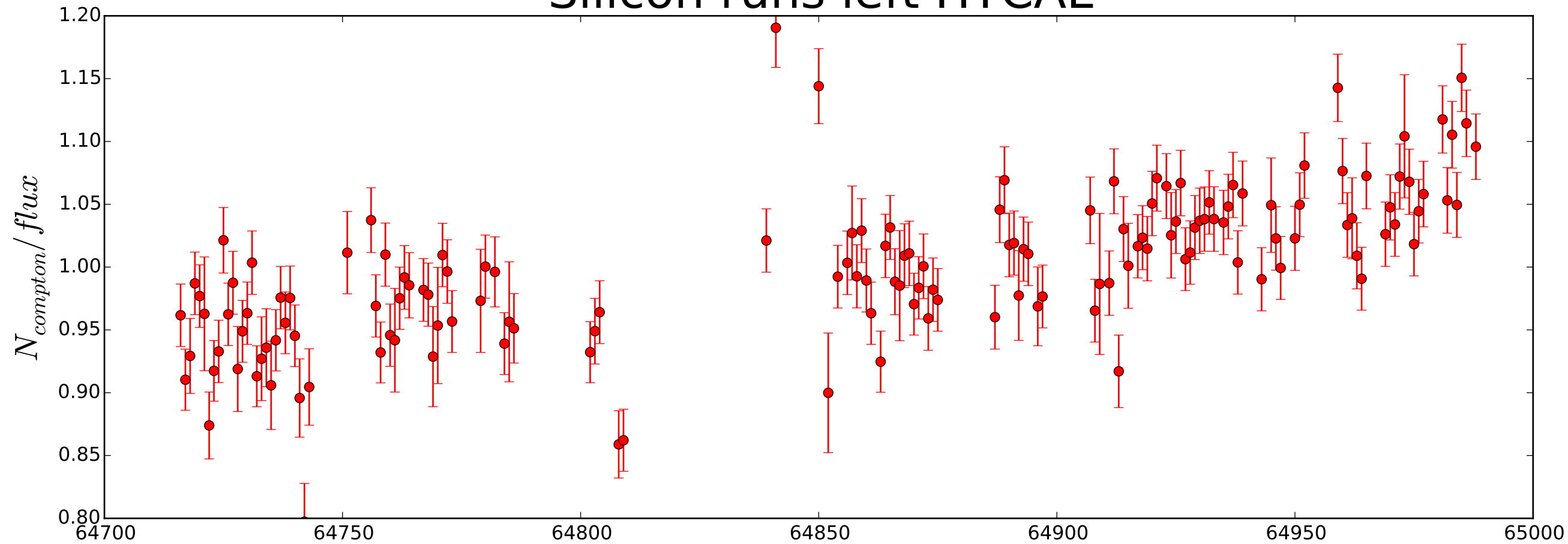
Si runs T counter: 16

Silicon runs right HYCAL

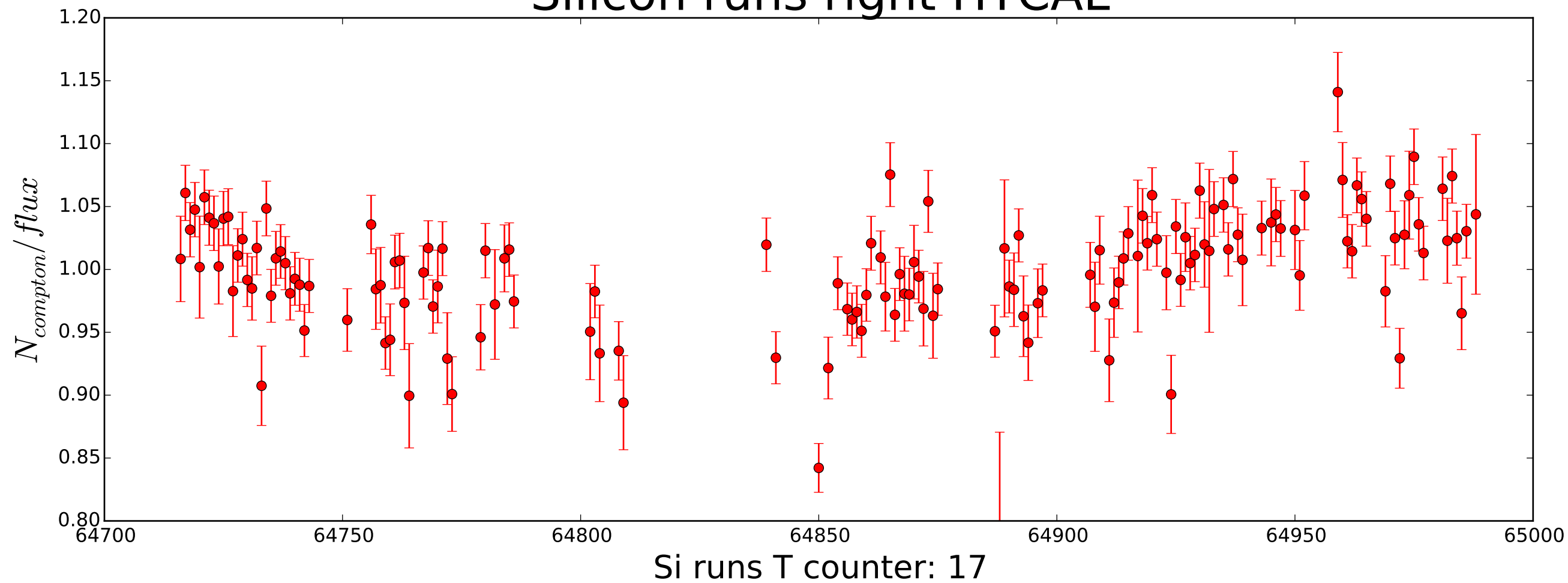


Si runs T counter: 16

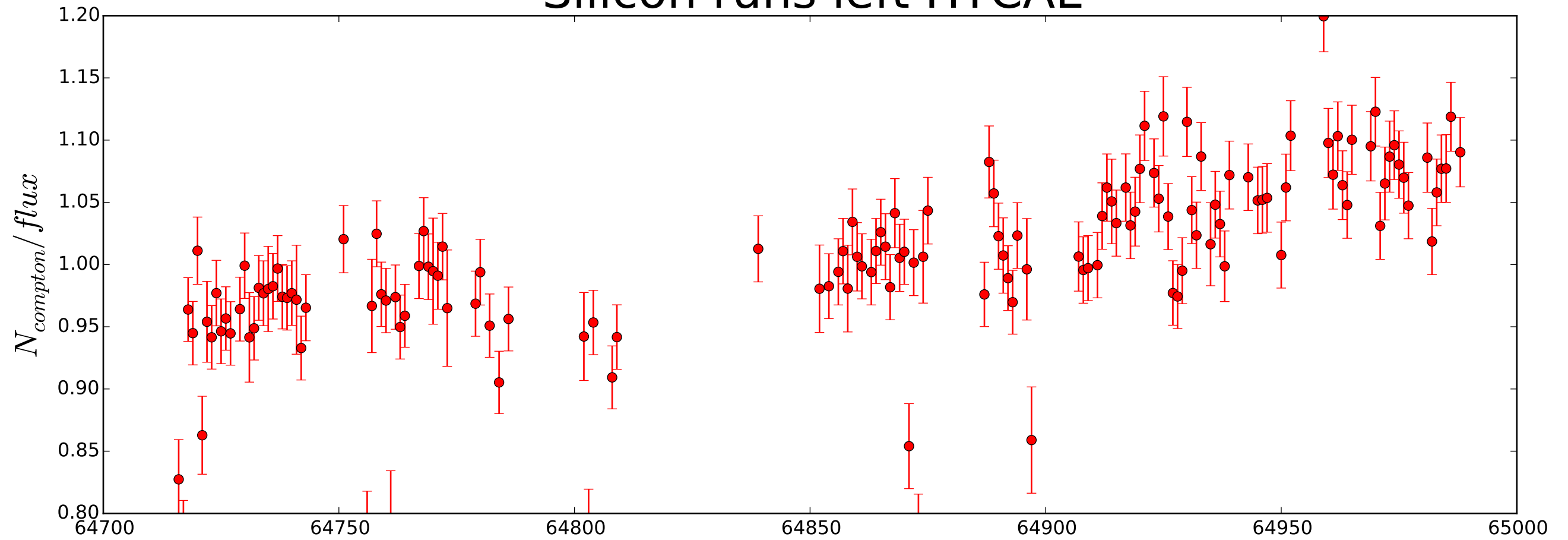
Silicon runs left HYCAL



Si runs T counter: 17 Silicon runs right HYCAL



Silicon runs left HYCAL



Silicon runs right HYCAL

