

DSG-RICH R&D Meeting Minutes

Date: March 5, 2021

Time: 11:00AM – 12:00PM

Attendees: Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Upgrade to mirror reflectivity test station

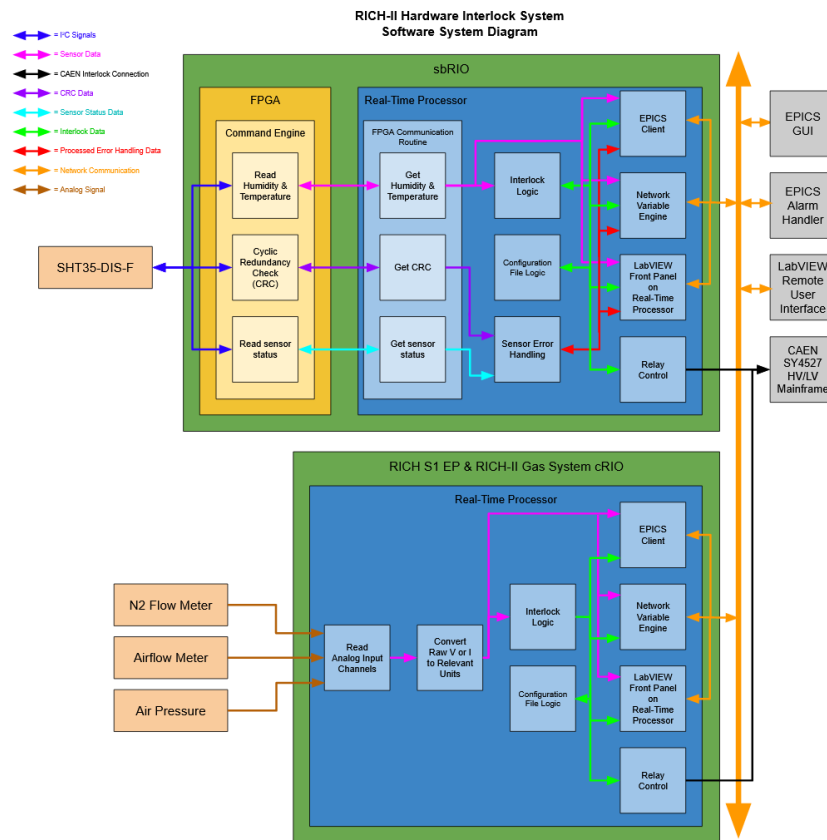
- 1.1. DSG suggested upgrade in February 2020 and discussed it in [DSG Talk 2020-03](#)
- 1.2. Upgrade not pursued because George Washington University (GWU) was building a lab for testing RICH mirrors

2. ANSYS temperature analysis of SHT35 sensor board

- 2.1. Discussed results showing that if sensor is $\sim 0.22''$ away from buffer driver, heat dissipated by buffer driver should not affect temperature reading of sensor
- 2.2. Tyler Lemon will continue adjusting simulation and will now import the PCB rendering from Altium, providing exact dimensions and layout, into the ANSYS simulation

3. RICH interlock system hardware block diagram in progress

4. RICH interlock system software block diagram



5. Reviewed latest schematic of SHT35 sensor board

