

DSG-RICH R&D Meeting

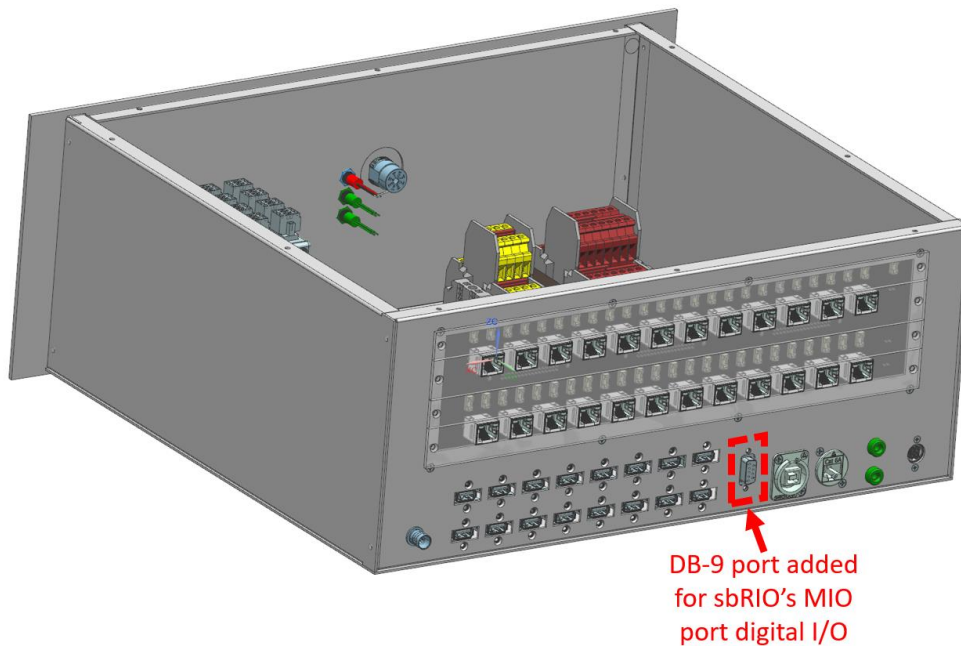
Date: December 6, 2021

Time: 11:00 AM – 12:00 PM

Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Hardware interlock chassis design

1. One nine-pin d-sub feedthrough added to chassis for additional sbRIO DIO channels
 - sbRIO has four DIO channels on MIO port that were left open for future R&D



2. Backplane PCBs

Mindy Leffel, Tyler Lemon, and Marc McMullen

1. Mindy Leffel populated one PCB and fabricated 21 of 24 RJ45-Molex cables
2. Tyler Lemon will test backplane with RMC and sbRIO