

DSG MSELV Chassis R&D Meeting Minutes

Date: November 20, 2020

Time: 11:00AM – 11:05AM

Attendees: Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Discussed Tyler Lemon's plan to start development of MSELV Chassis using PYNQ-Z1 board
 - 1.1. Review pre-compiled examples to learn to use board's Jupyter notebook interface
 - 1.2. Develop communication program for talking to one MSELV Chassis DAC and one MSELV Chassis ADC on board's FPGA
 - 1.3. Expand communication program to all DACs and ADCs in chassis
 - 1.4. Develop ARM processor program to calculate excitations
 - 1.5. Develop external communication program to allow data to be sent over network