



Detector Support Group

We choose to do these things "not because they are easy, but because they are hard".

Weekly Report, 2020-11-04

Summary

Hall A – SoLID Magnet Controls

Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, Tyler Lemon, Marc McMullen

- Completed *Liquid Level Expert* HMI screen
- Completed *Solenoid JTV-Setup* HMI screen
 - ★ Screen allows setup of proportional and integral gain parameters to control the opening and closing of seven valves
 - ★ Created Position Proportional buttons for each valve which allows user to navigate to Position Proportional screen
 - Position Proportional screen shows user defined cycle time with a pulse width proportional to the difference between the set and actual positions

11/5/2020	SoLID Solenoid Valve Setup
8:43:17 AM	

<div style="border: 1px solid black; padding: 2px; margin-bottom: 5px;"> <p style="text-align: center; font-weight: bold;">All Valve Settings</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Valve Timeout Time: 3000 s</td> <td style="width: 50%;">LVDT Max: 105.0 %</td> </tr> <tr> <td>Deadband Max: 2.00 %</td> <td>LVDT Min: -11.0 %</td> </tr> <tr> <td>Deadband Min: -2.0 %</td> <td>Max. Setting: 100.0 %</td> </tr> <tr> <td></td> <td>Min. Setting: -8.0 %</td> </tr> </table> </div>	Valve Timeout Time: 3000 s	LVDT Max: 105.0 %	Deadband Max: 2.00 %	LVDT Min: -11.0 %	Deadband Min: -2.0 %	Max. Setting: 100.0 %		Min. Setting: -8.0 %	<div style="border: 1px solid black; padding: 2px;"> <p style="text-align: center; font-weight: bold;">Hall A 4k Flow Limit Control</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;"></td> <td style="width: 50%;">EPICS 4K Flow Limit: 0.00</td> </tr> <tr> <td style="text-align: center; border: 1px solid black;">4K Flow Limit Override</td> <td>PLC 4K Flow Limit: 5.00 g/s</td> </tr> <tr> <td></td> <td>Override Flow Limit: 20.0 g/s</td> </tr> </table> </div>		EPICS 4K Flow Limit: 0.00	4K Flow Limit Override	PLC 4K Flow Limit: 5.00 g/s		Override Flow Limit: 20.0 g/s
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Click to Load to PLC	DBL Click to Save / Restore Values	<input style="width: 90%;" type="text"/>
<input checked="" type="checkbox"/> He Level Status	<input checked="" type="checkbox"/> N2 Level Status Liquid Level	Print

SoLID Solenoid Valve Setup HMI screen

Hall A – GEM Gas System

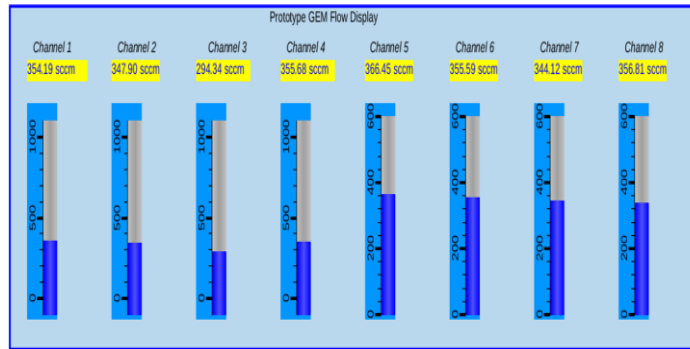
Peter Bonneau, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen

- Installed Phoebus version of CSS on Raspberry Pi to display GEM gas flow
- Generated eight channel gas flow display

Detector Support Group

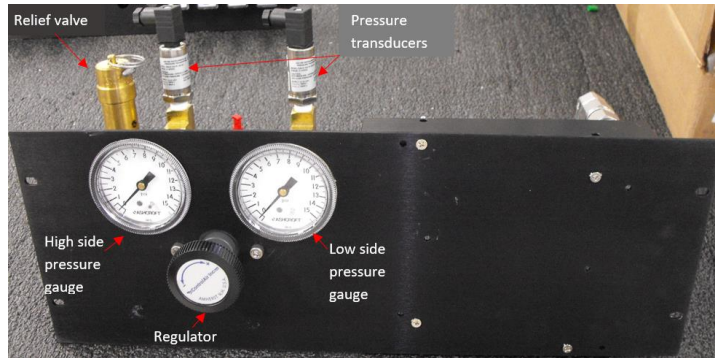
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Weekly Report, 2020-11-04

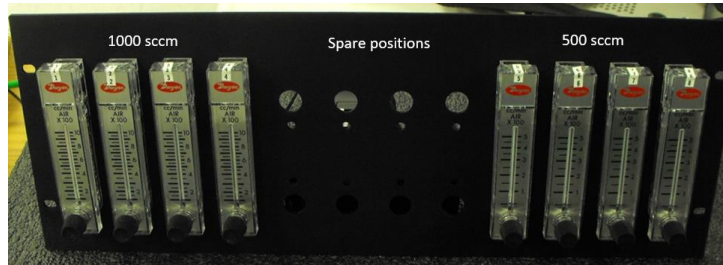


GEM Gas Flow display CSS screen

- Completed fabrication of prototype gas supply regulator and flow meter panel



Prototype of gas supply regulator for the GEM Detector Gas Distribution System



Prototype of flow meter panel with four 1000 sccm and four 500 sccm channels

Hall C – NPS

Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, George Jacobs, Mindy Leffel, Tyler Lemon

- Redesigning NPS Overview screen
 - ★ Instead of 3x3 sub-grid each LED will be clickable and will display a PMT Status pop-up screen
- Developing PMT Status pop-up screen
 - ★ One screen is created; the correct channel/PMT information is filled in via macros according to which LED is selected
 - ★ PMT Status screen displays channel/PMT faults as well as voltage and current readback



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- Conducted ramp testing of 16 of the 34 CAEN HV modules with EPICS CSS program
- Nine hundred and ninety of 1100 HV divider cables fabricated
- One hundred and eighty-six of 1080 PMT Settings screens developed
- Analyzed, with Excel, HV (with load) stability test current data
 - ★ 32 of 32 module's current data analyzed

HDice

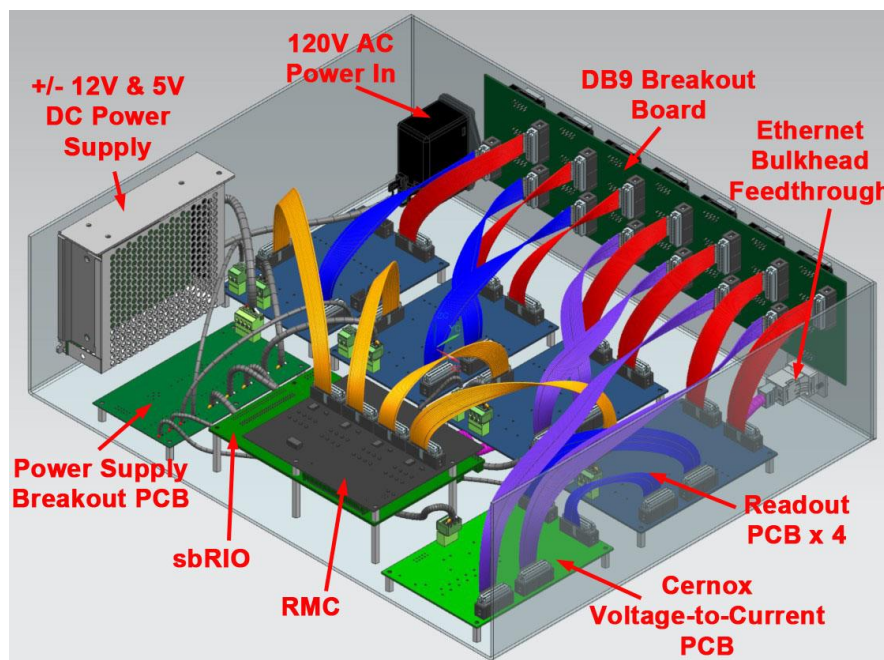
Peter Bonneau, Tyler Lemon

- Added error handling feature to fsNMR program for situations where there is no Gaussian peak in the data used for background scaling
- Added a case to the program to use absolute maximum of the background amplitude rather than the maximum of the Gaussian fit of the background amplitude

DSG R&D – MSELV Chassis

Peter Bonneau, Tyler Lemon, Marc McMullen

- Created, using NX12, three-dimensional model of MSELV Chassis
 - ★ Model includes all excitation and readout PCBs, sbRIO, sbRIO Rio Mezzanine Card, power supply, power supply breakout PCBs, and all cabling



Three-dimensional model of the DSG designed MSELV Chassis

EIC

Brian Eng

- Continued work on Tracking Detectors' (6.10.3 in WBS)
 - ★ Added TPC costs; still compiling labor costs