

Summary

Hall A – SoLID

Mary Ann Antonioli, Pablo Campero, Mindy Leffel, Marc McMullen

- Completed testing of the Motor Controller relay board



Testing the relay circuits of the SoLID Motor Controller PCB as a bus

- Completed five electrical drawings

Hall B – RICH-II

Mary Ann Antonioli, Peter Bonneau, Pablo Campero, Tyler Lemon

- Developing LabVIEW front panel for the hardware interlock system
- Developed Visio drawing of air cooling system

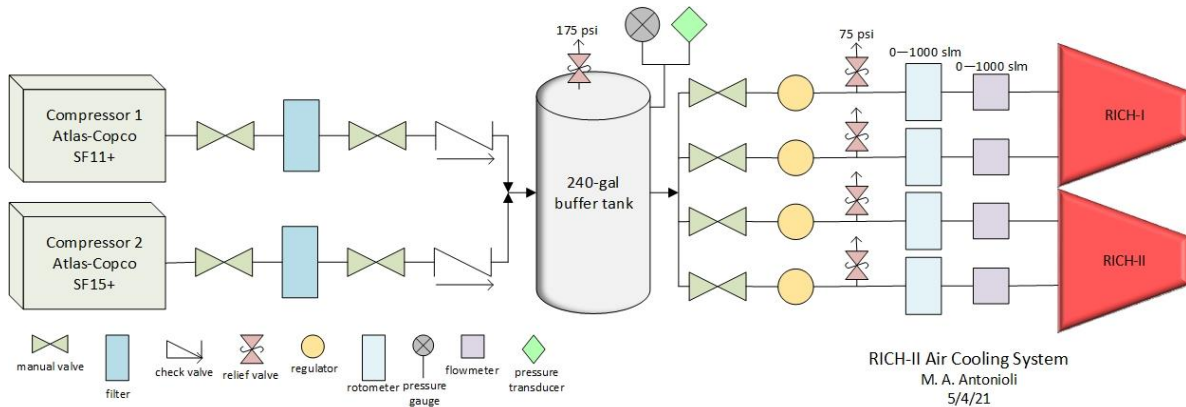


Diagram showing air cooling system flow path for two RICH sectors

Hall C – NPS

Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen

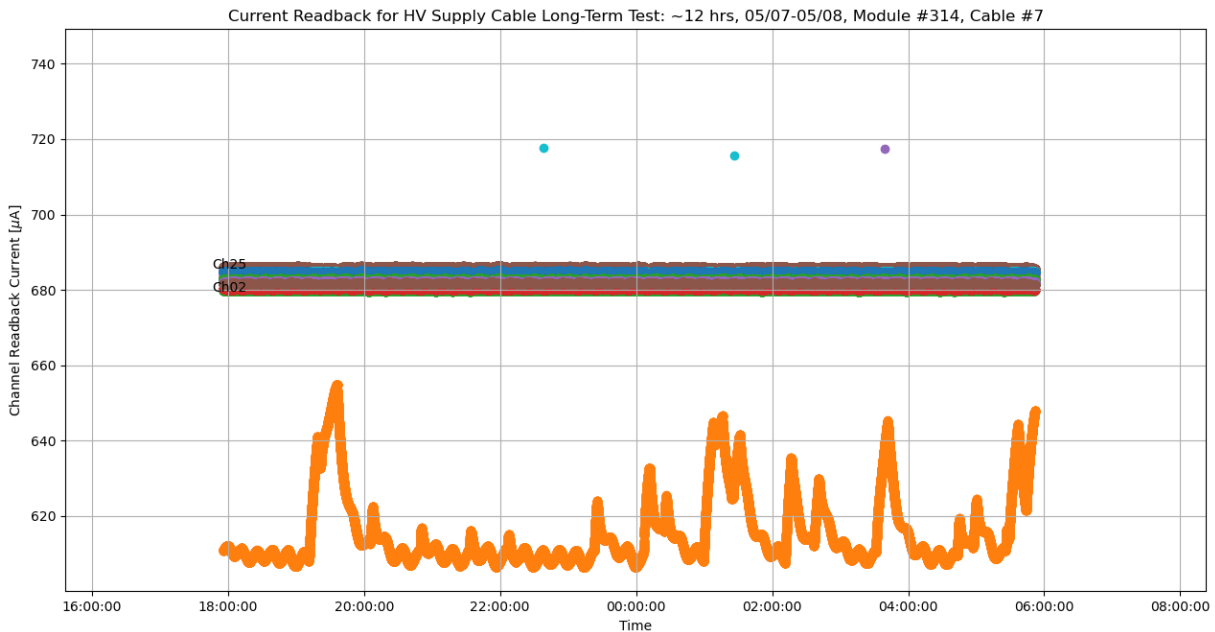
- Generated plots for long-term load test of cables #7 and #8 on module #314
 - ★ Noticed that readback current for channel #1 never reached the expected value and had large oscillations for both cables



Detector Support Group

We choose to do these things "not because they are easy, but because they are hard".

Weekly Report, 2021-05-12



Plot of long-term load test (~12 hours) of cable #7 in module #314; channel #1 at bottom in orange

- Fabricated four HV supply cables; 21 of 40 complete

EIC

Brian Eng

- Reviewing data rates to try and come up with a reasonable amount of sensor chaining for disks

Safety – POAM 10

Marc McMullen

- Implementation team members reviewed member input on the must-have components of the electronics/electrical review application