



Detector Support Group

We choose to do these things "not because they are easy, but because they are hard".

Weekly Report, 2024-05-15

Hall A – ECAL

Marc McMullen

- The aluminum right channel failed inside the relay box
 - ★ The channel positive wire heated up at the crimp of the fork terminal
 - ★ The system has been shut down and the heaters will be off during the run, ending 5/20
- Submitted quote numbers for cable to the ECAL group

Hall A – LAPPD

Pablo Campero

- Drilled 1-mm hole in two LEDs and fabricated spare LED cable
- Developed LabVIEW program to read the position of the gantry carrier

Hall B - Magnets

Brian Eng and Pablo Campero

- Solenoid PLC lost network module in local chassis
 - ★ Recovered with a power cycle: <https://logbooks.jlab.org/entry/4302975>
 - ★ Lost ability to pass tags between solenoid and torus PLCs in the process
 - Only way to recover is to manually toggle error bit, which was not possible since unable to go online with torus PLC
- Power cycled torus PLC to recover from firmware anomaly
 - ★ <https://logbooks.jlab.org/entry/4304412>
 - ★ Many PID settings needed to be restored
- Disabled TR8122A splice temperature interlock due to either bad sensor or connection at service tower
 - ★ <https://logbooks.jlab.org/entry/4304555>
- Debugged issues to access CLAS12 CSS-EPICS screens from personal computer and laptop

Hall C – NPS

Aaron Brown and Mary Ann Antonioli

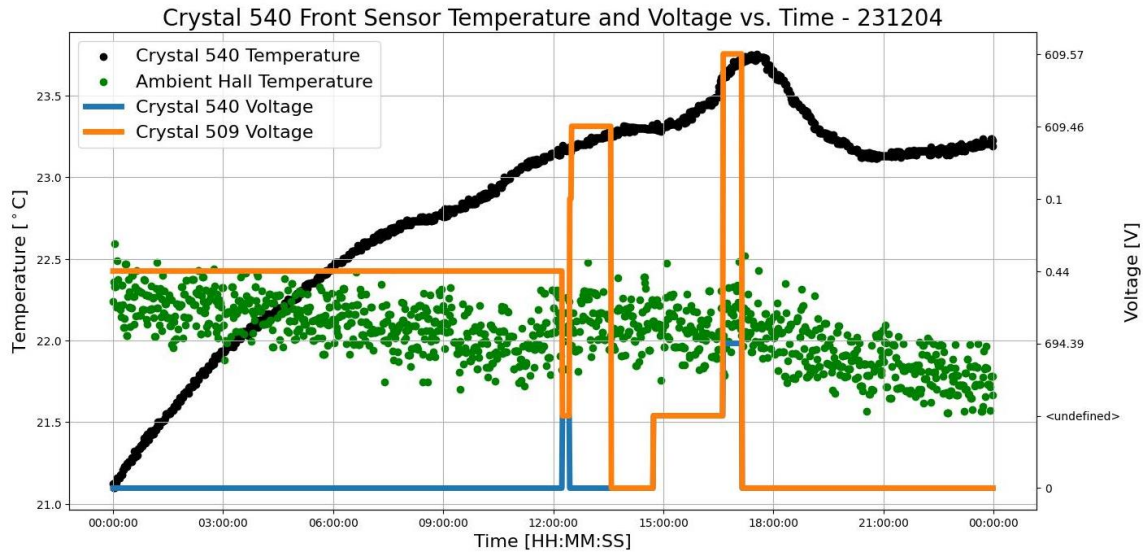
- Investigating behavior of the average temperature for 2023-12-04 and the possibility that it was affected by a combination of the ambient temperature and the high voltage status (on or off) of channels near a sensor



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Plot of crystal #540 front temperature with ambient temperature and high voltage channels for PMTs for crystals #540 and #509

- Continuing development of version 3 of the control and monitor LabVIEW program

Hall D – FCAL2

George Jacobs

- Tested 126 PMT bases; 1113 bases tested and acceptable

DSG R&D – Phoebus Test Station

Peter Bonneau, Mary Ann Antonioli, and Mindy Leffel

- Building Linux-based SSD Phoebus development computer for test system
 - ★ System configuration using EPICS base version 7 fails to connect to test softIOC; debugging
- Began a Phoebus screen for the test station; creating PV names
- Populated, tested, and labeled four HTSB V2 cables; completed all 10
- Stripped ferrule end (20”), tested, and labeled two HTSB V1 cables
- Modified 37-pin, D-sub female-to-female cable by replacing one connector with male; completed one of three